

FIG. 2

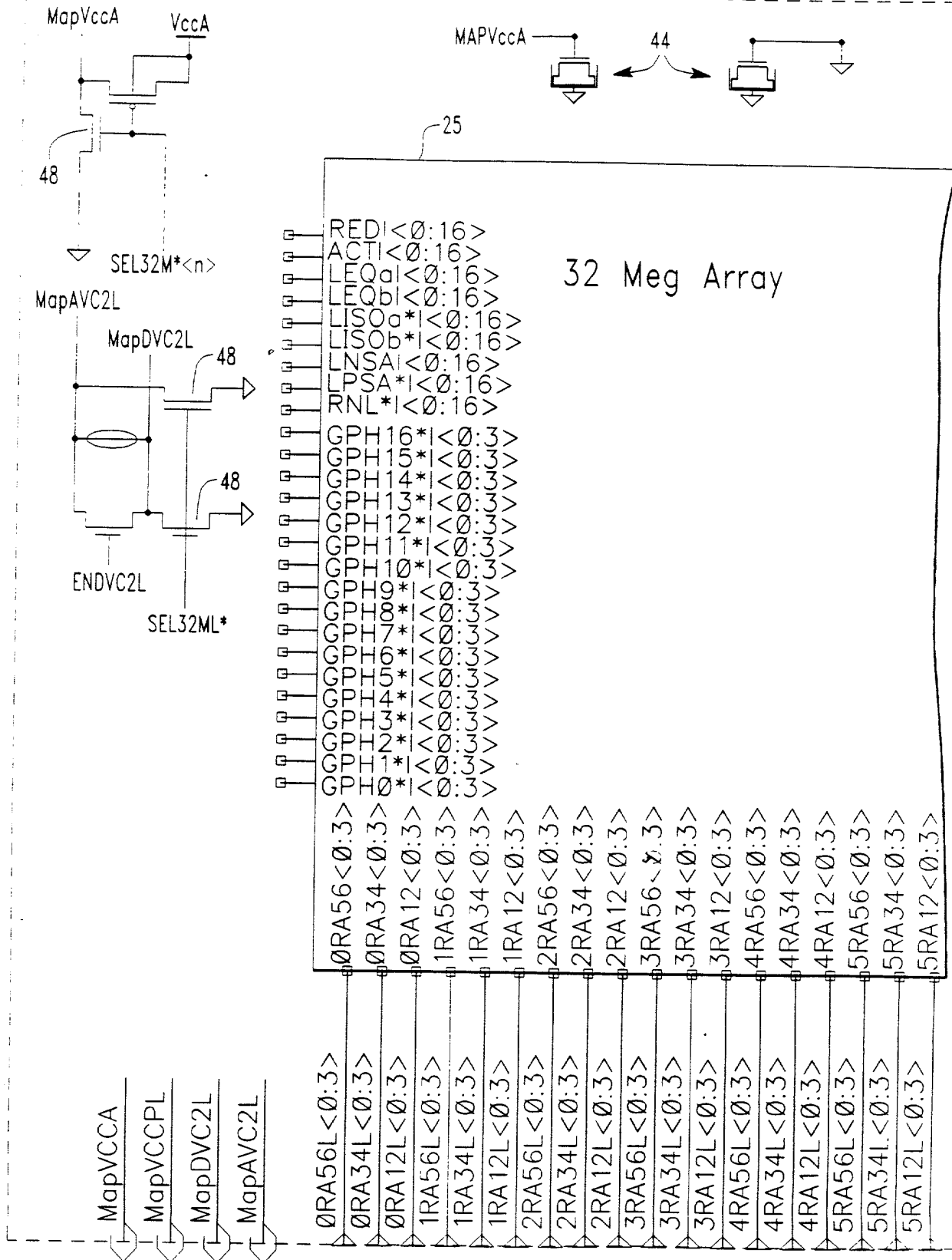
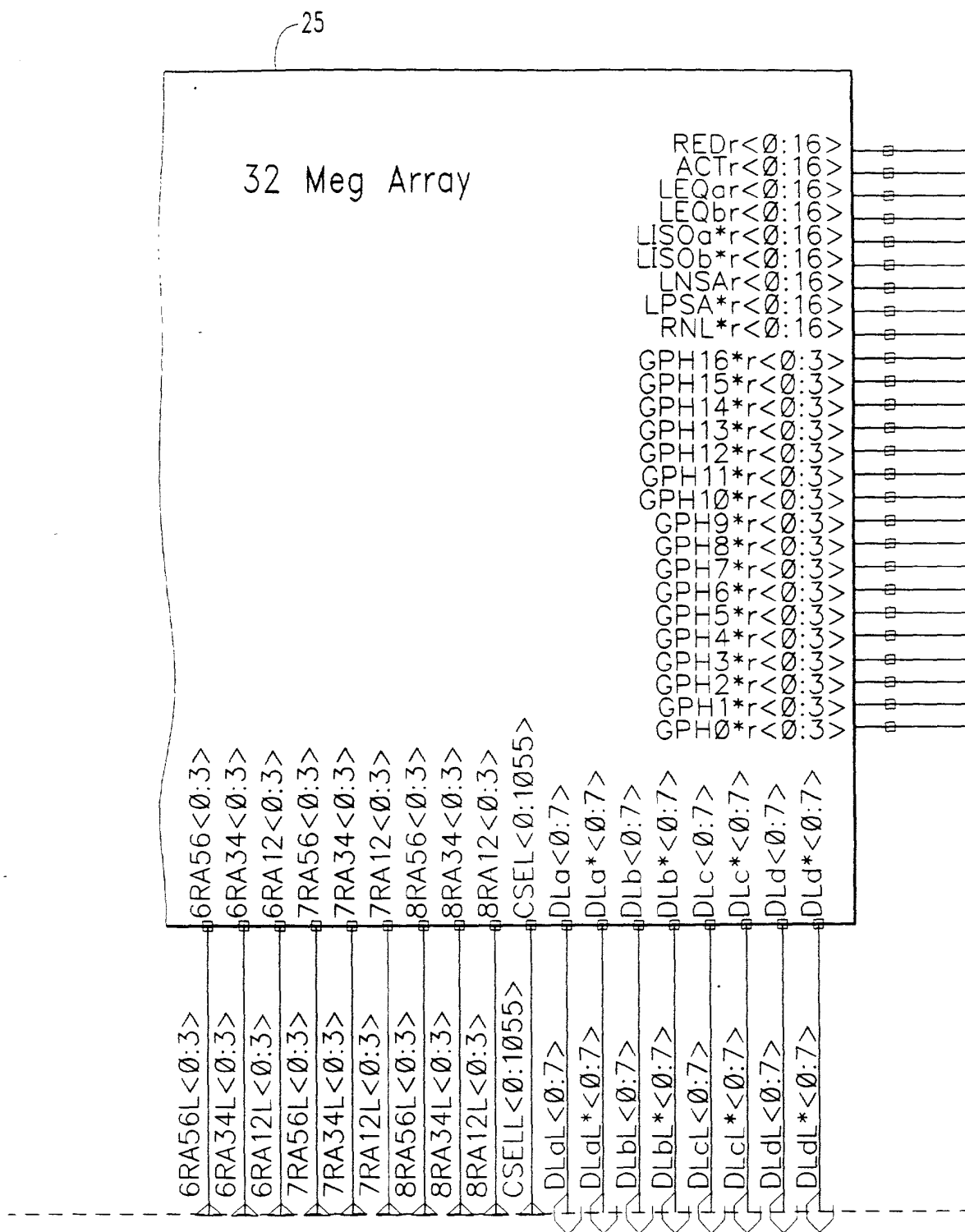
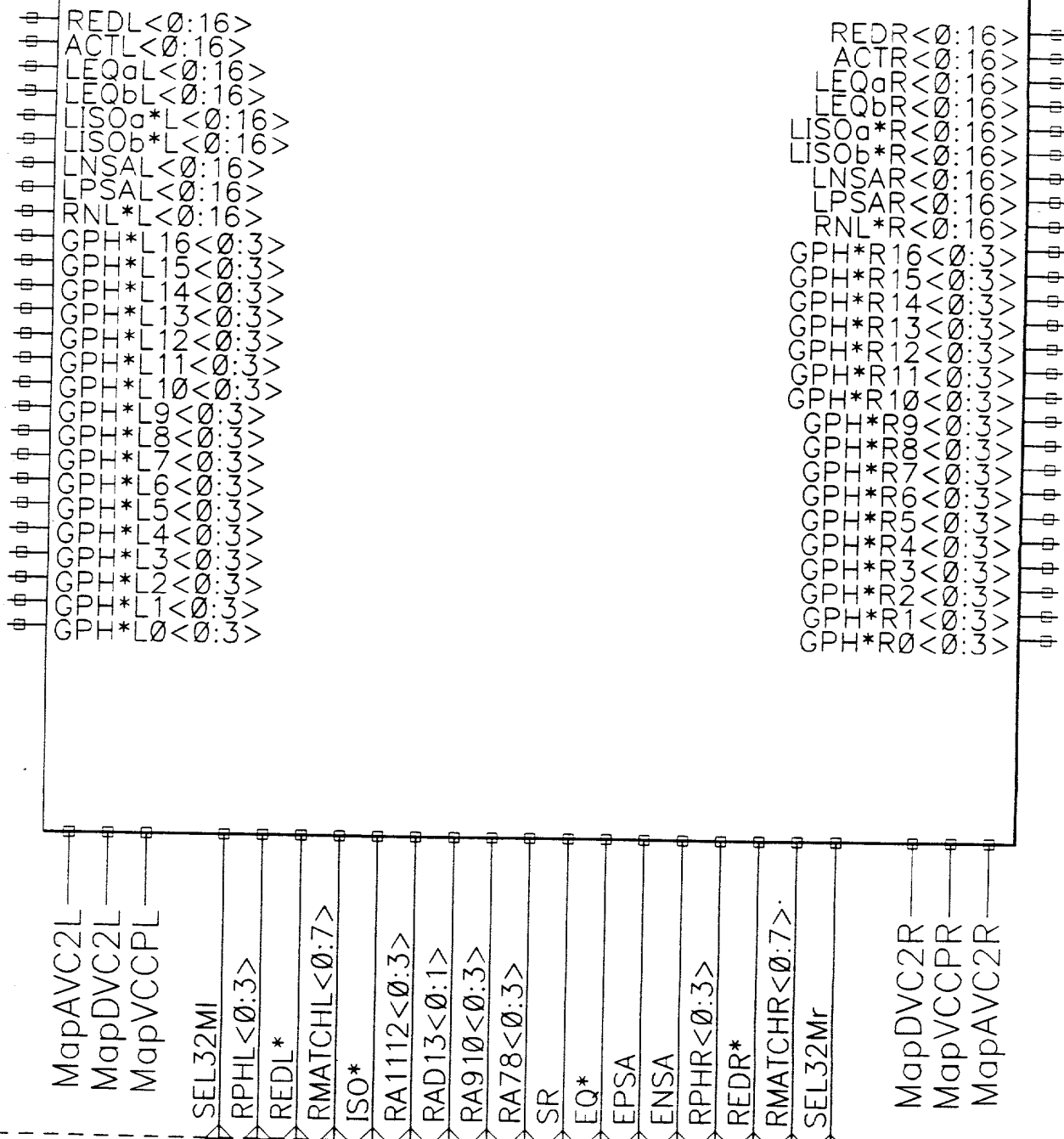


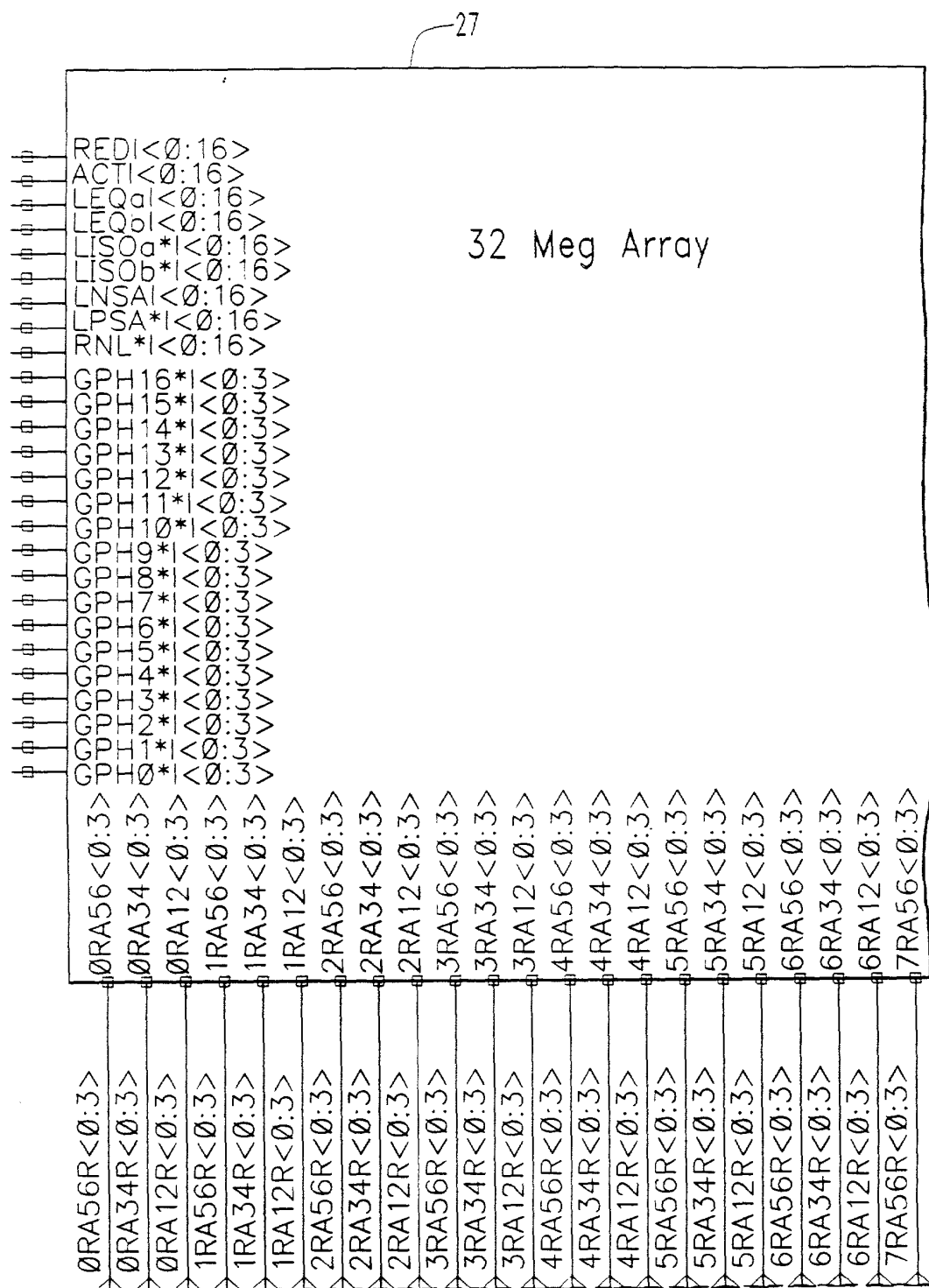
FIG. 3B

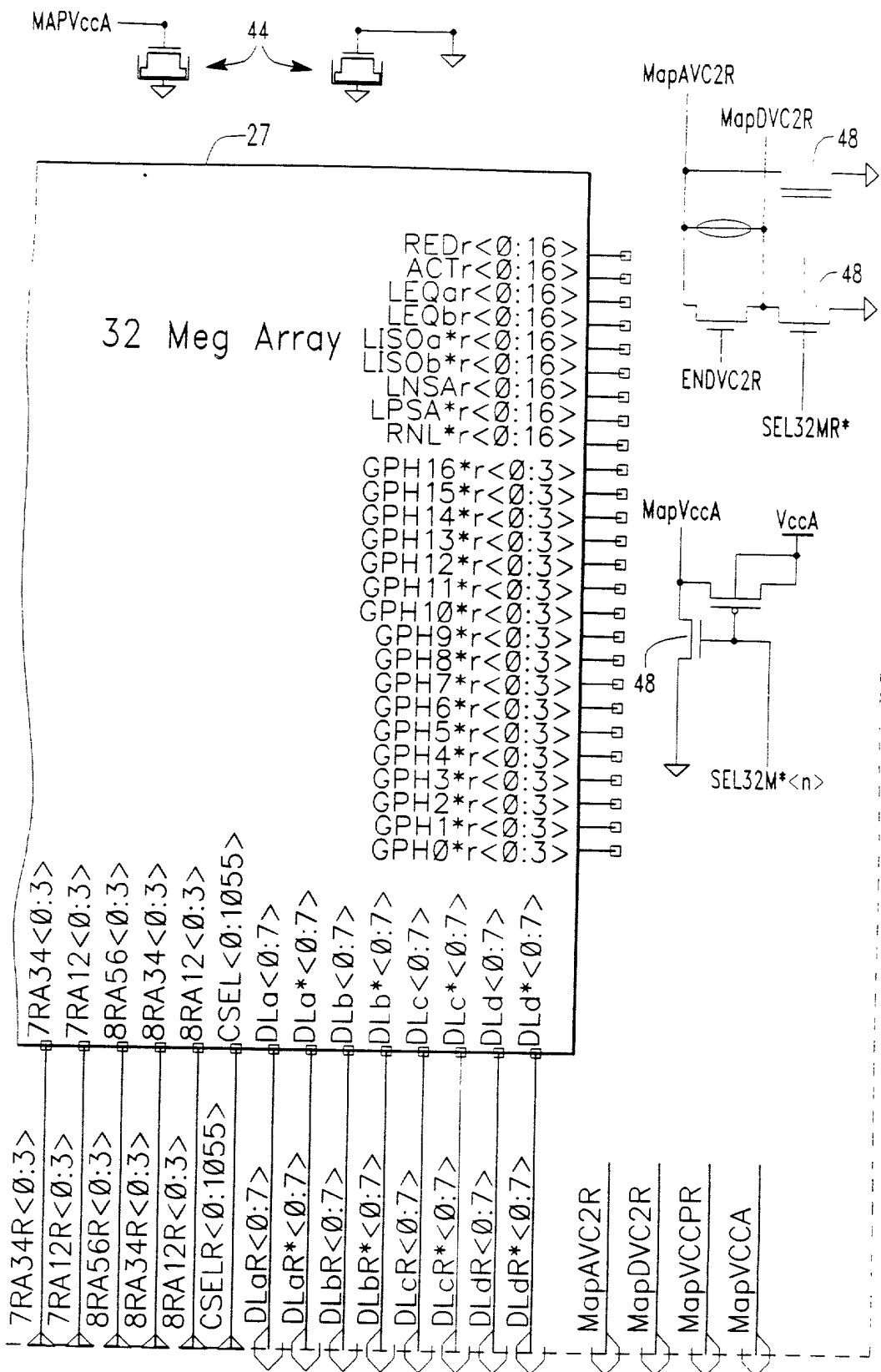
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FIG. 3B













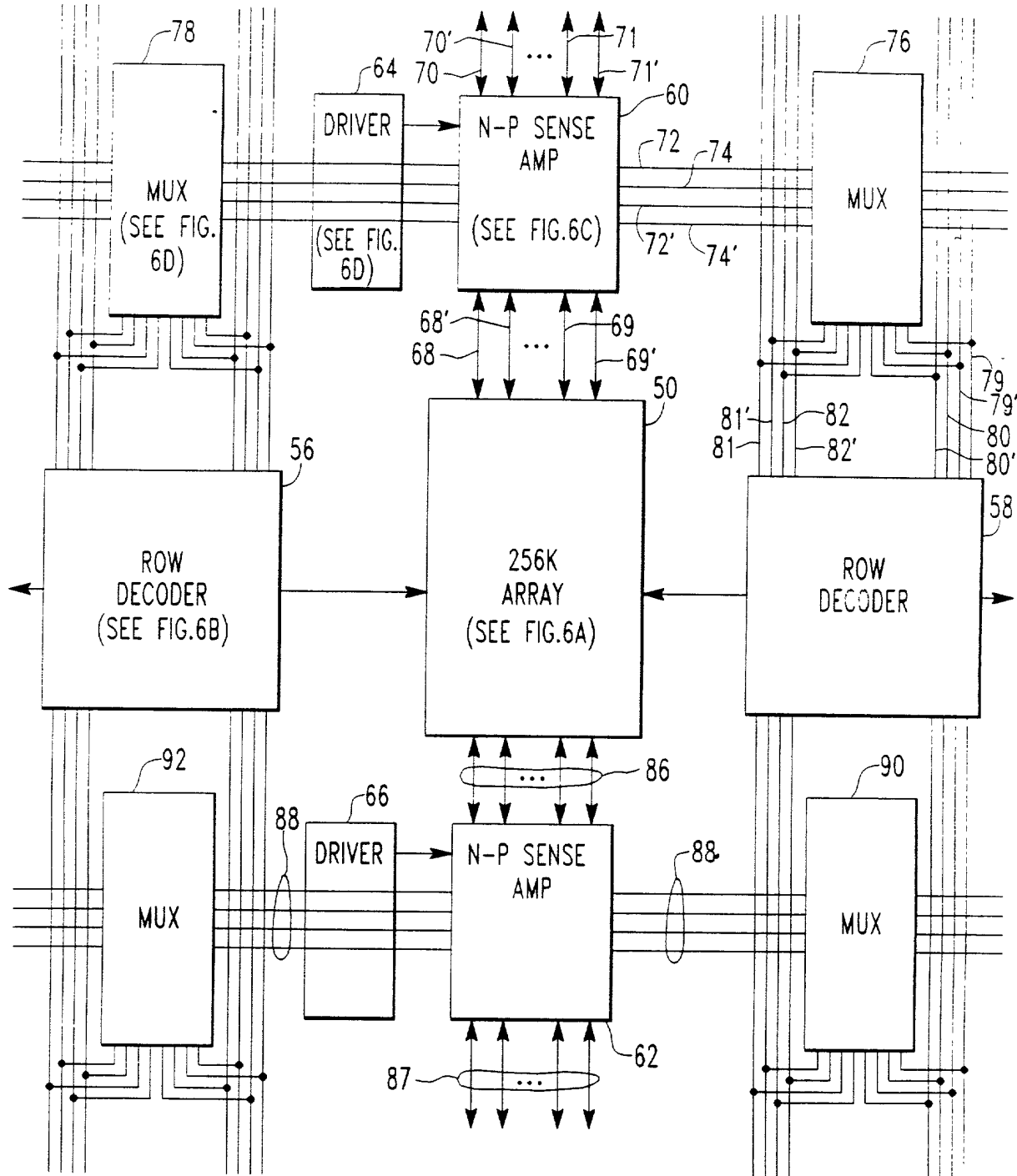


FIG. 5

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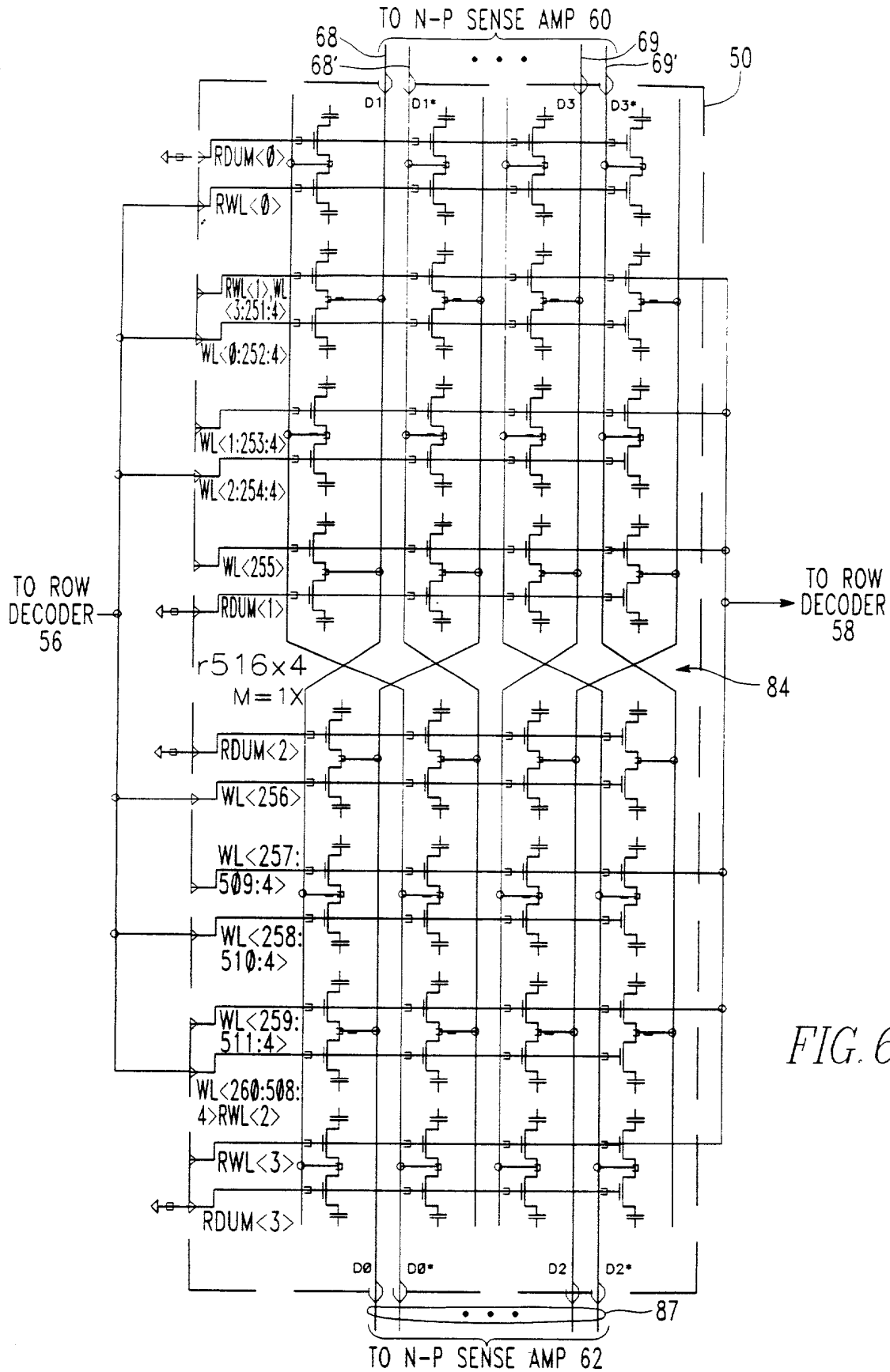


FIG. 6A

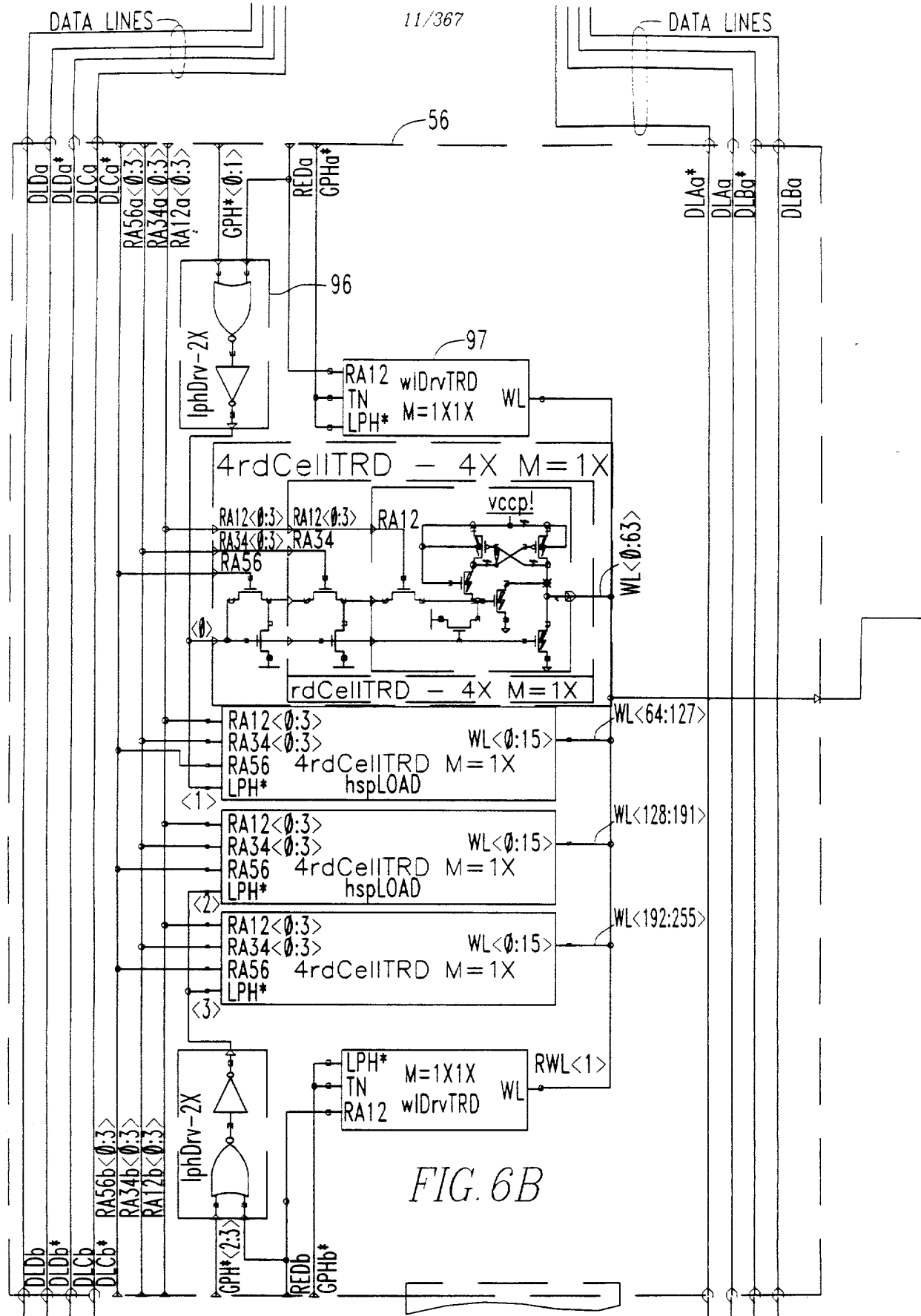
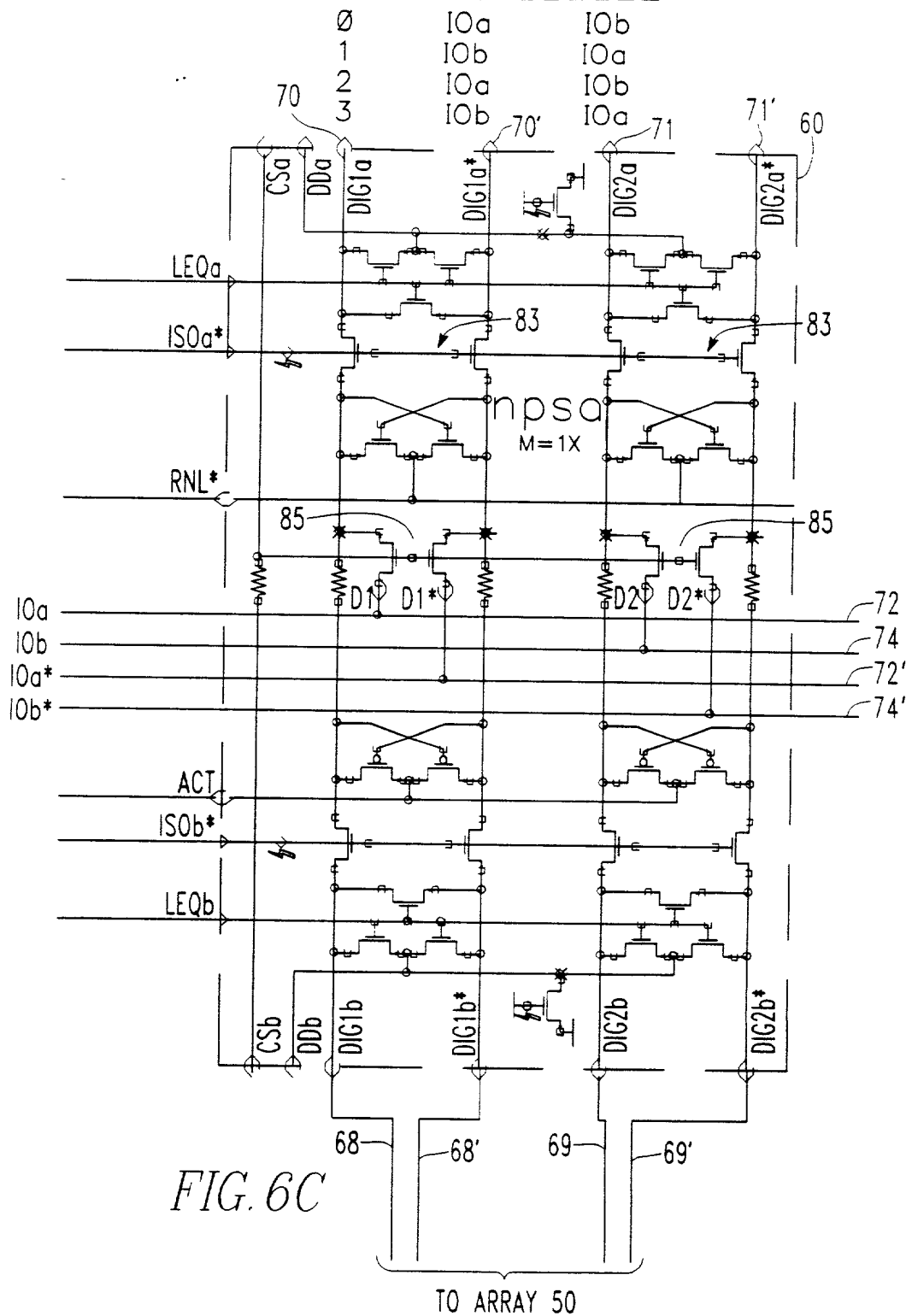


FIG. 6B

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Connections of odd/even  
columns to IOa and IOb  
alternates with odd/even  
column select lines:  
CA01\* D1(even) D2(odd)



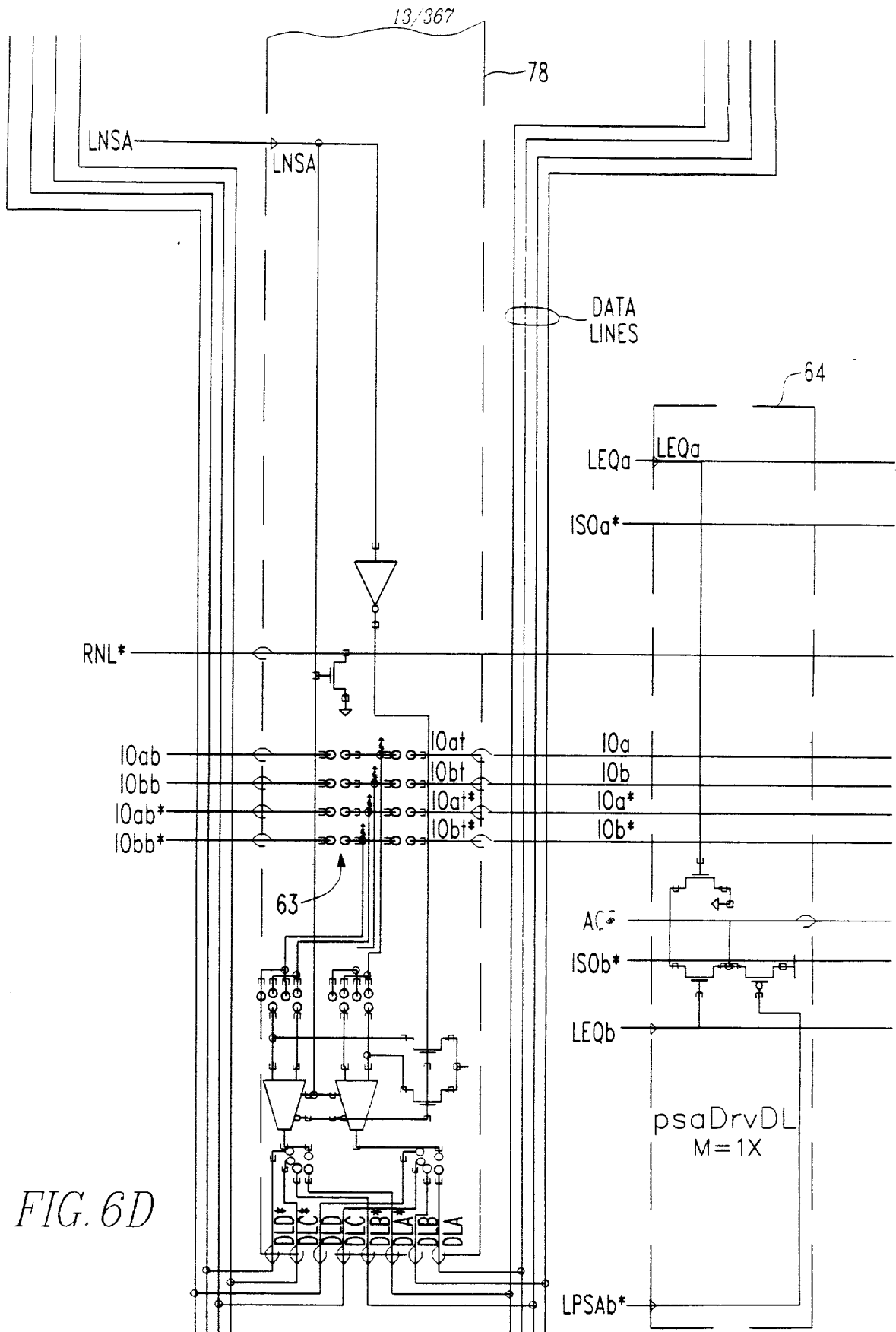
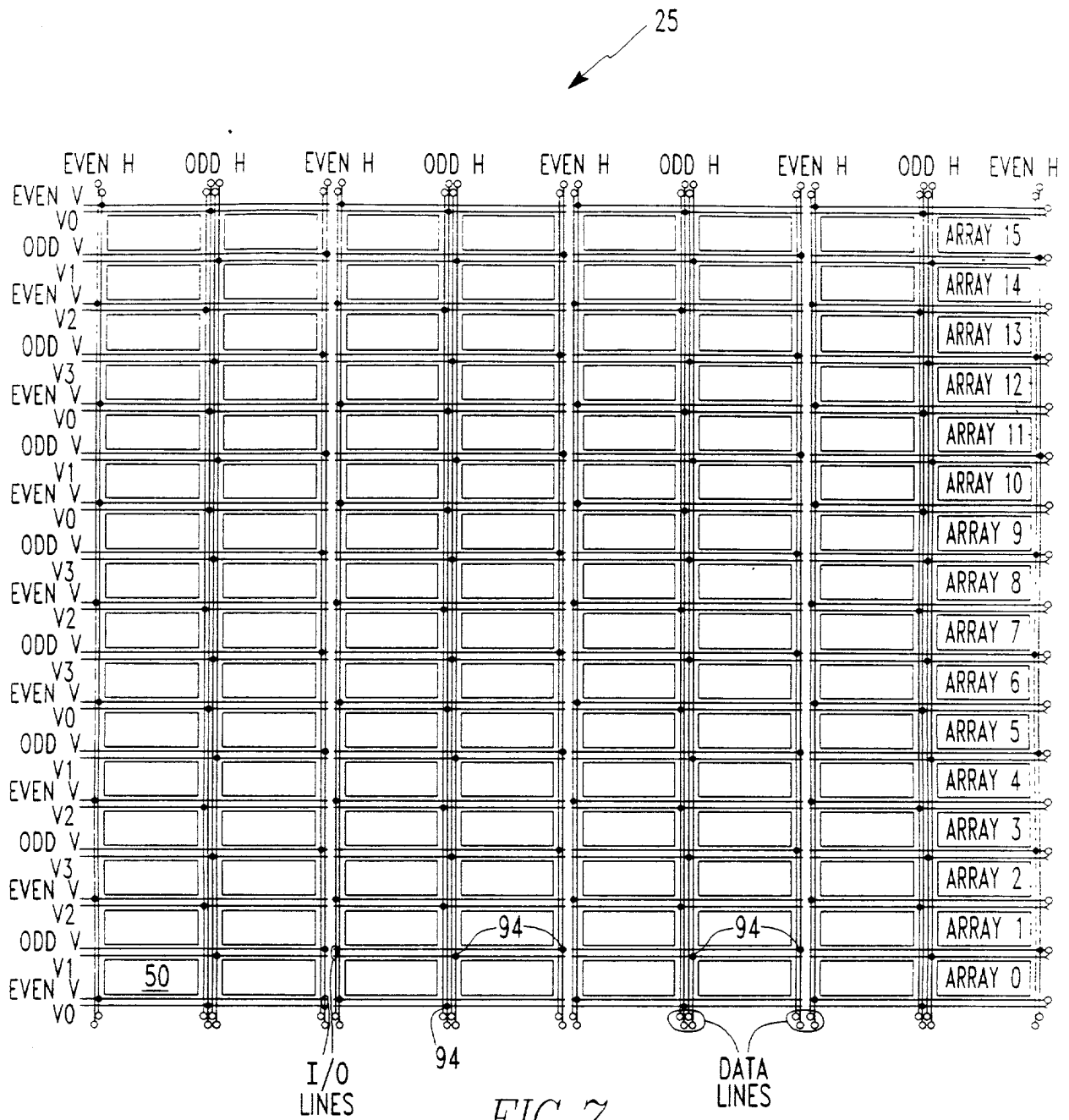


FIG. 6D



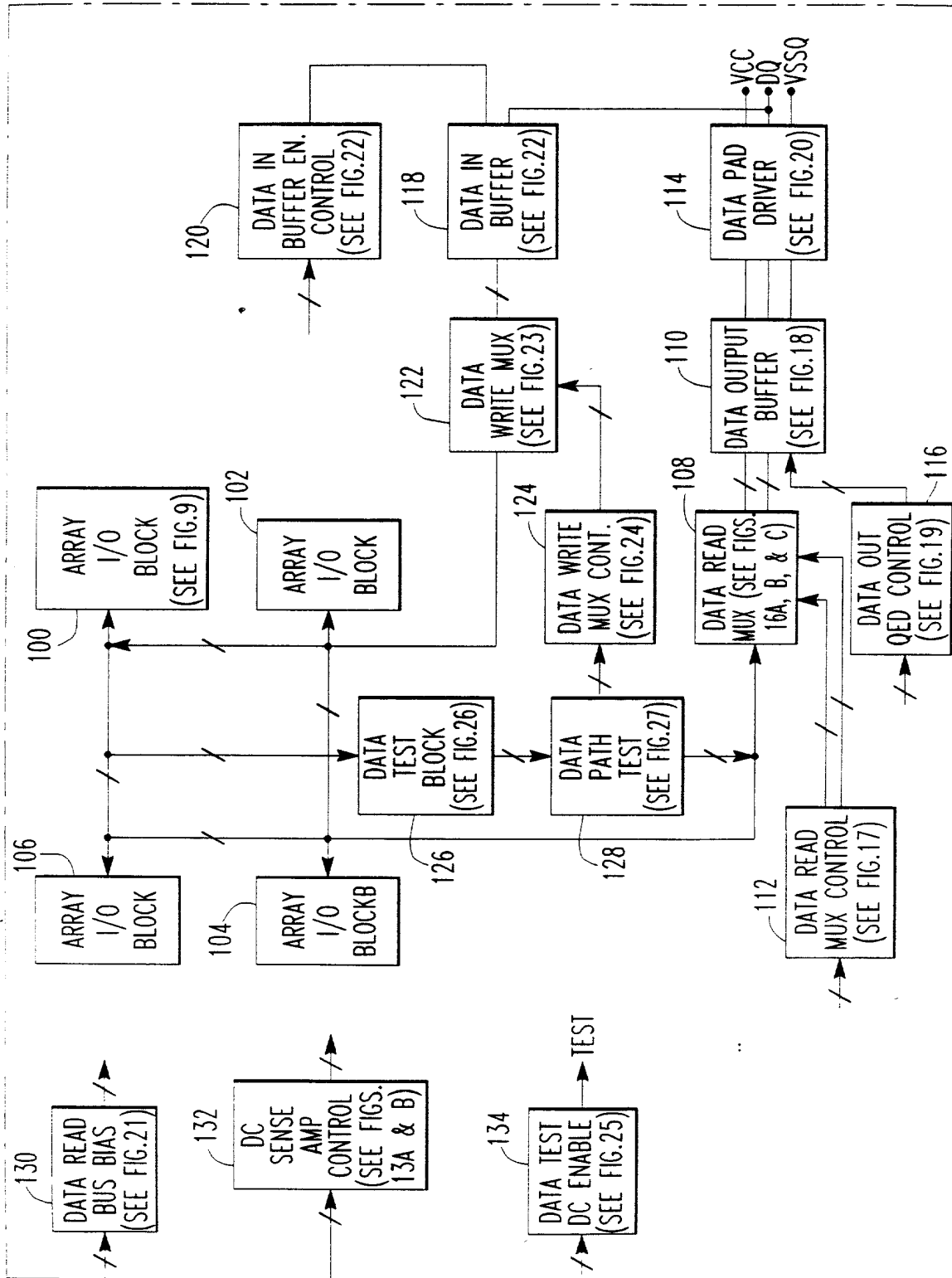


FIG. 8

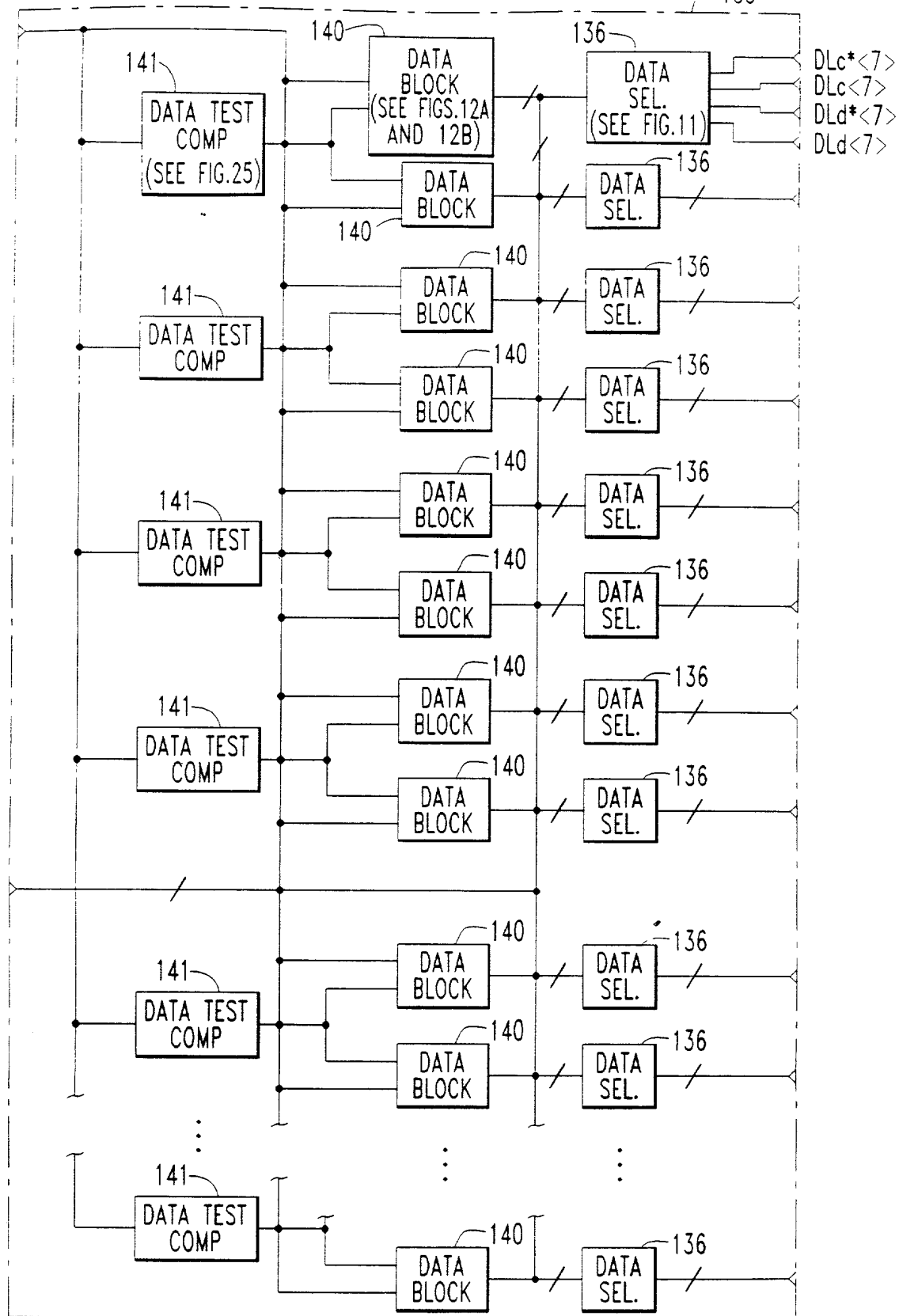
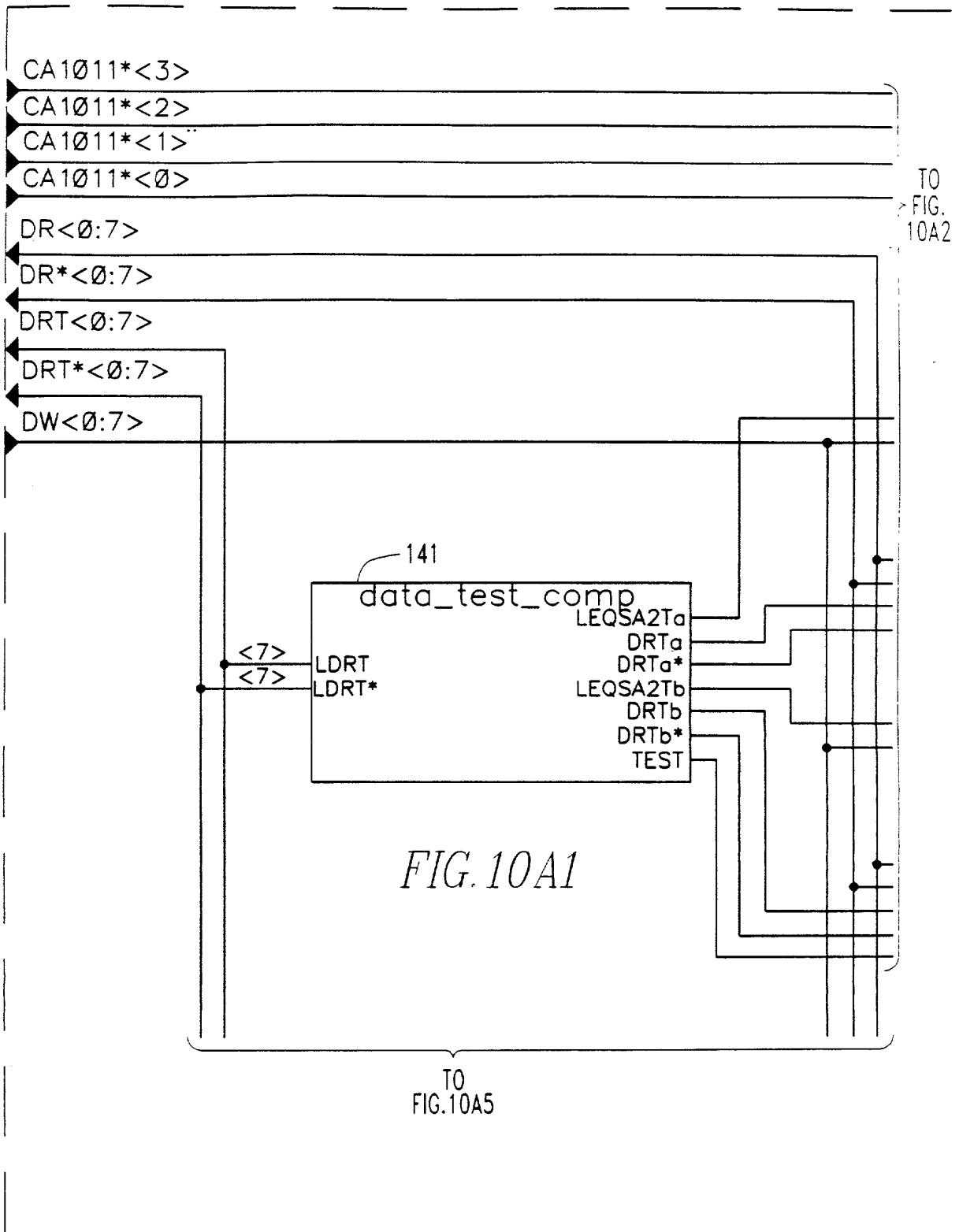


FIG. 9





102280 5044600

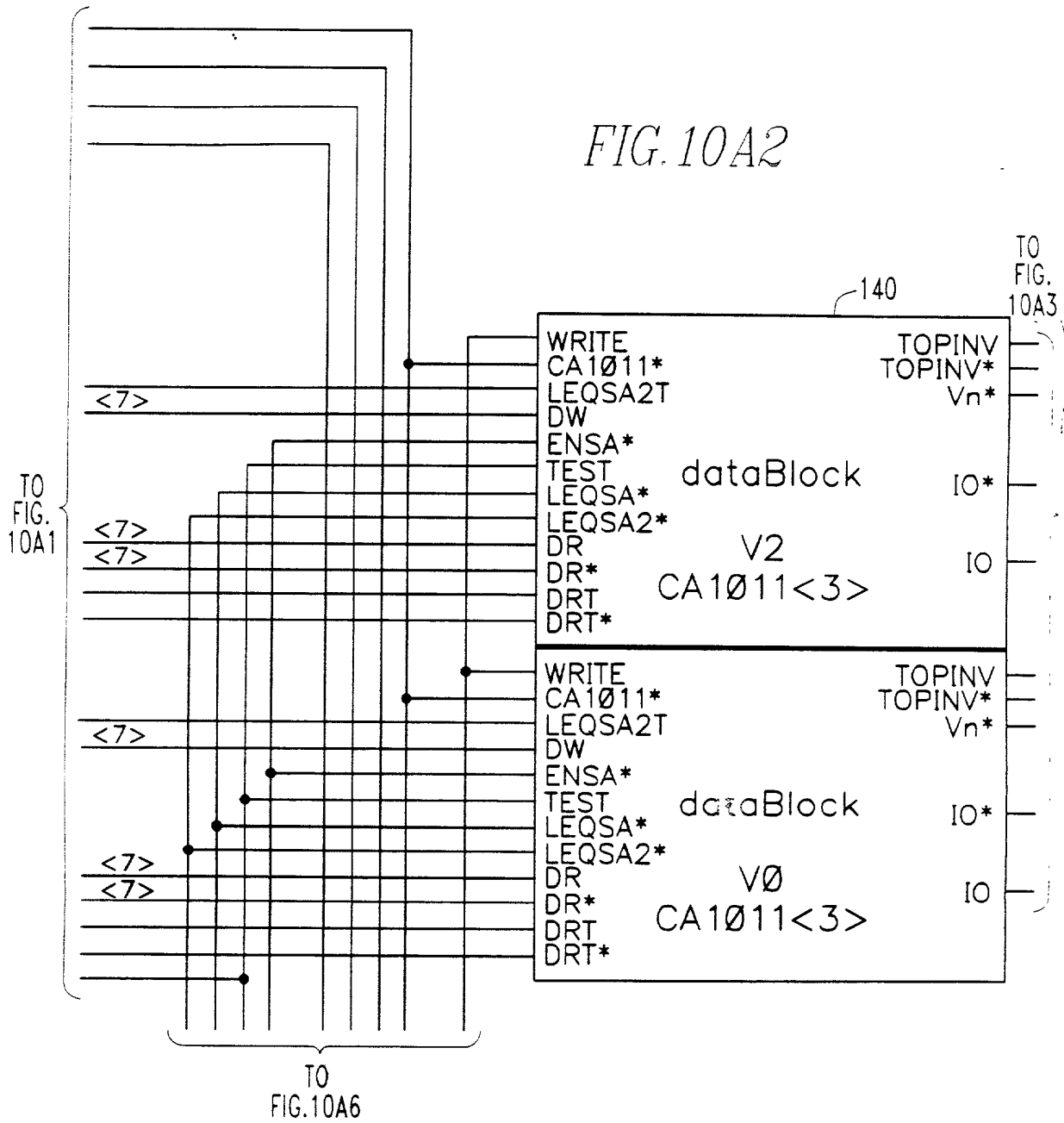
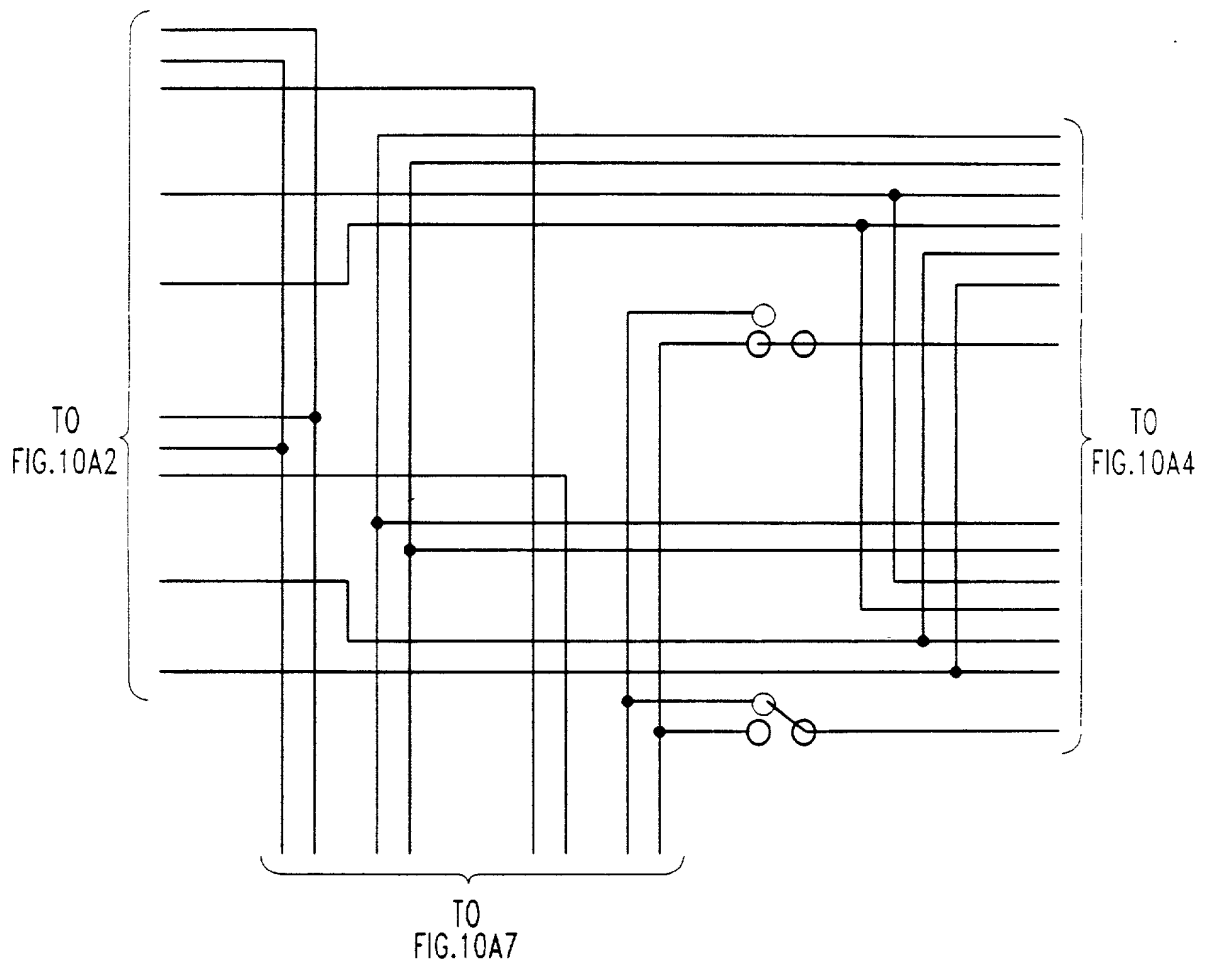


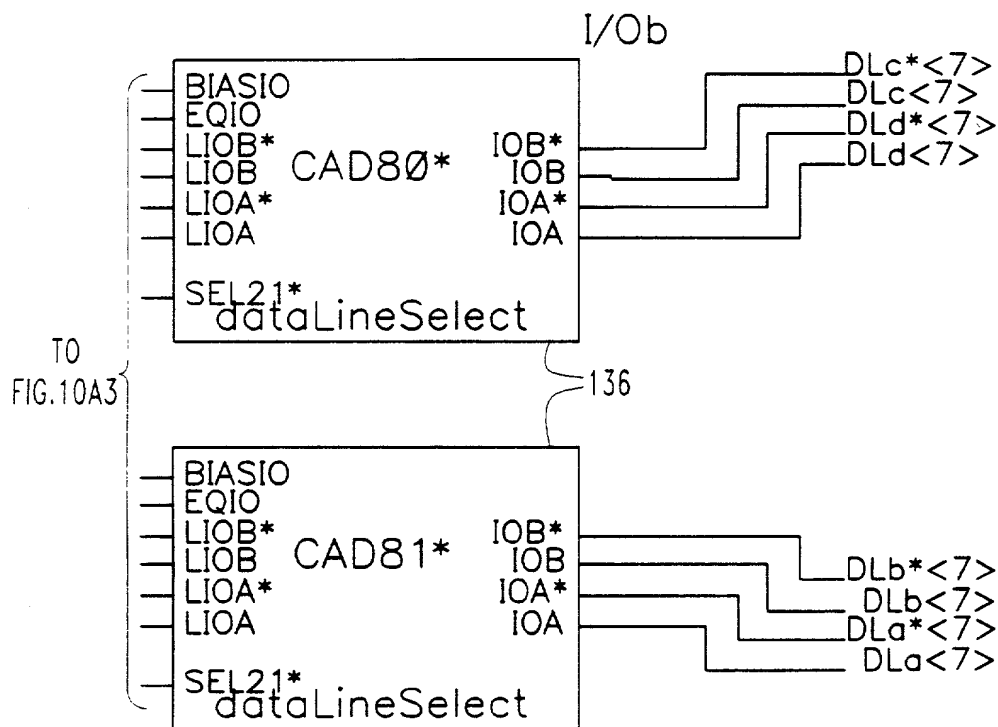
FIG. 10A3



arrayIOBlock

100

FIG. 10A4



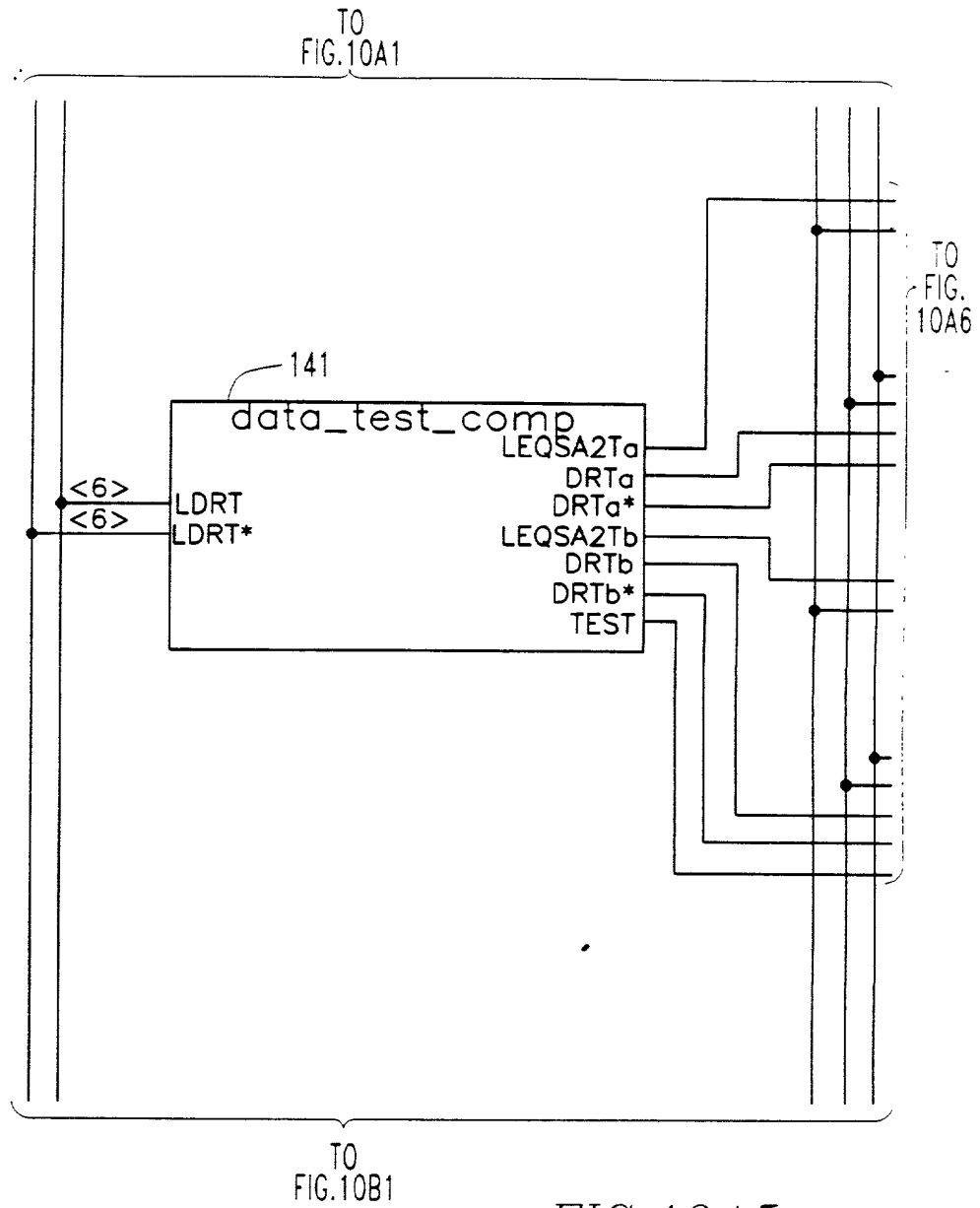


FIG. 10A5

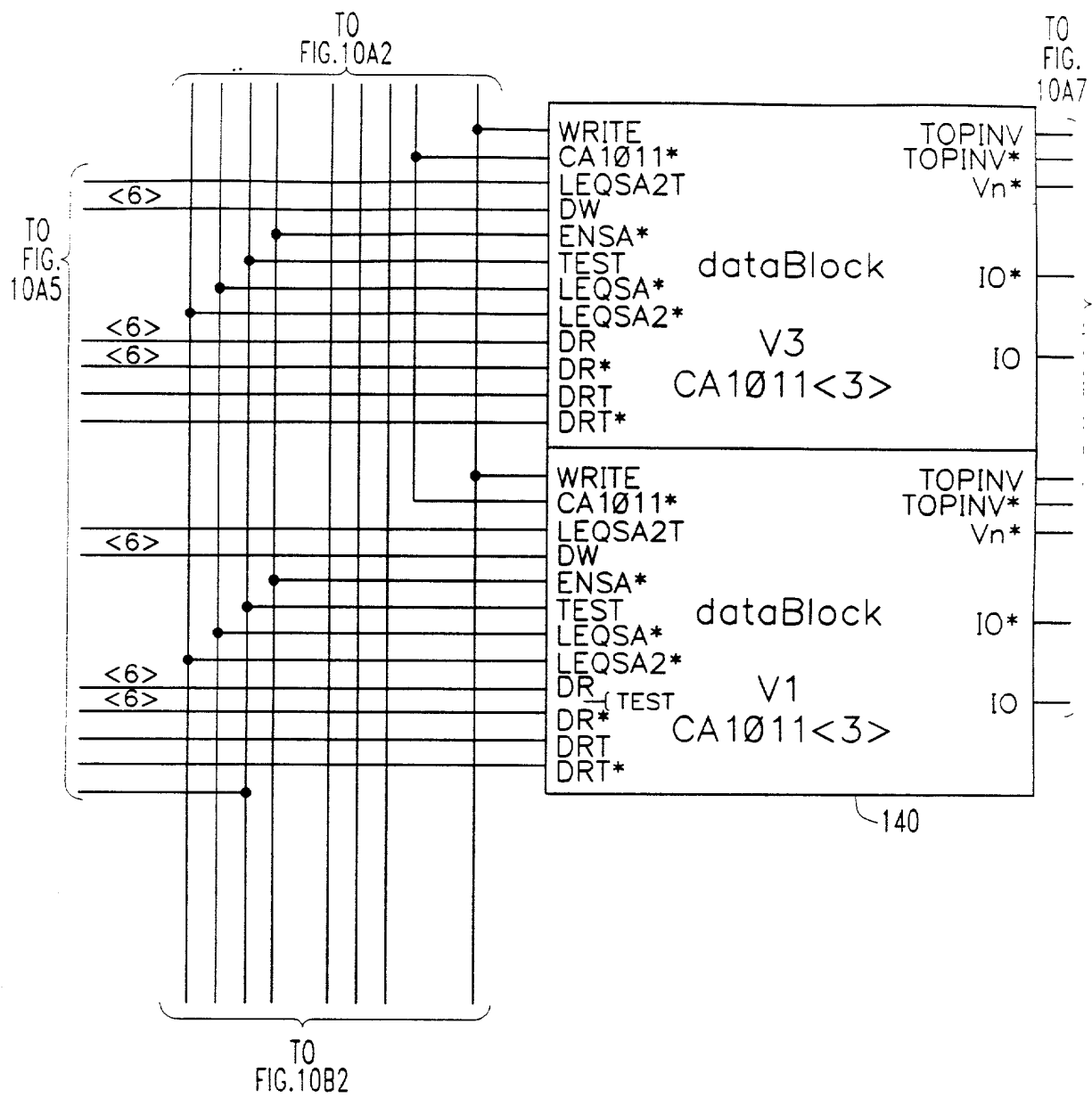


FIG. 10A6

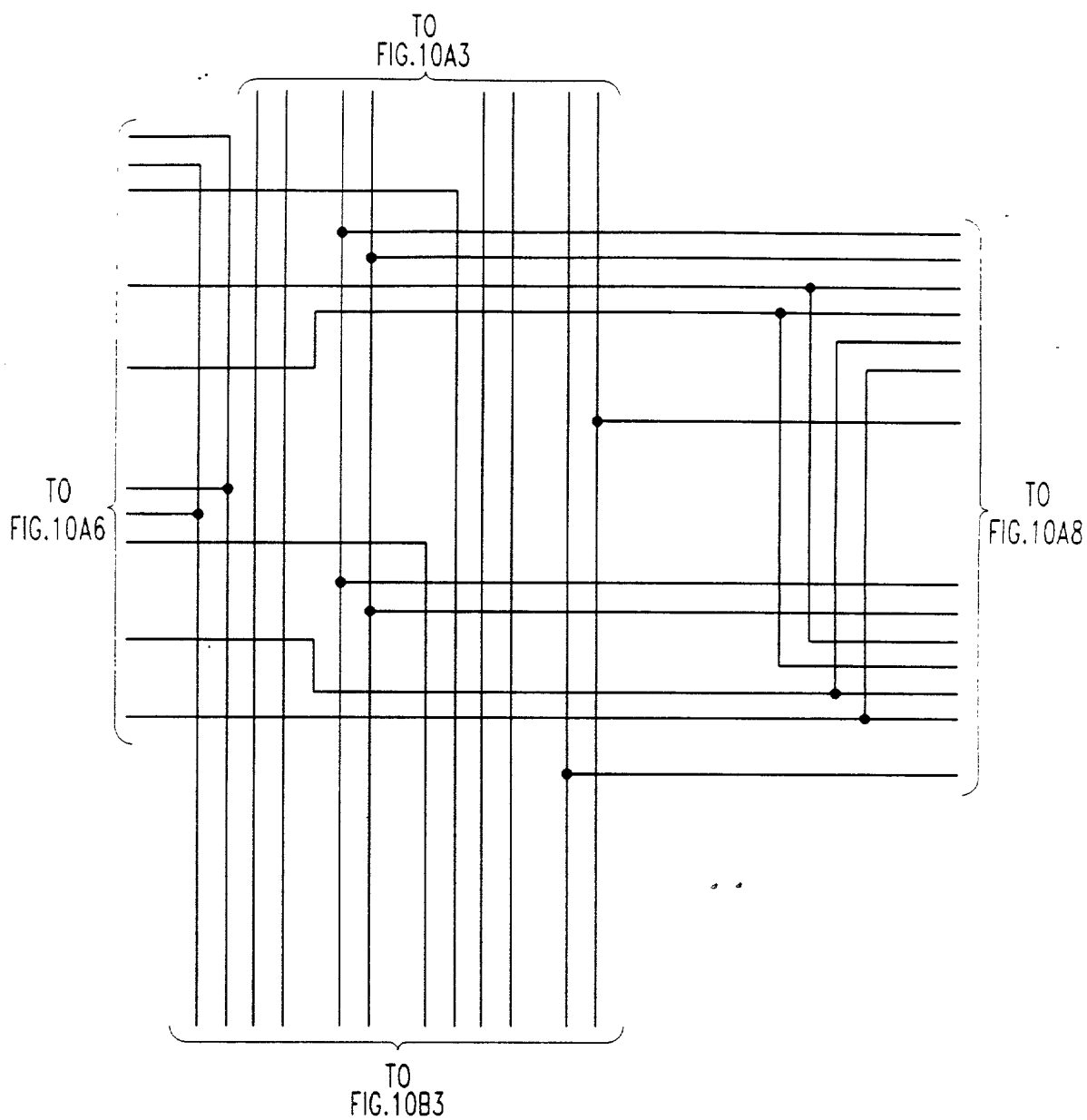


FIG. 10A7

I/Oa

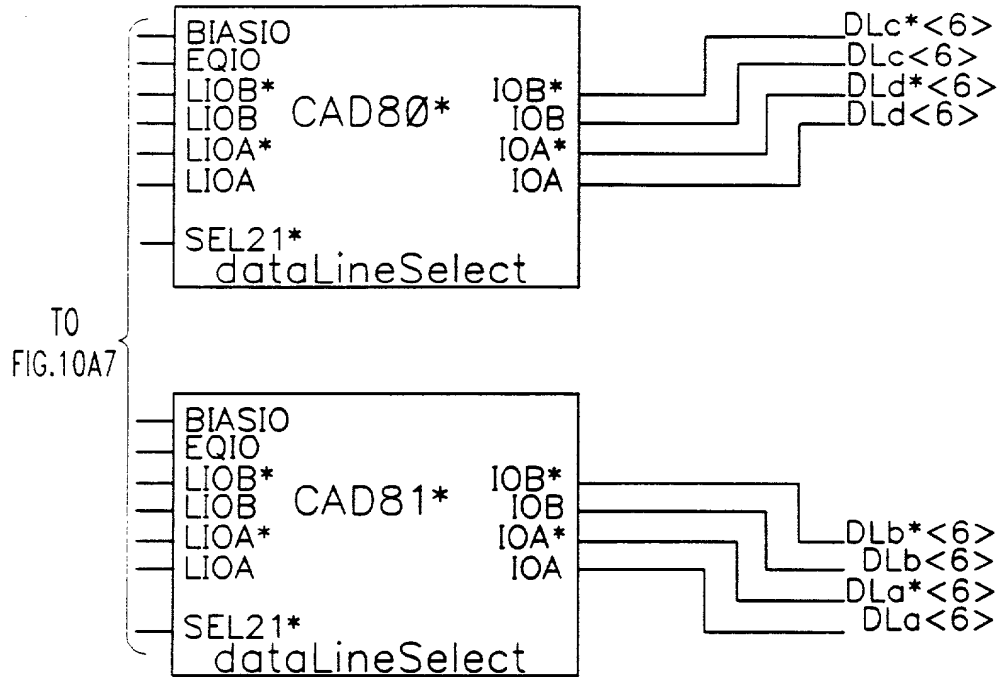


FIG.10A8



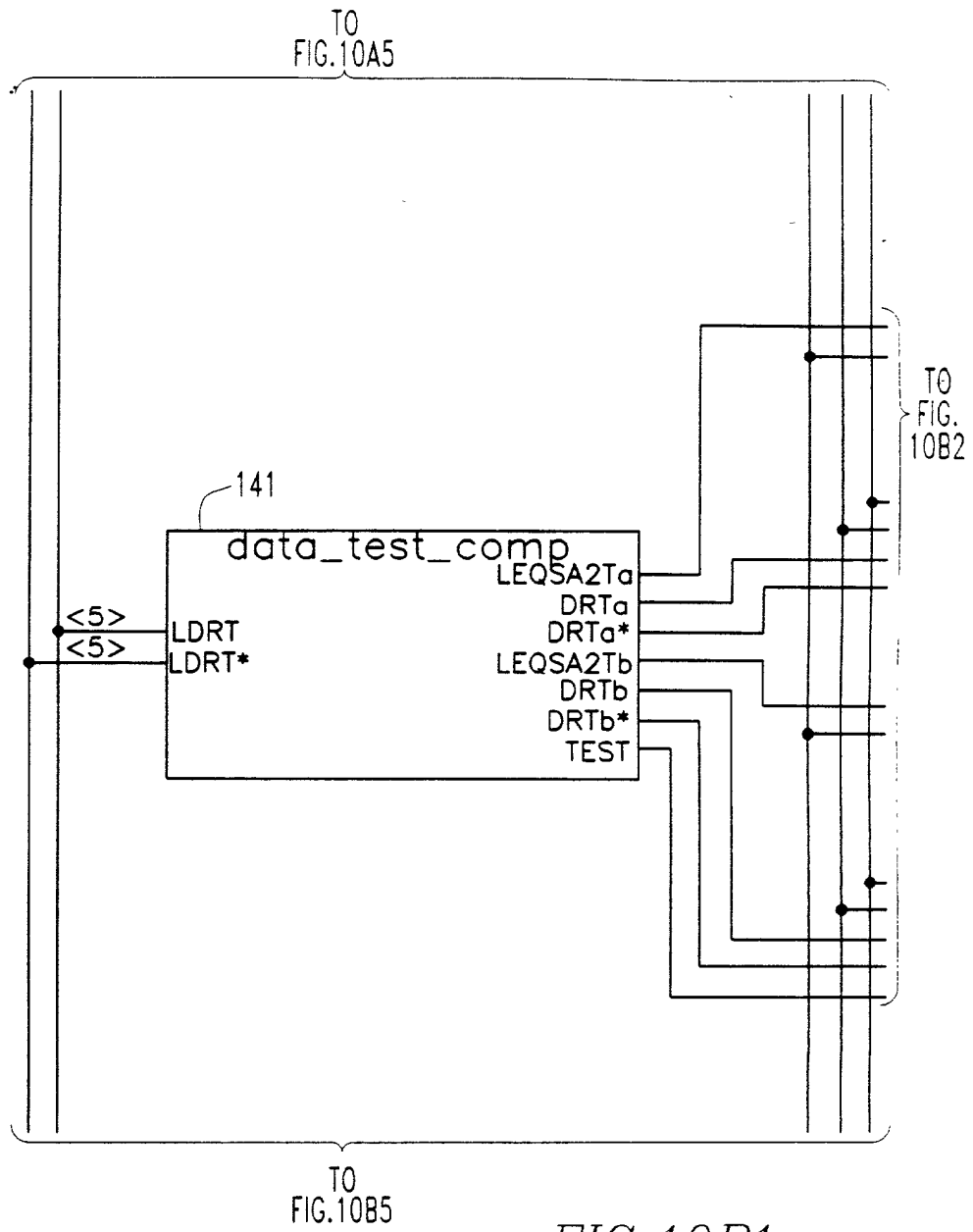
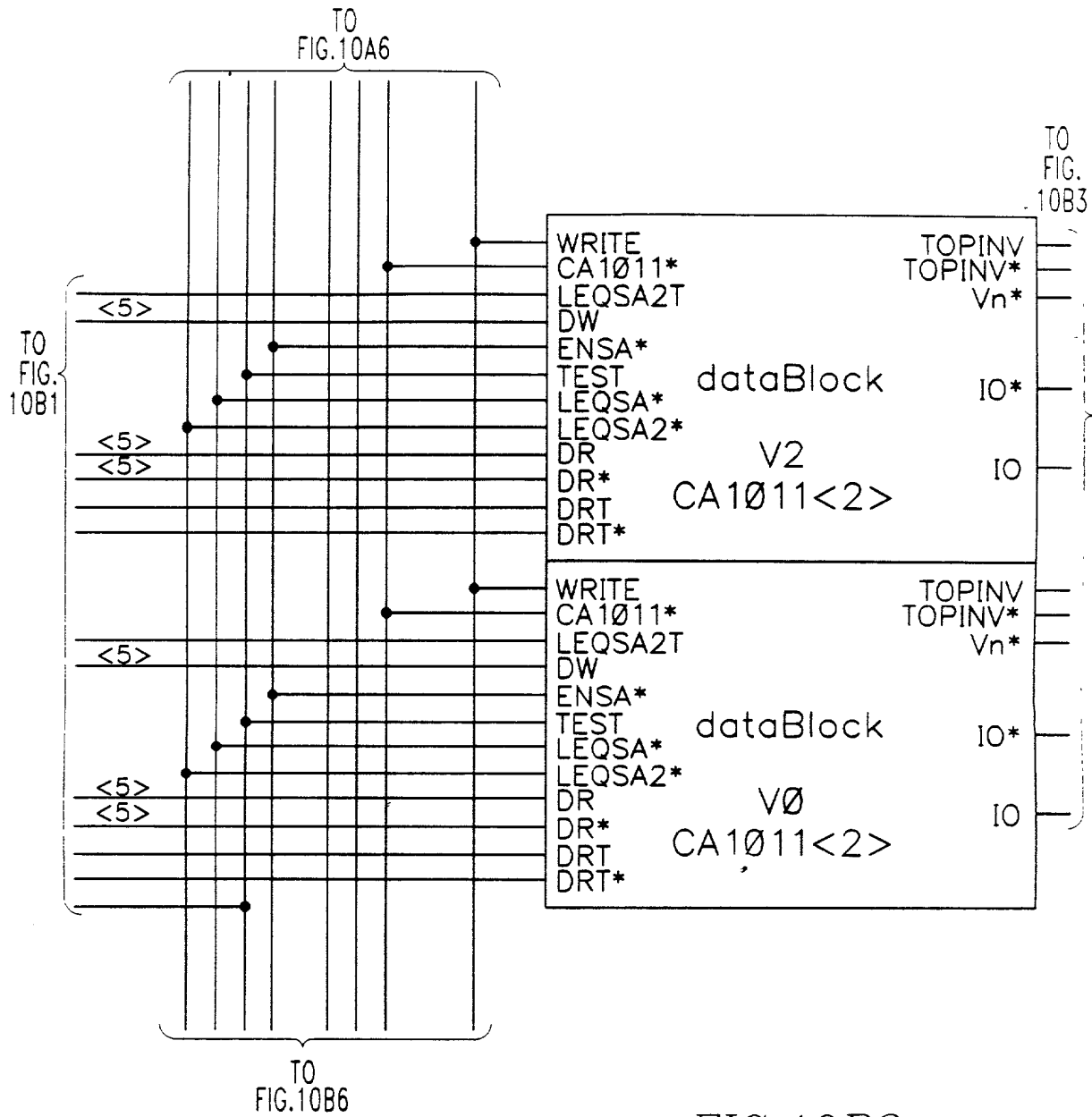


FIG. 10B1



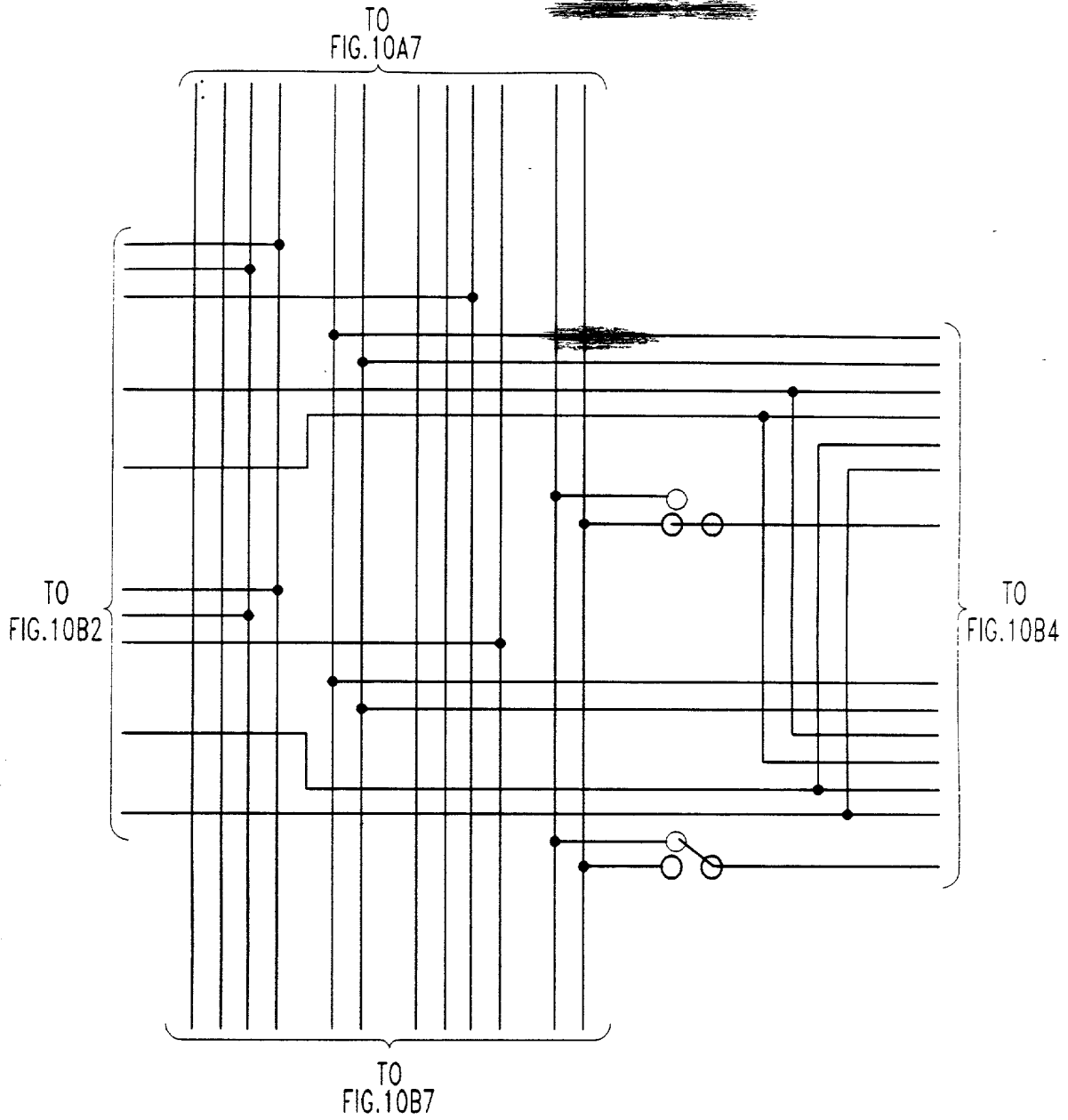


FIG. 10B3

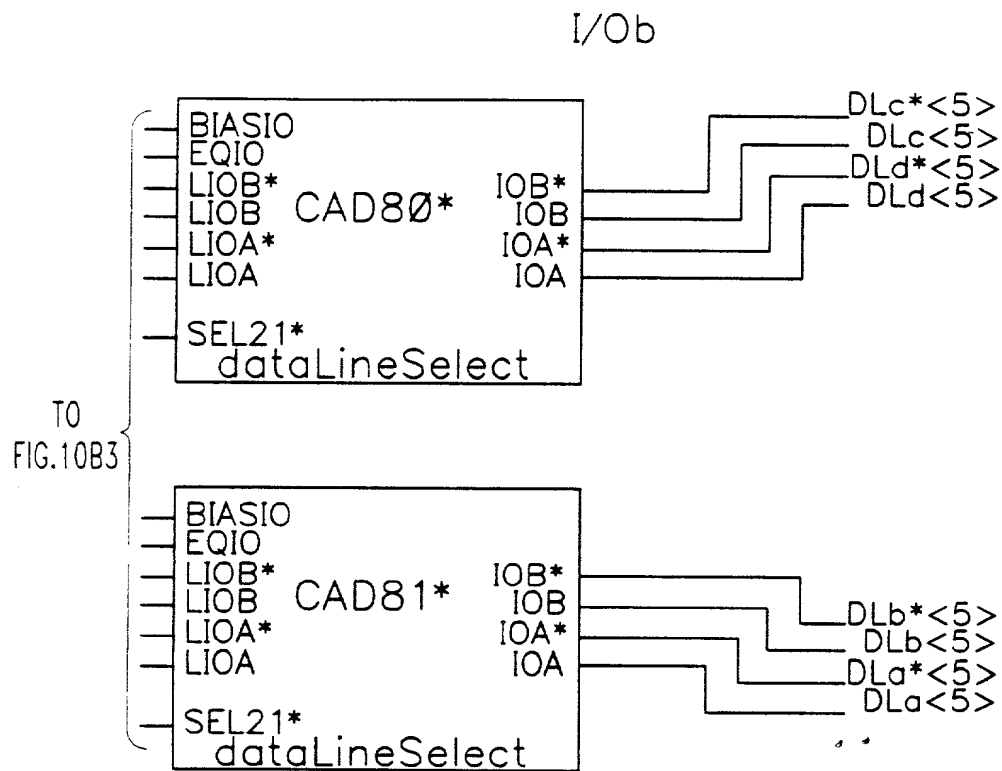


FIG.10B4

FIG. 10B5

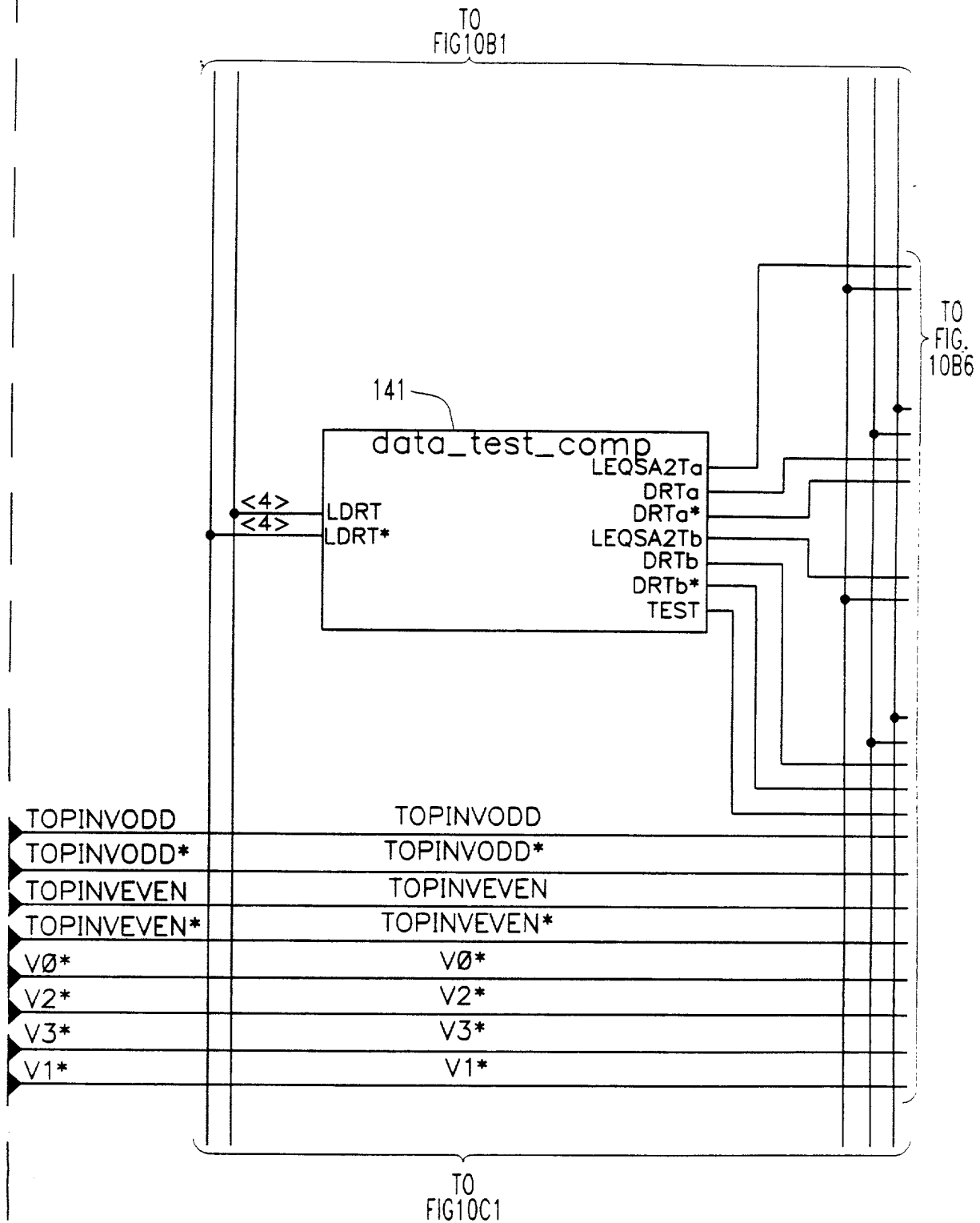


FIG. 10B6

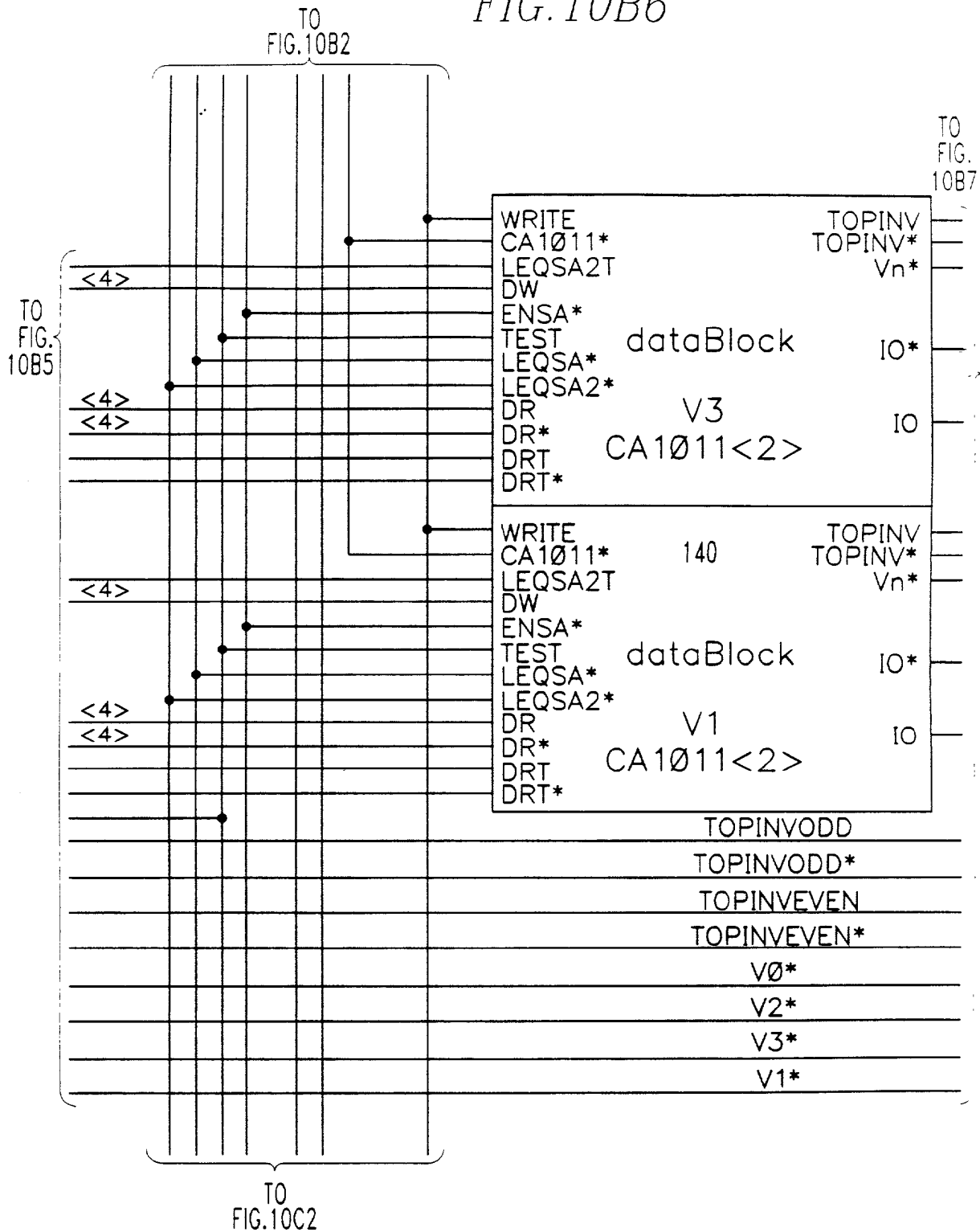


FIG. 10B7

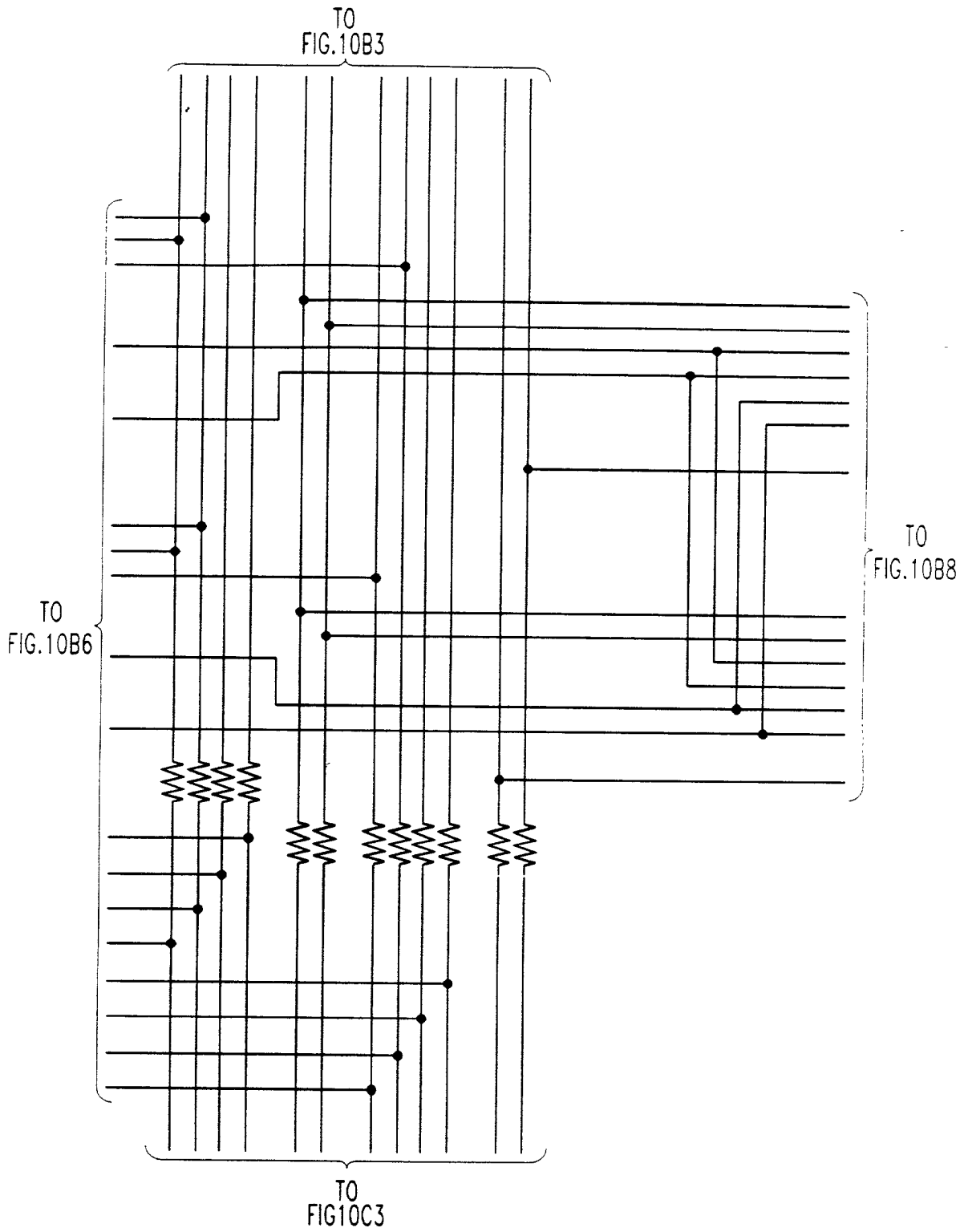
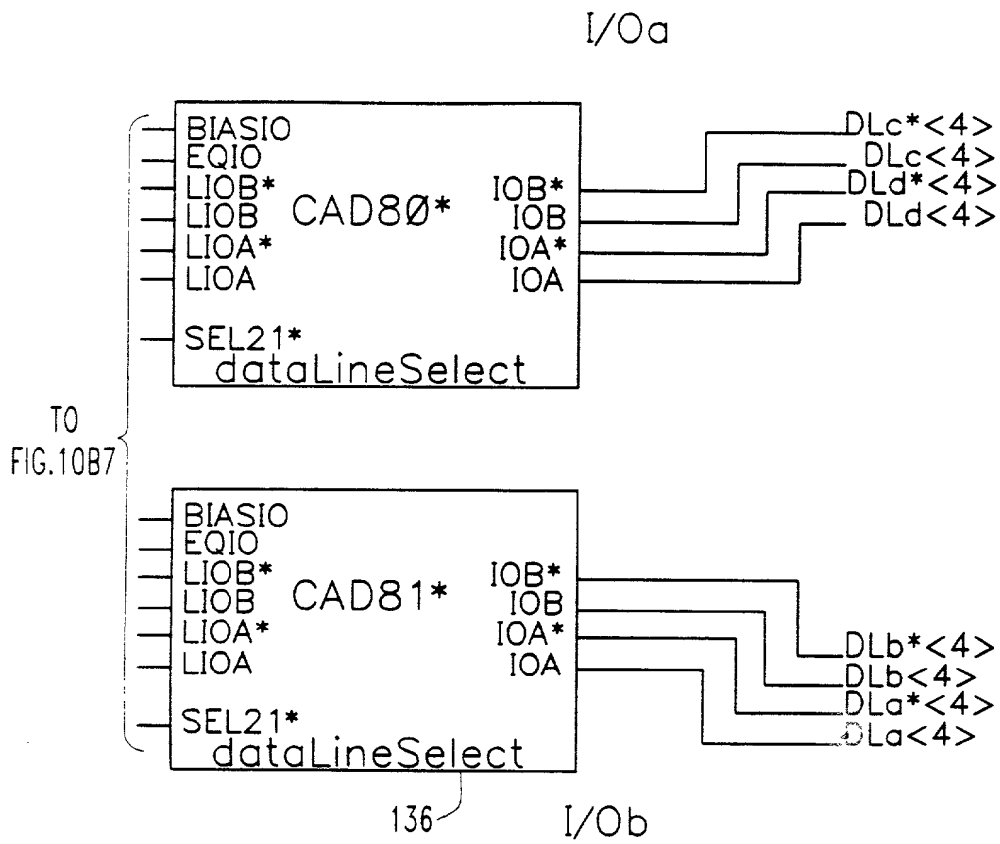


FIG. 10B7

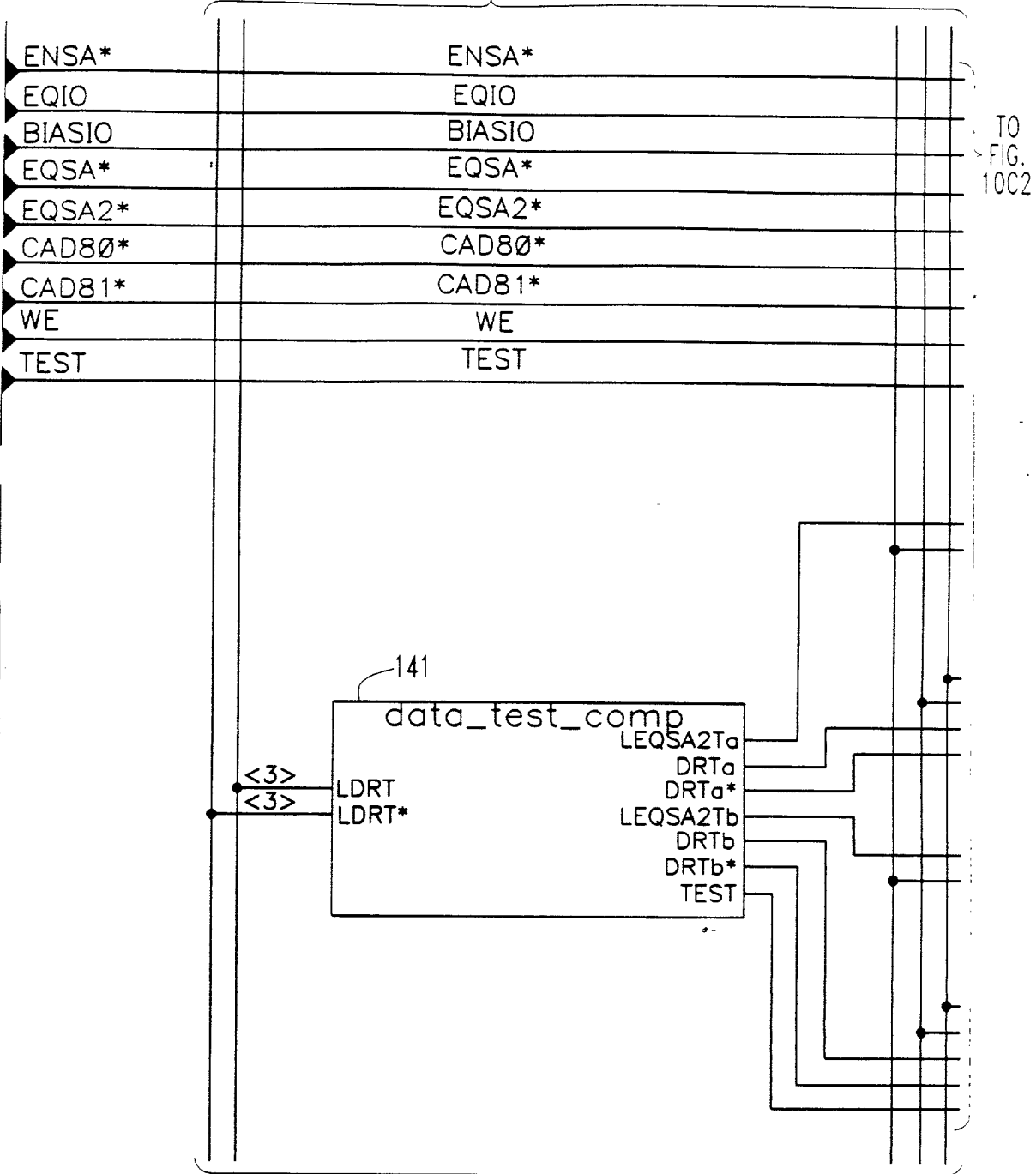
FIG. 10B8





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TO  
FIG. 10B5



TO  
FIG. 10C5

FIG. 10C1

FIG. 10C1

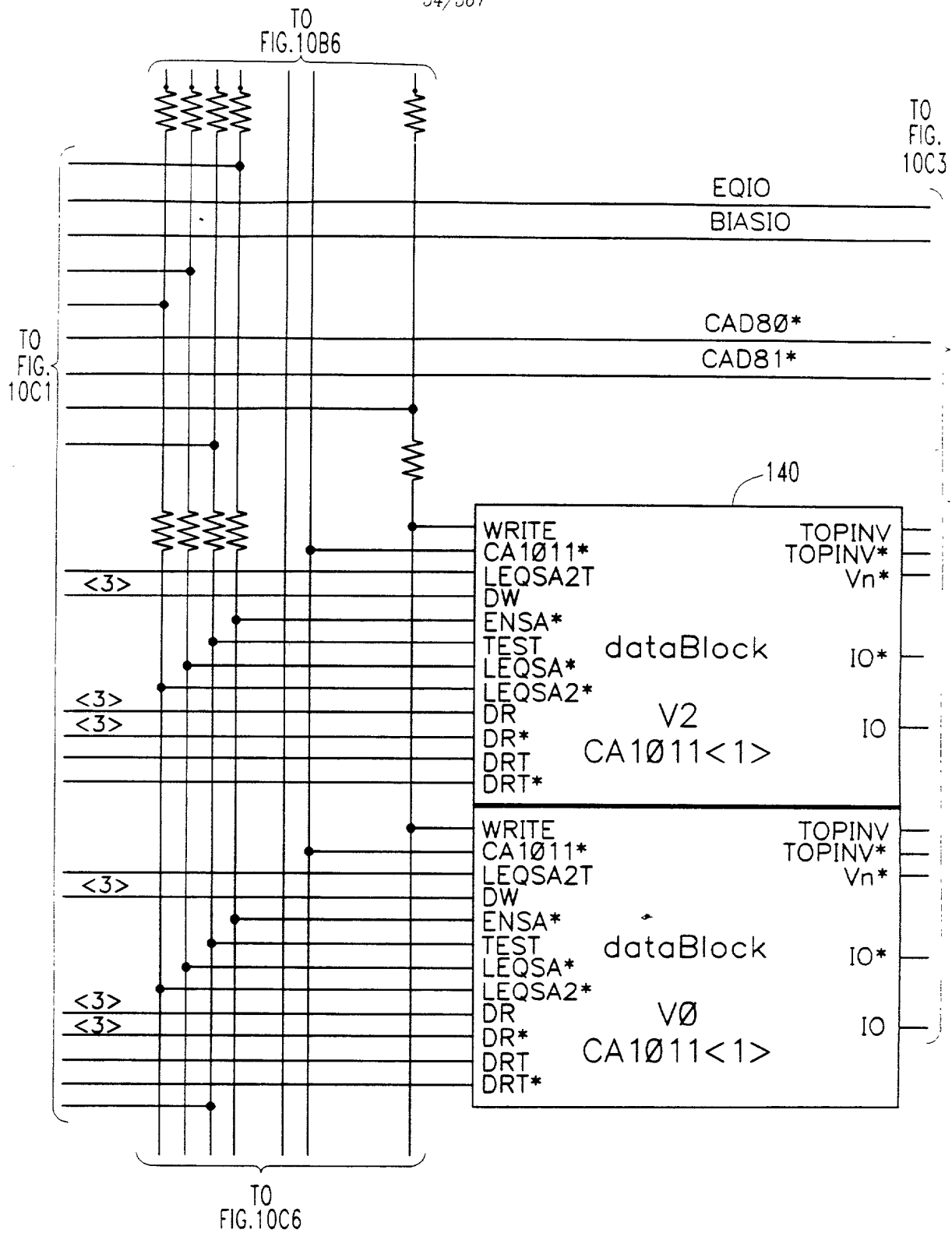


FIG. 10C2

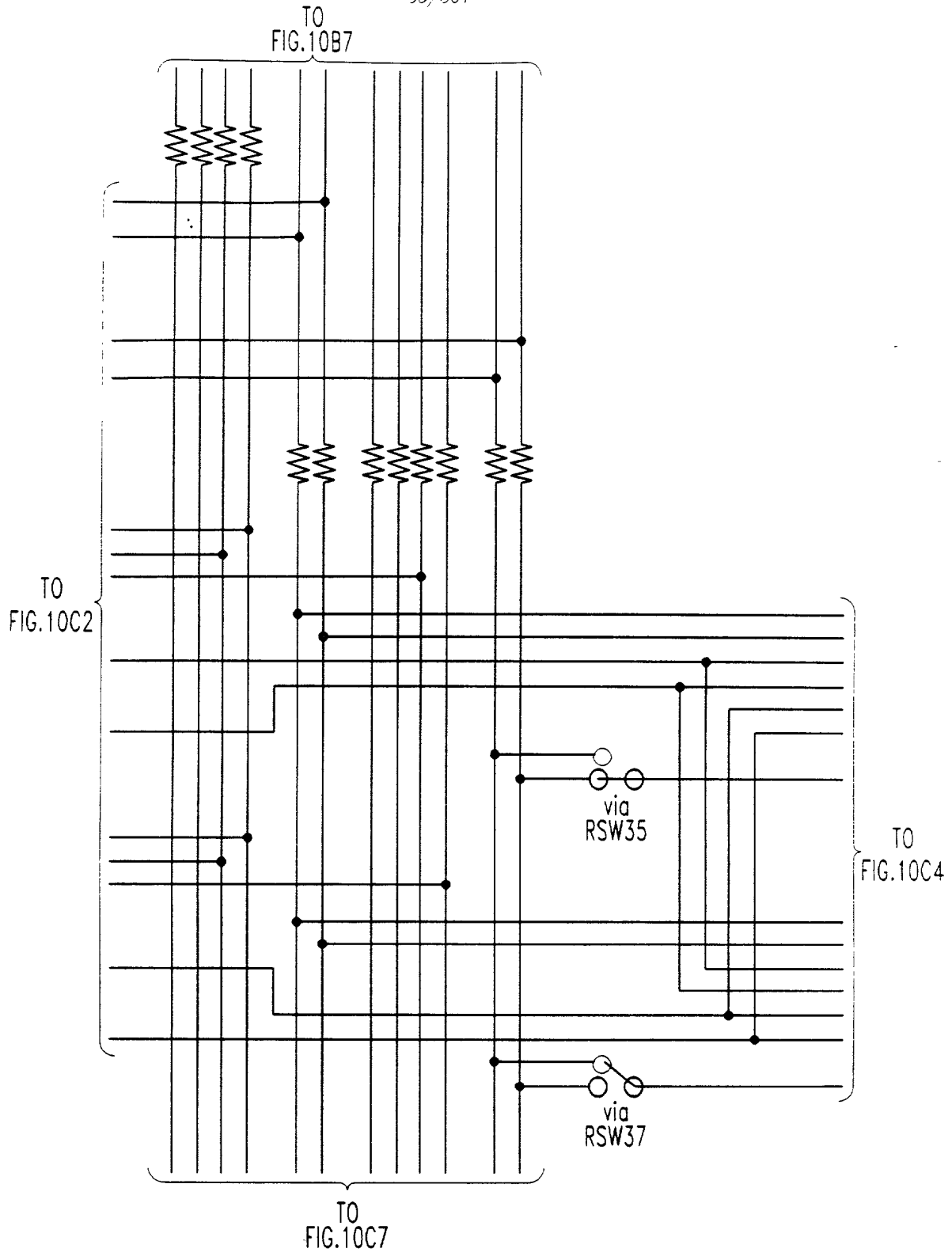
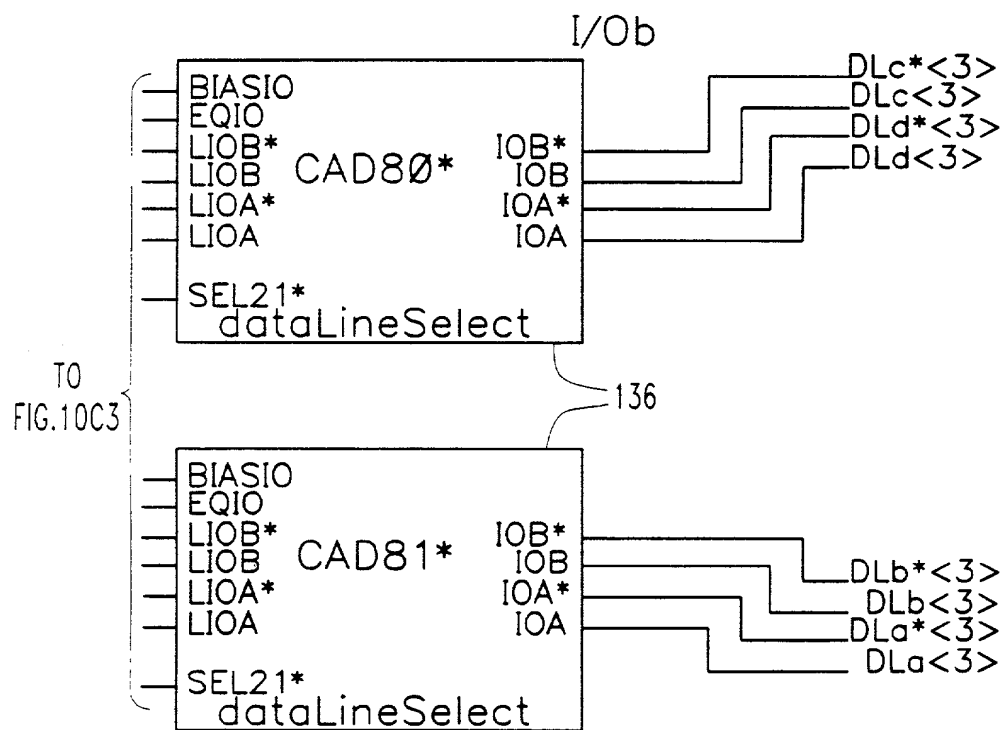


FIG. 10C3

FIG. 10C4



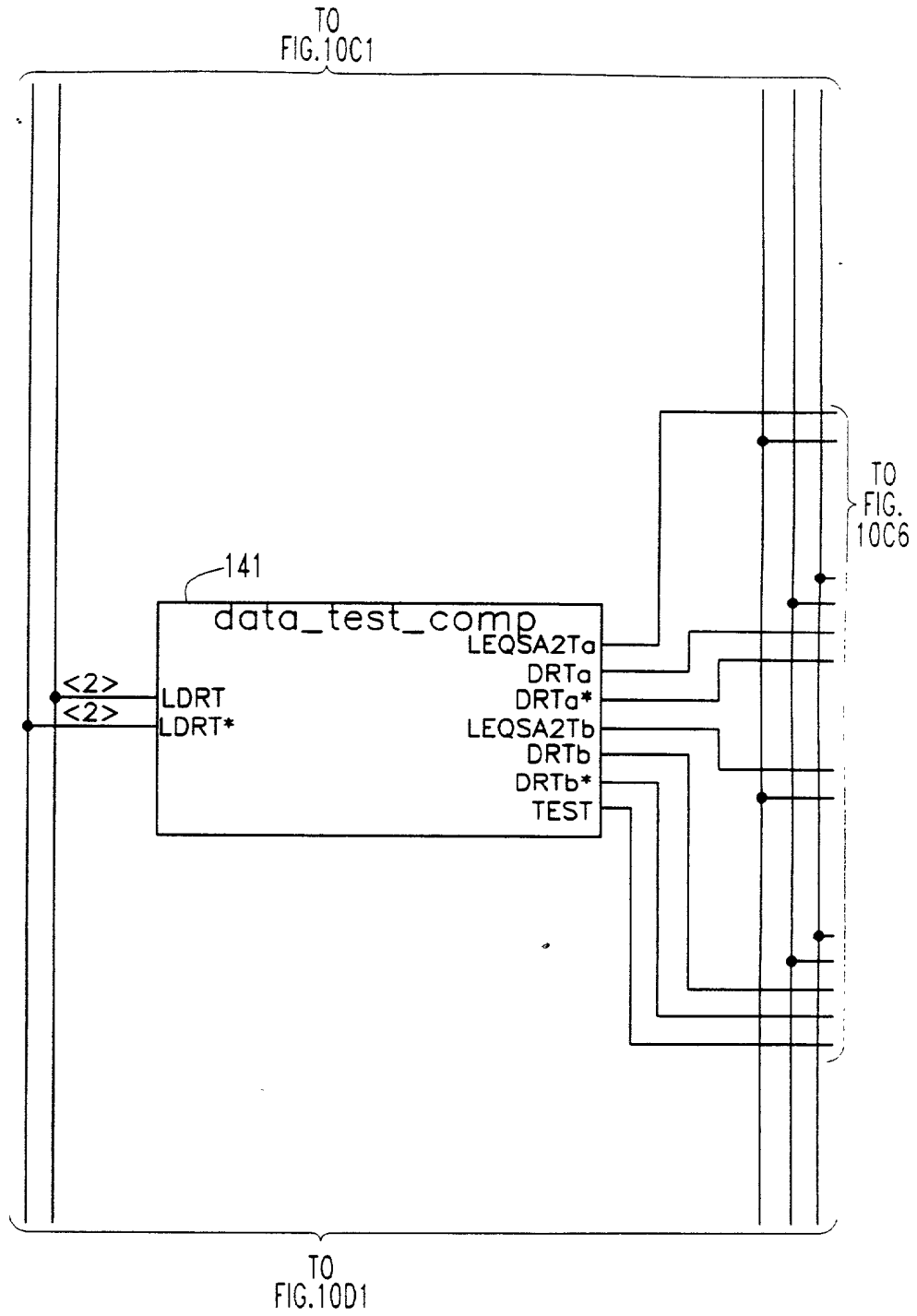


FIG. 10C5

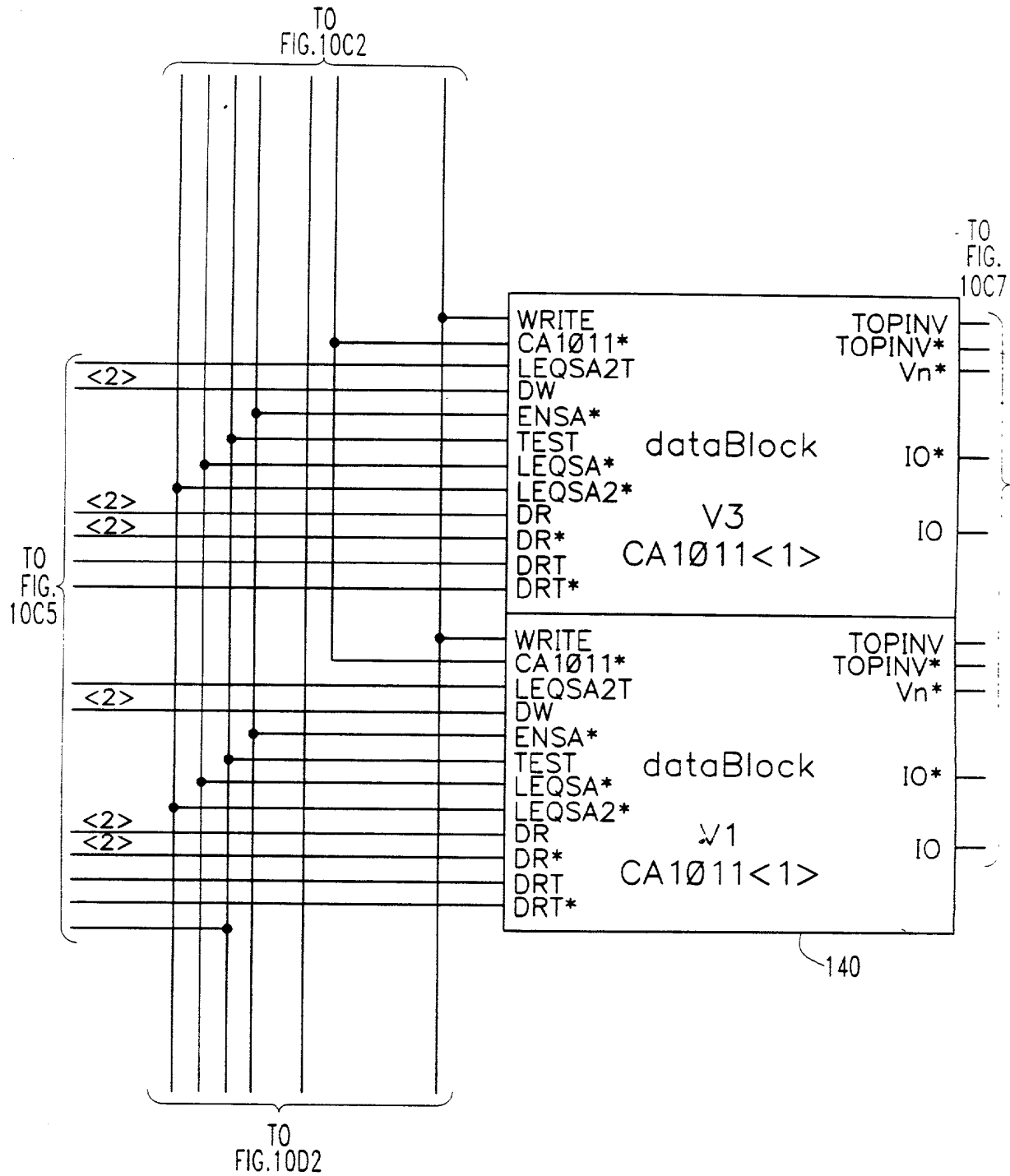


FIG. 10C6

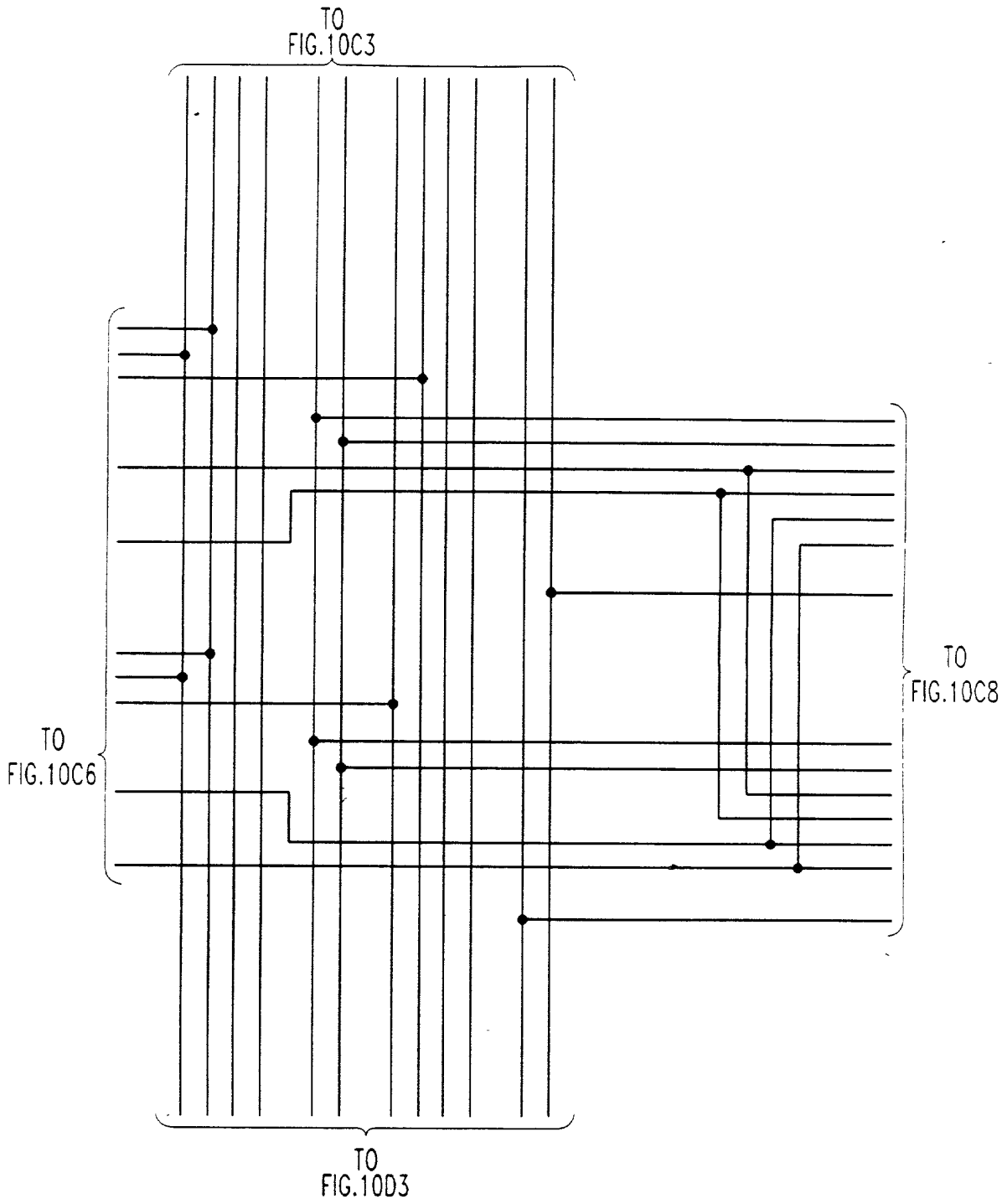


FIG. 10C7

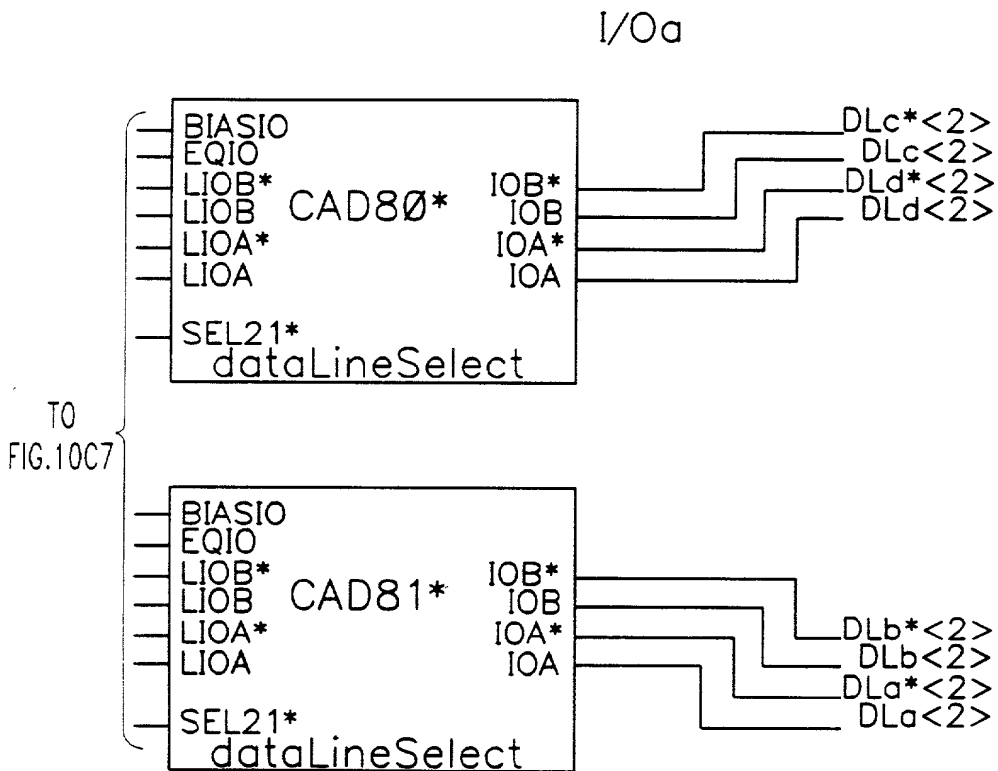


FIG.10C8



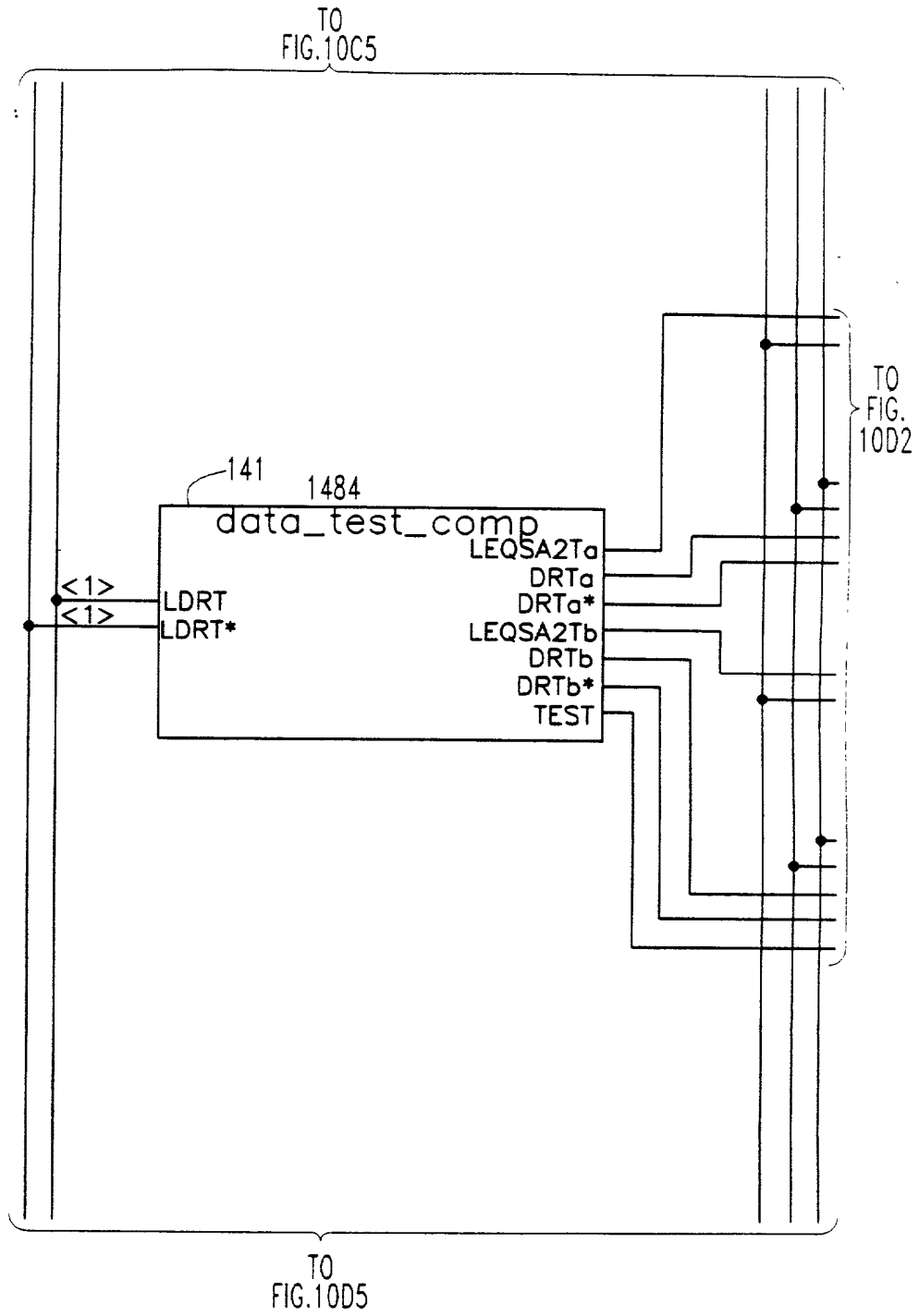


FIG. 10D1

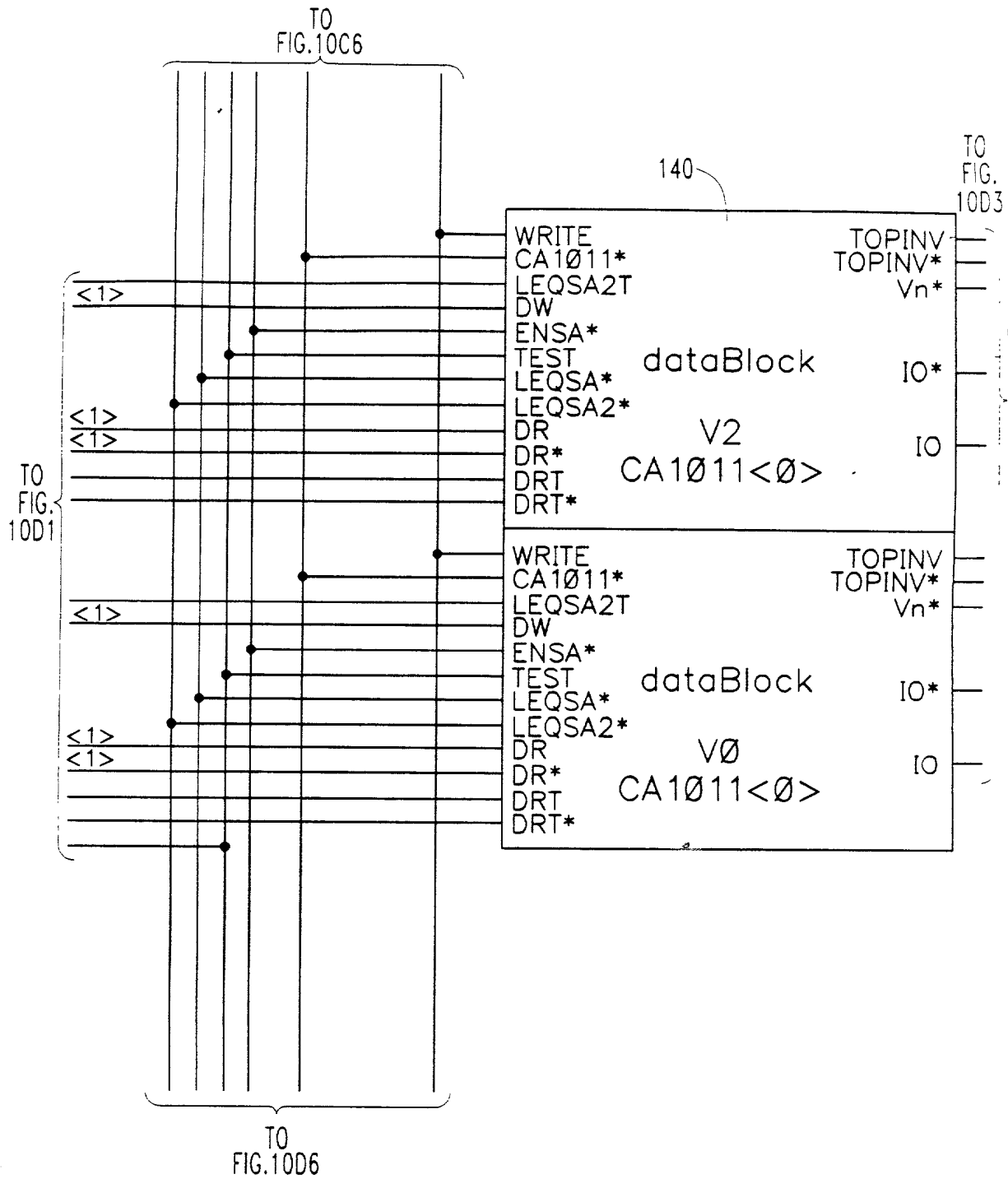
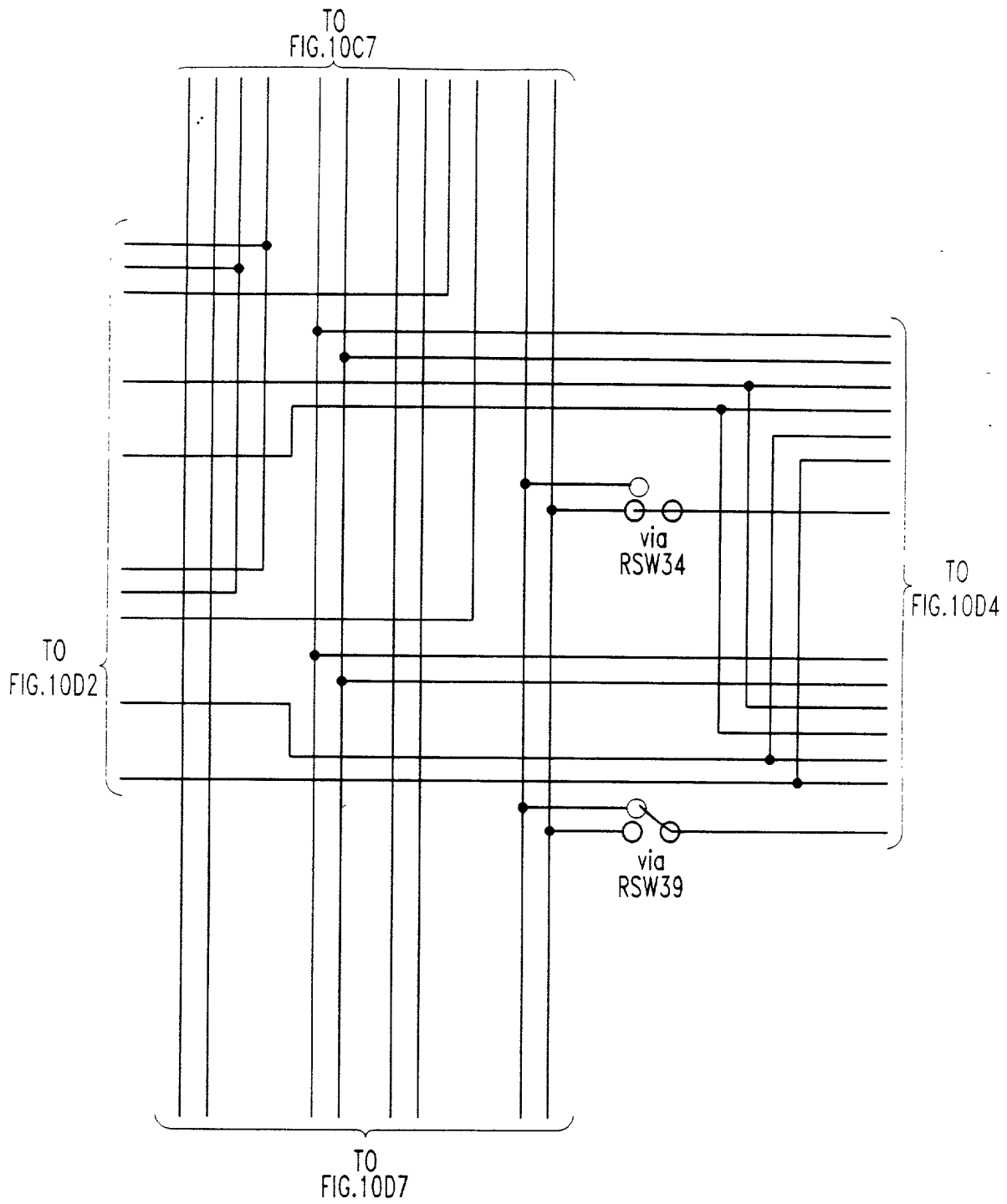


FIG. 10D2

*FIG. 10D3*

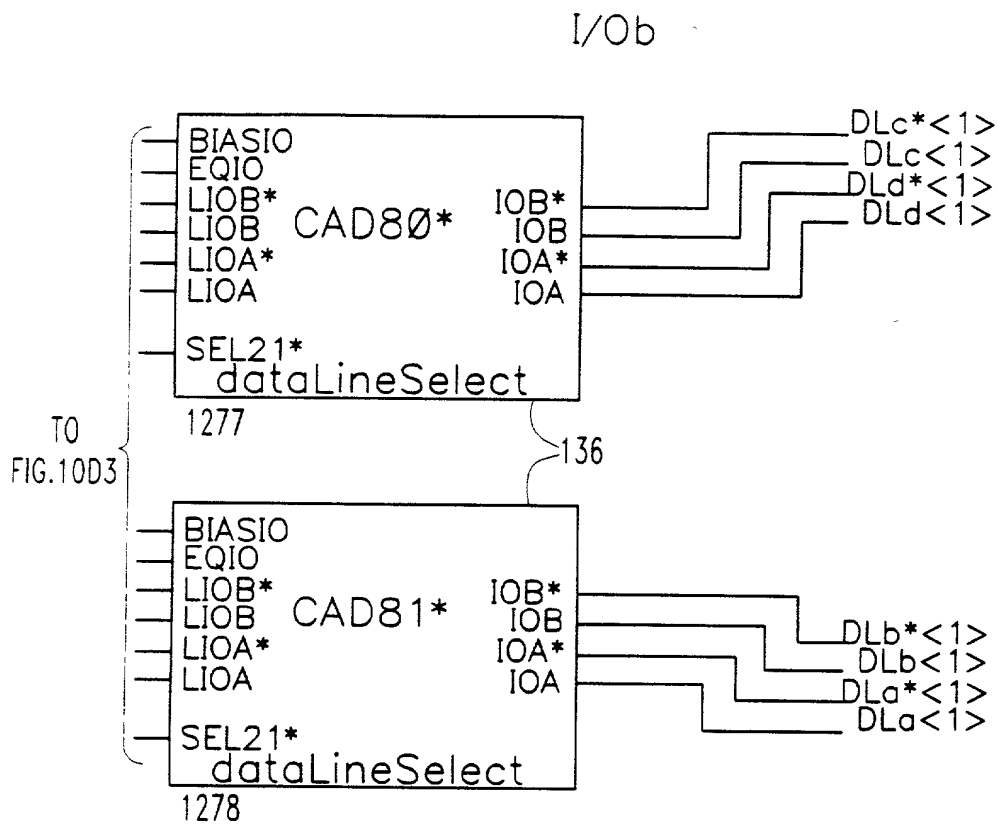


FIG. 10D4

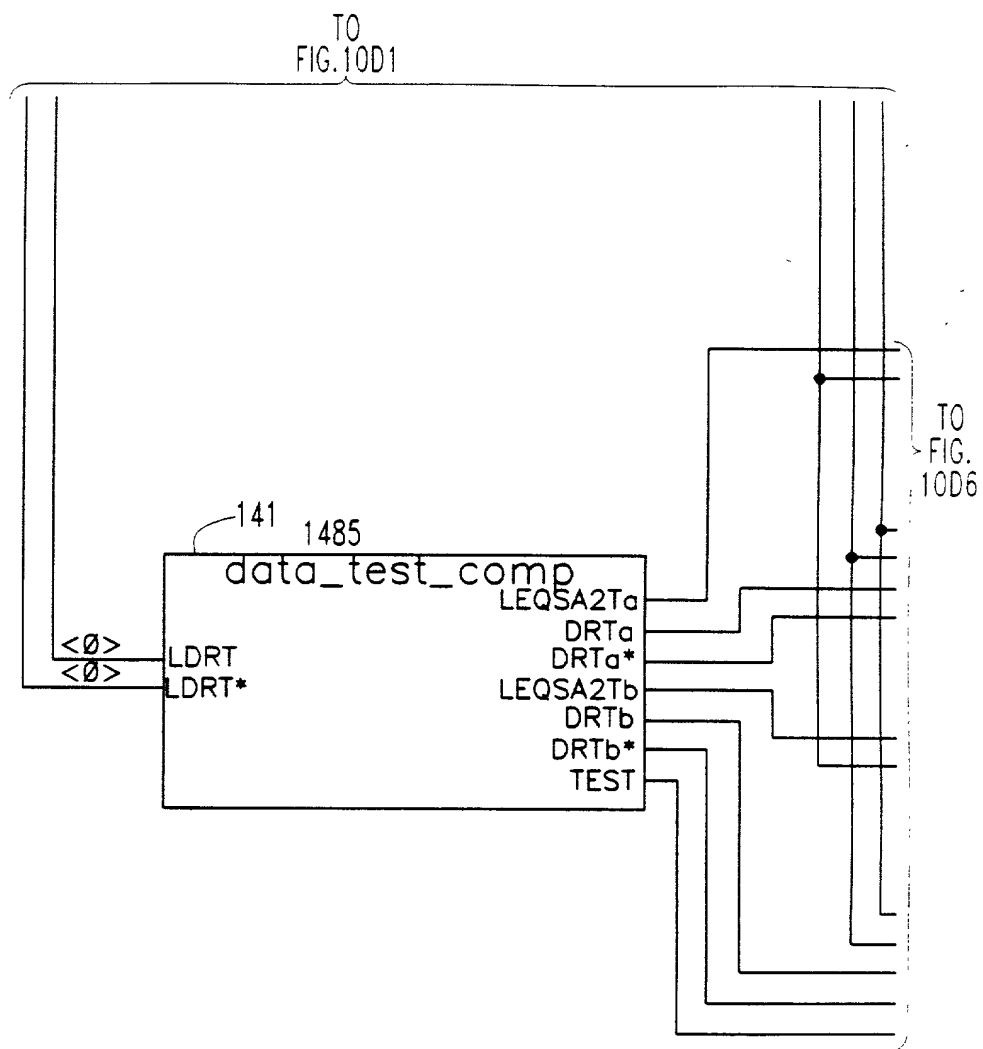


FIG. 10D5

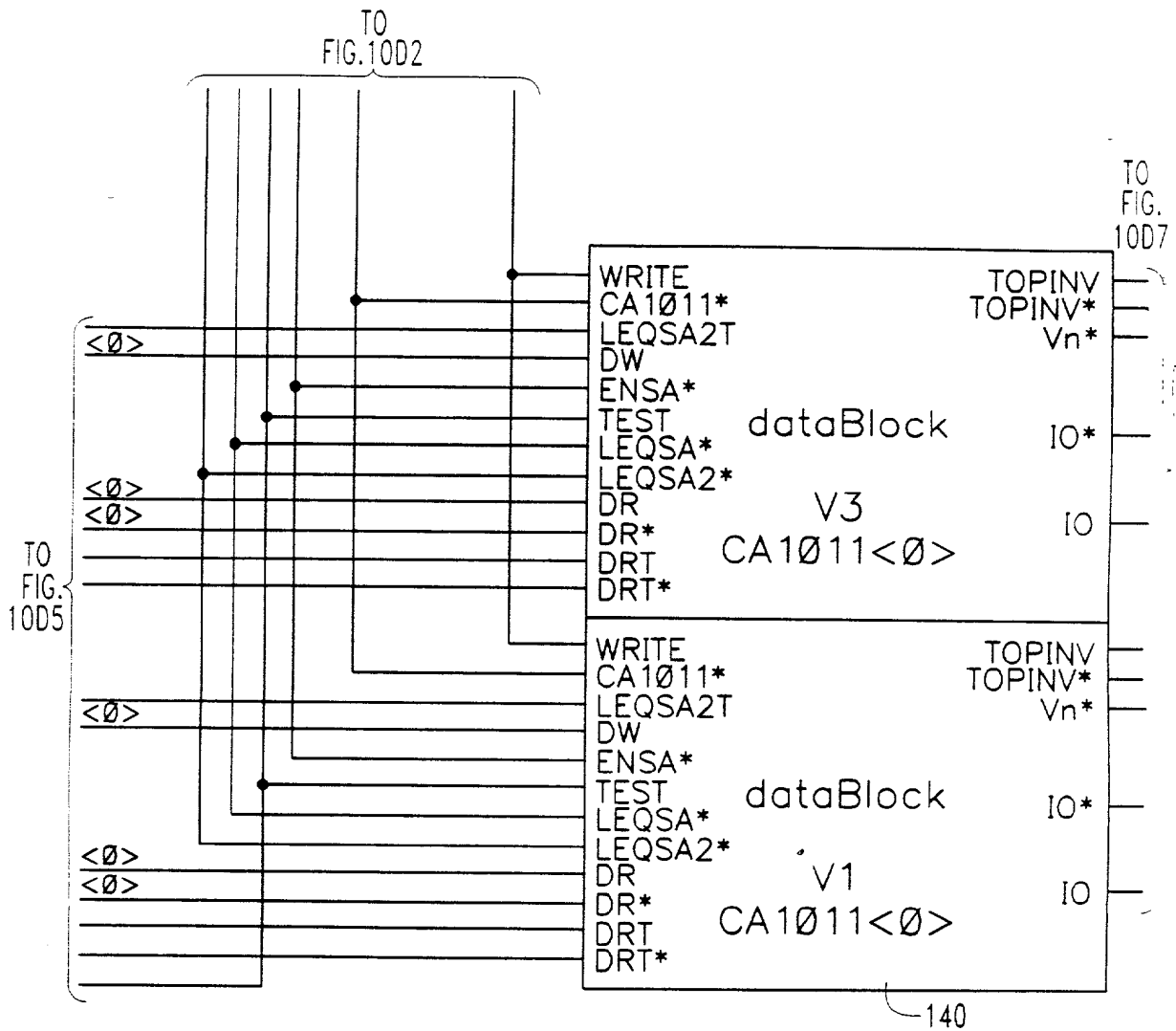


FIG. 10D6

FIG. 10D7

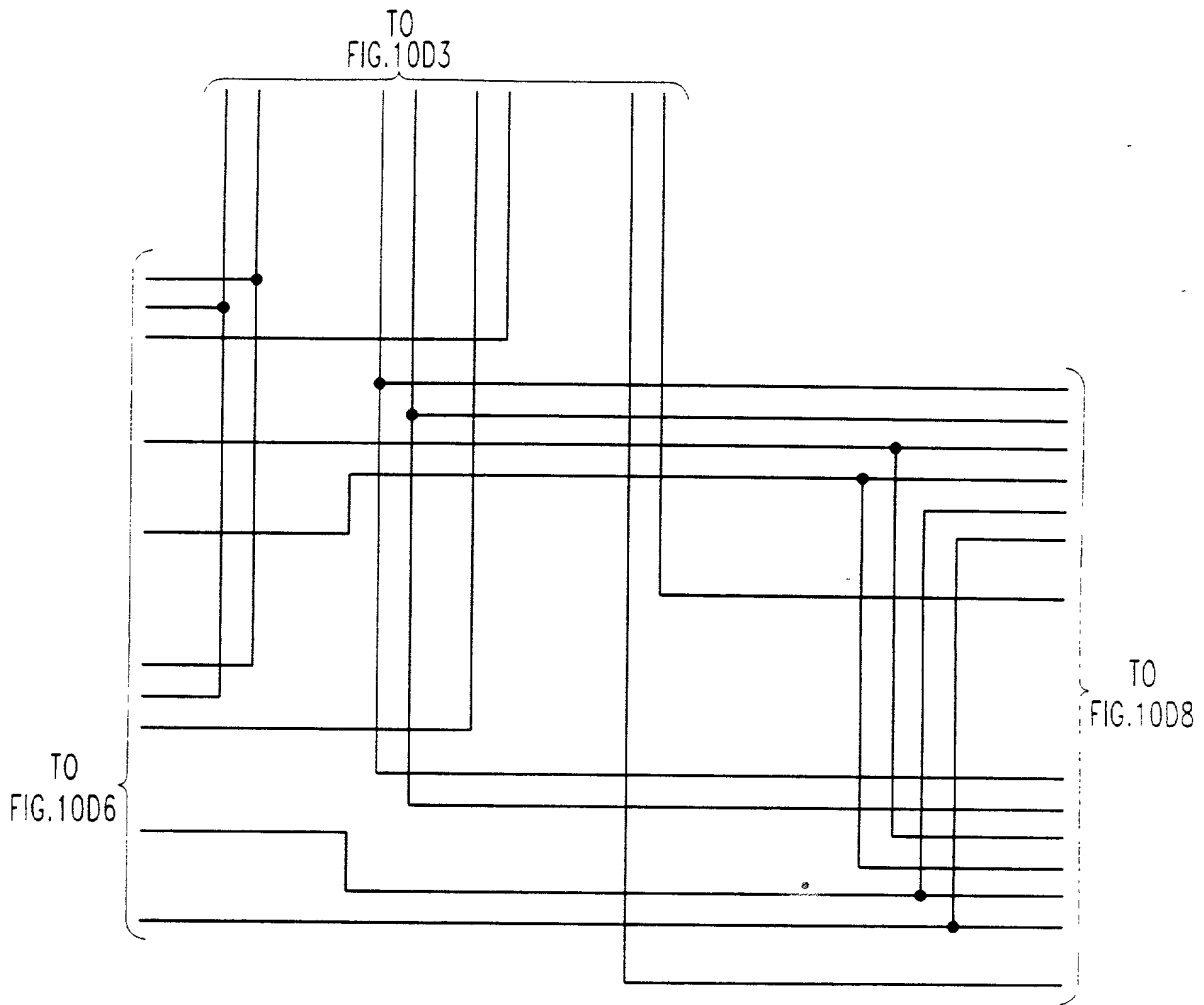


FIG. 10D7

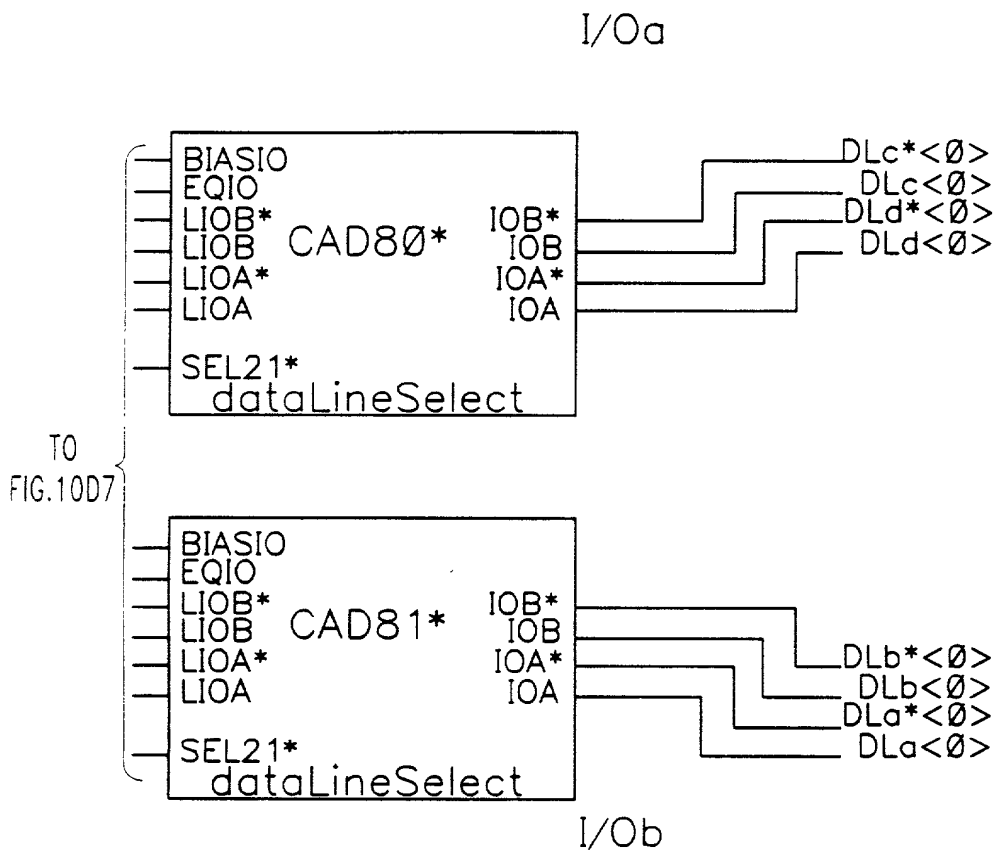


FIG.10D8



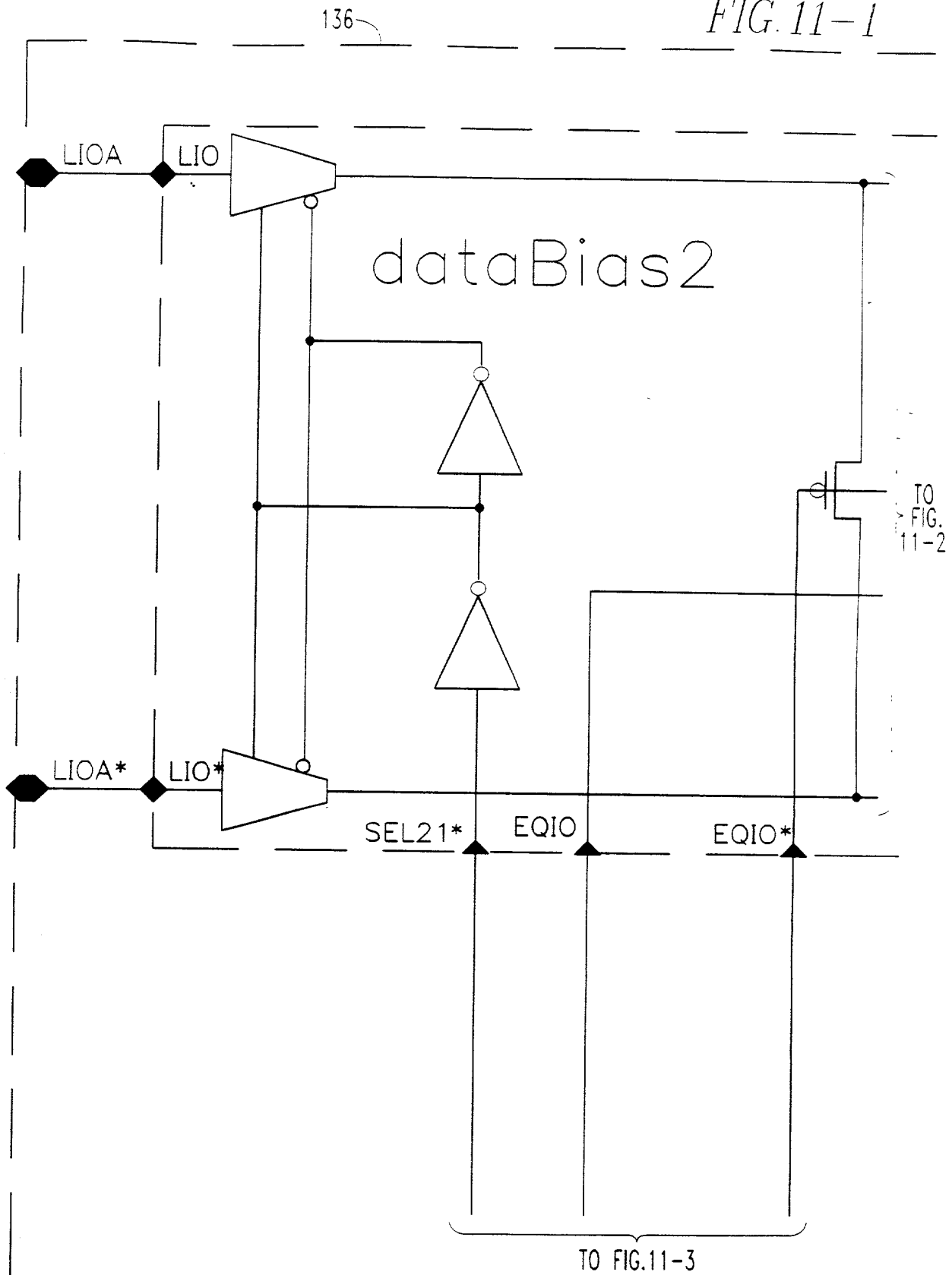


FIG. 11-2

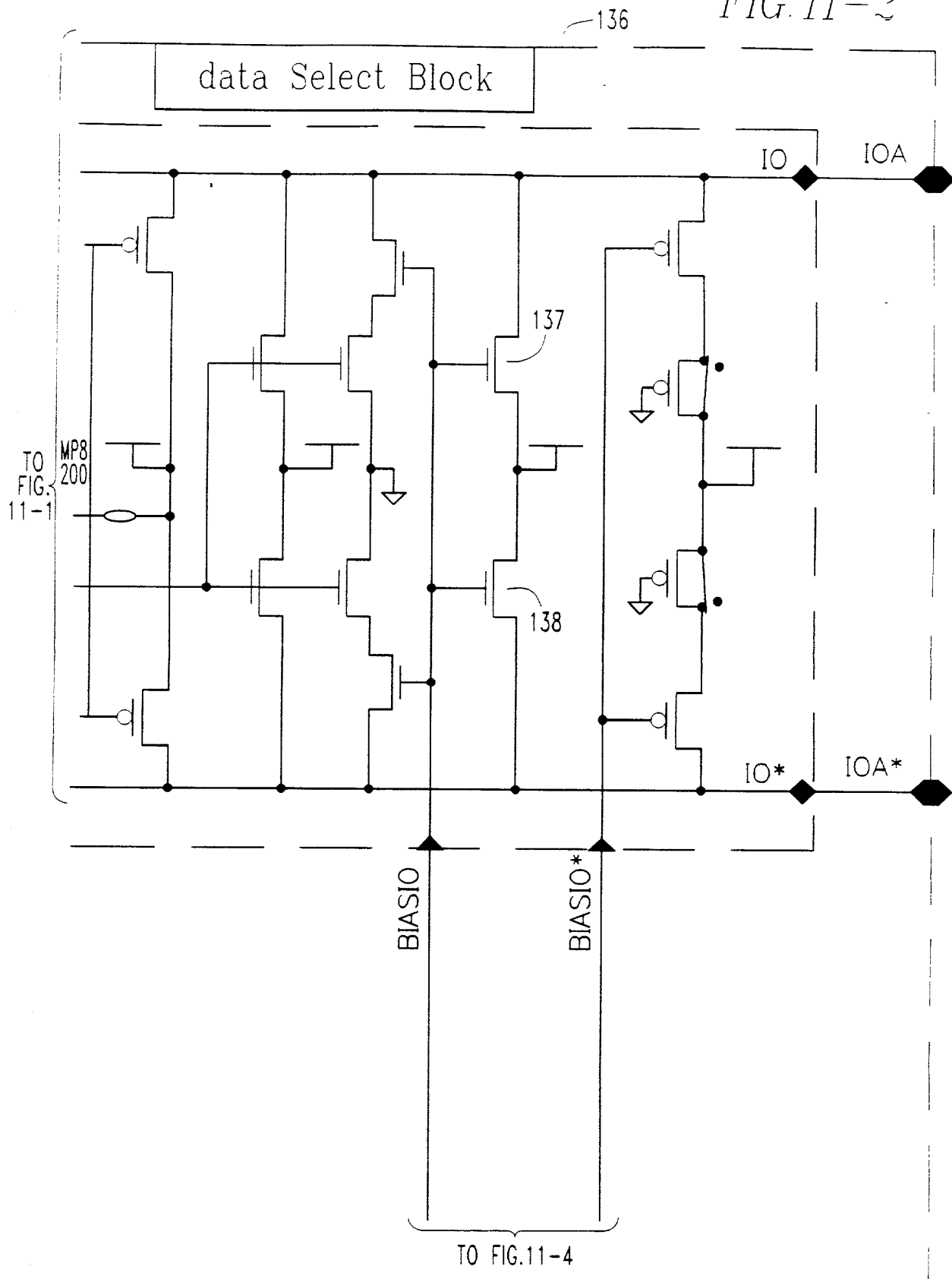


FIG. 11-3

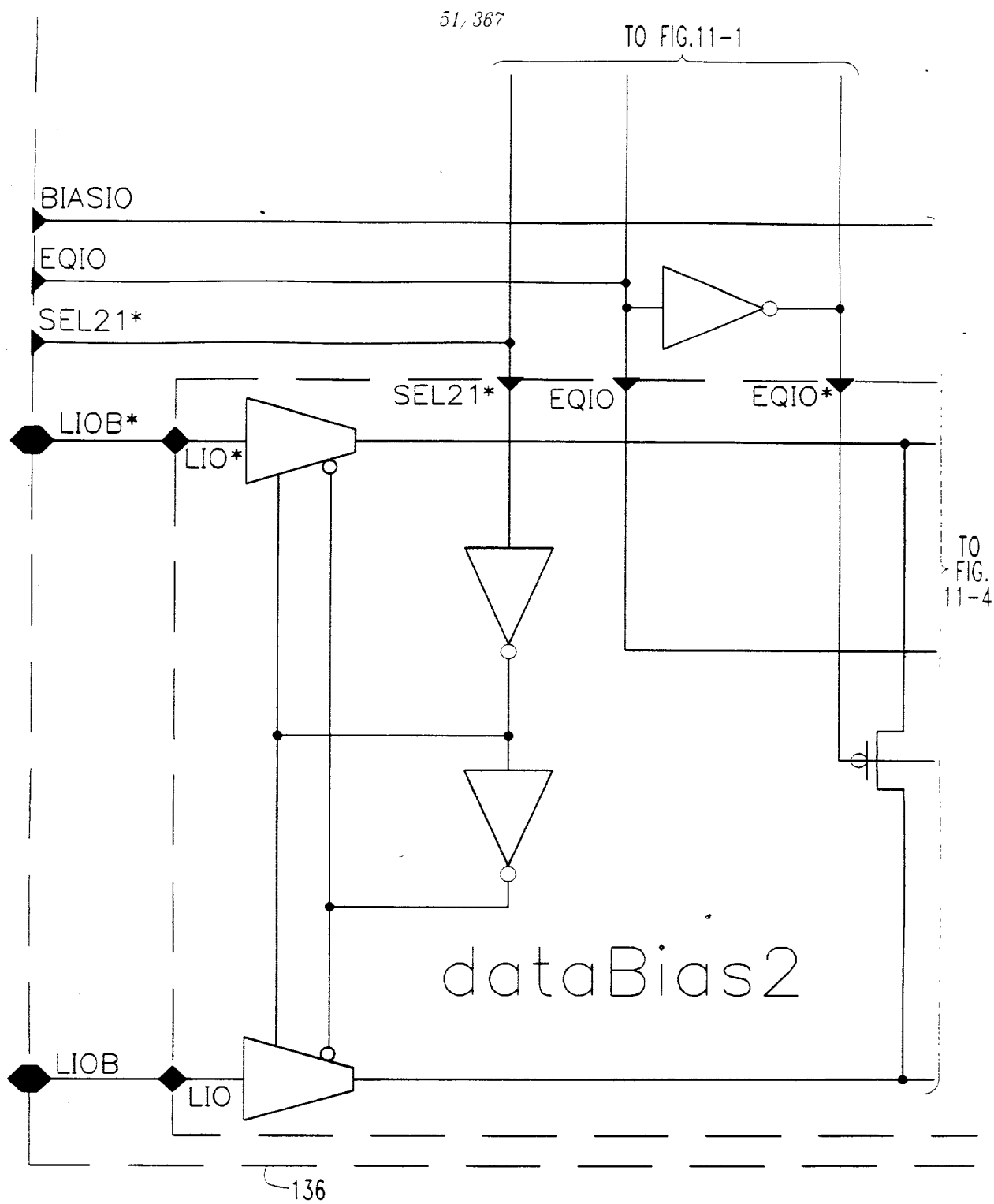


FIG. 11-3

TO  
FIG.  
11-3

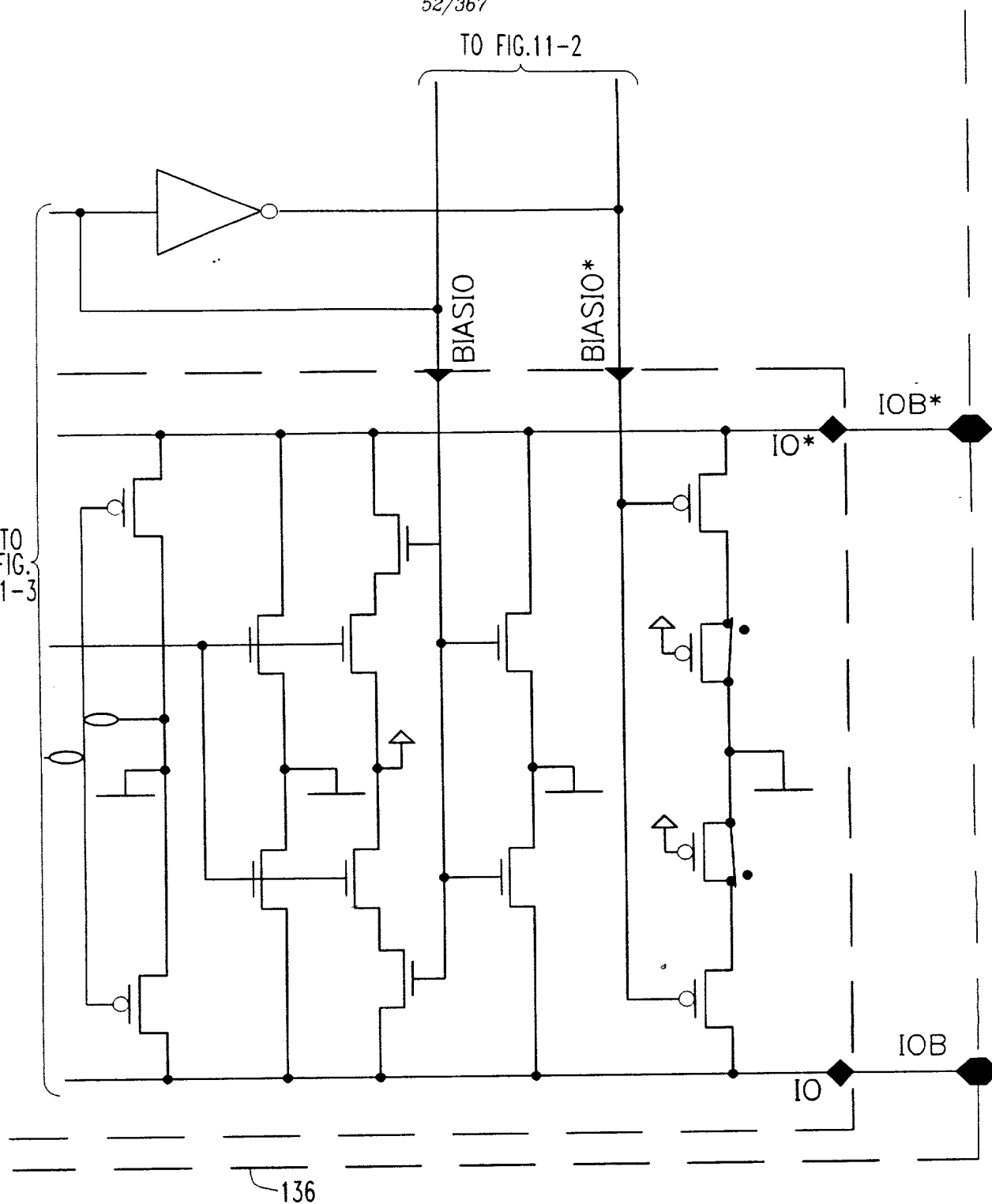
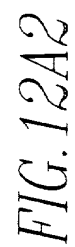
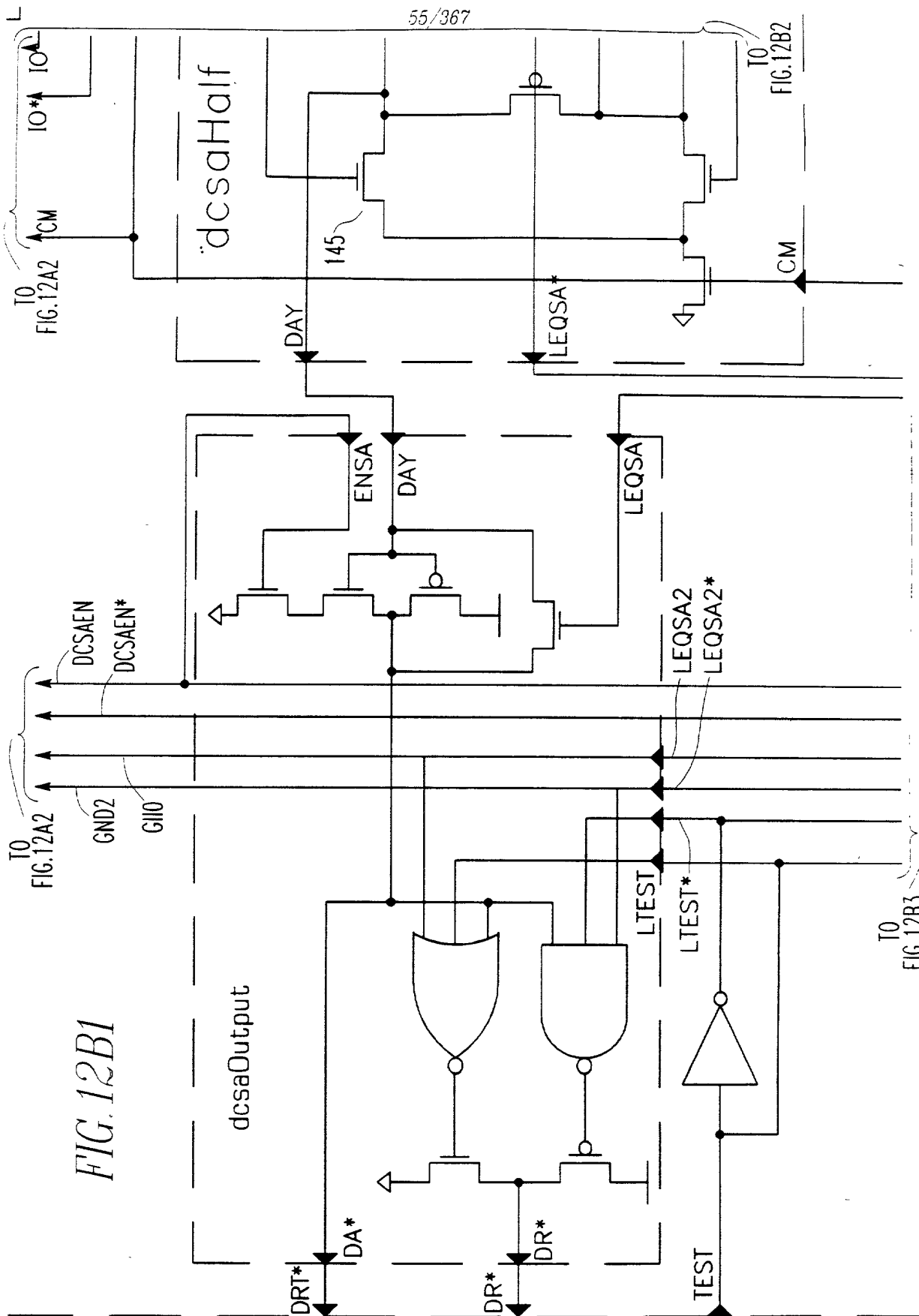


FIG. 11-4







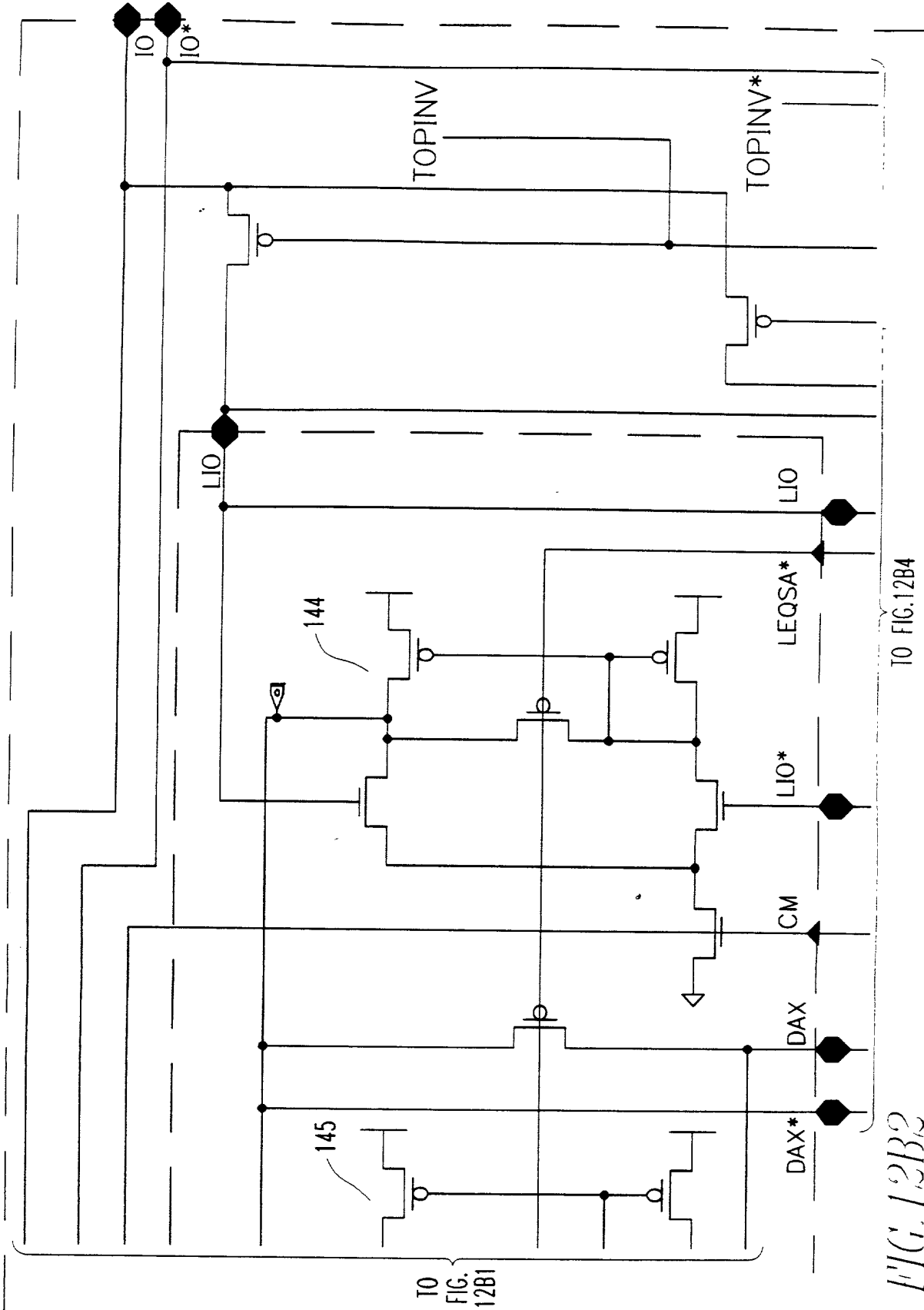
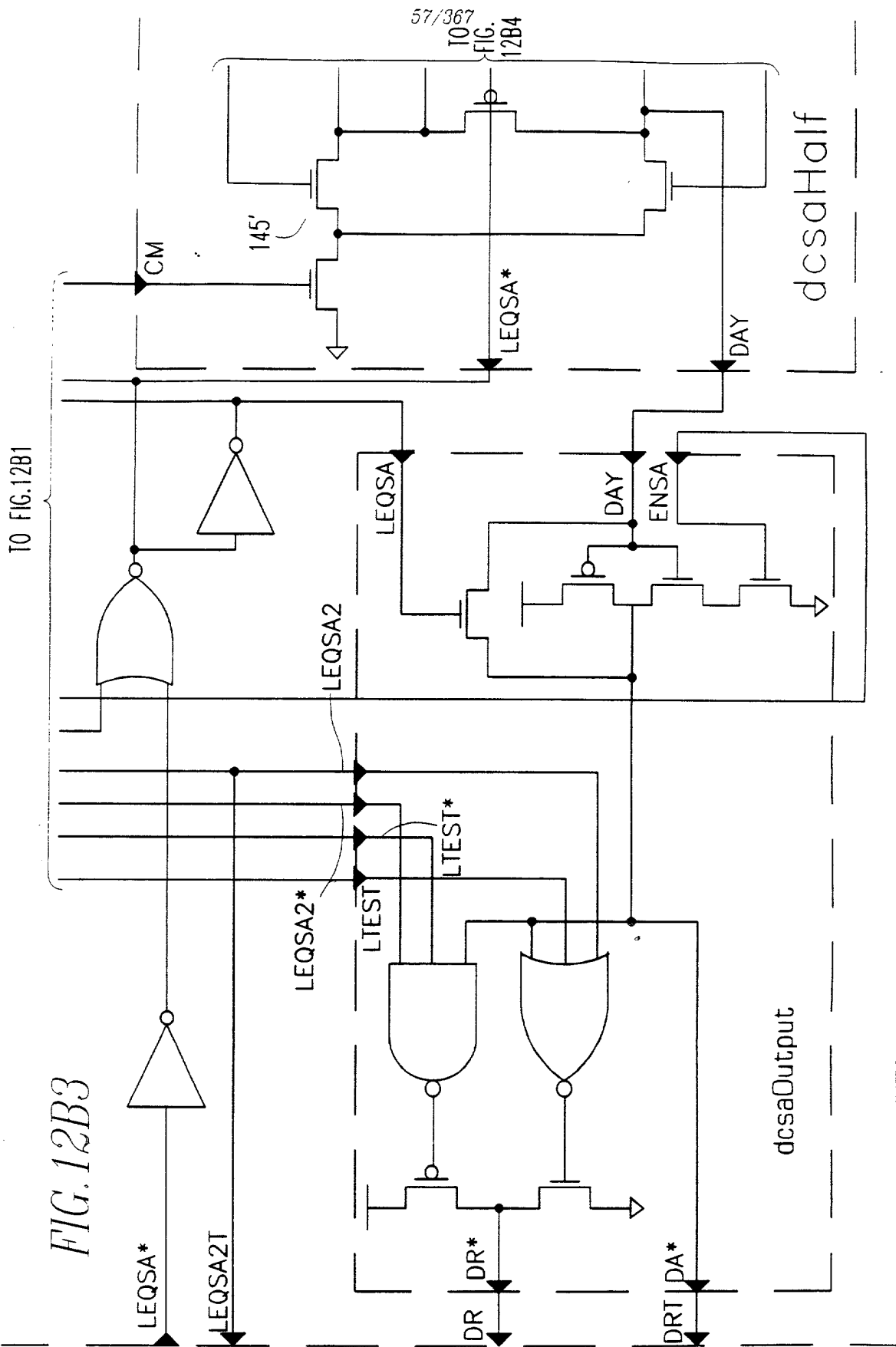
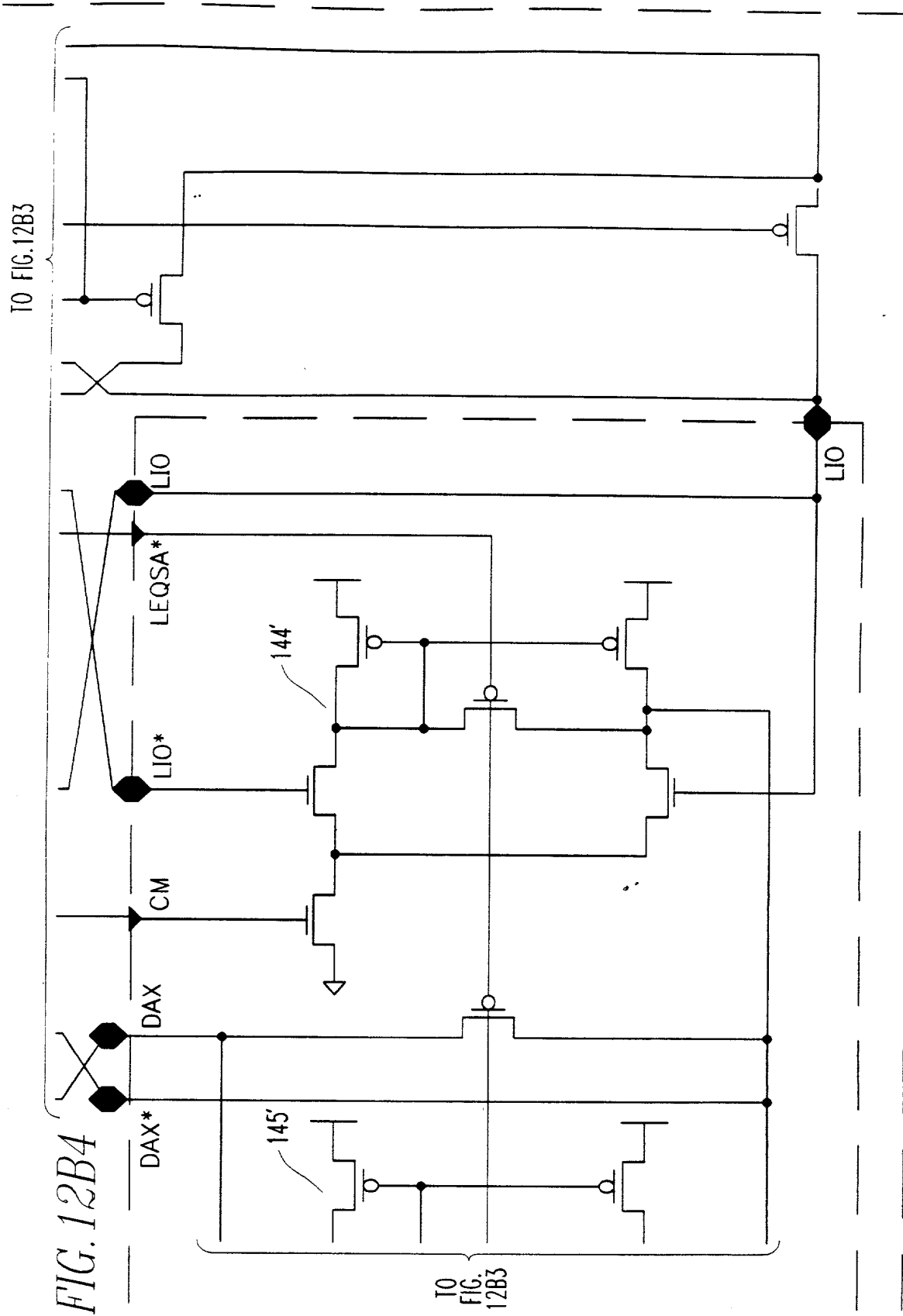


FIG. 12B2

FIG. 12B4









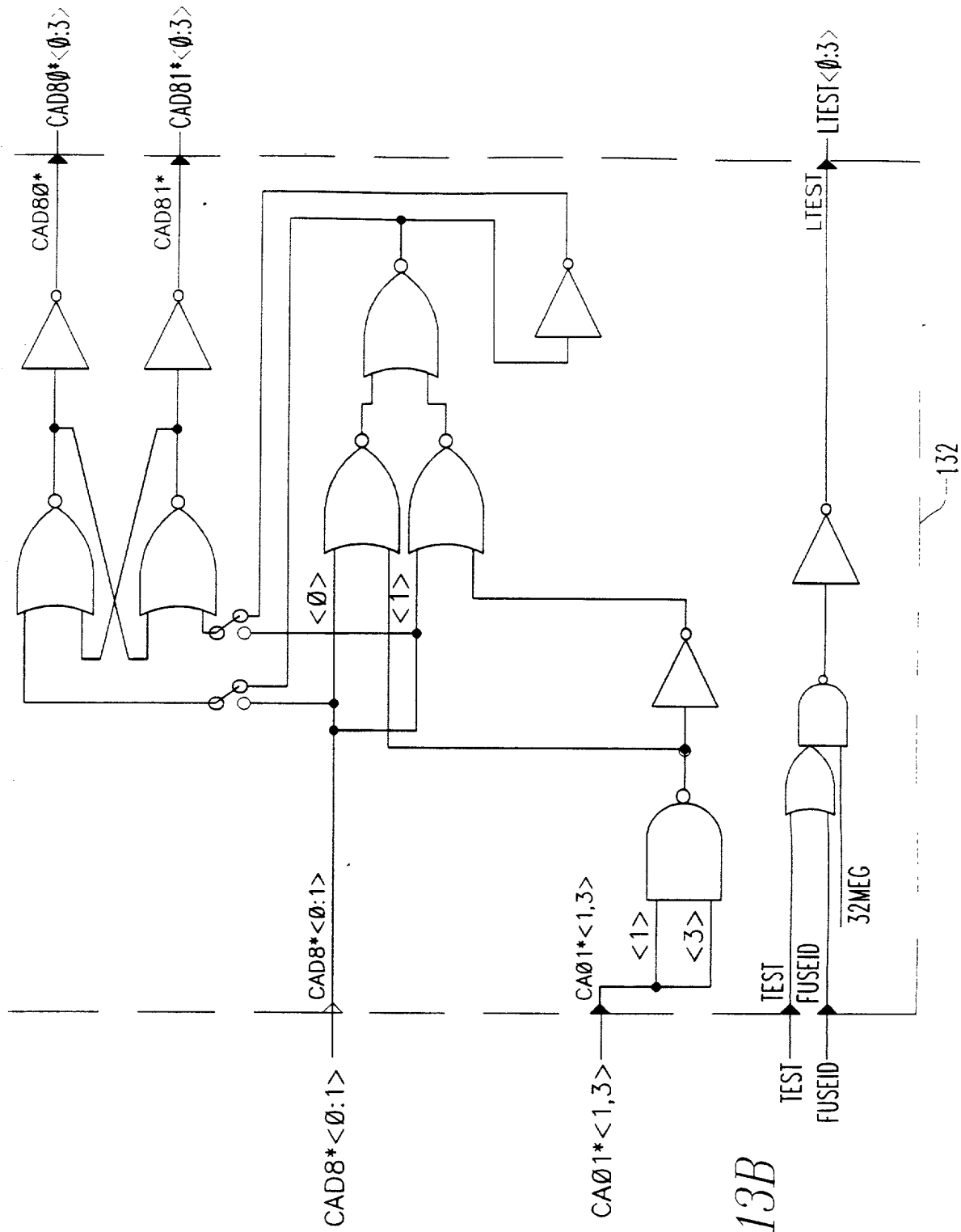


FIG. 13B

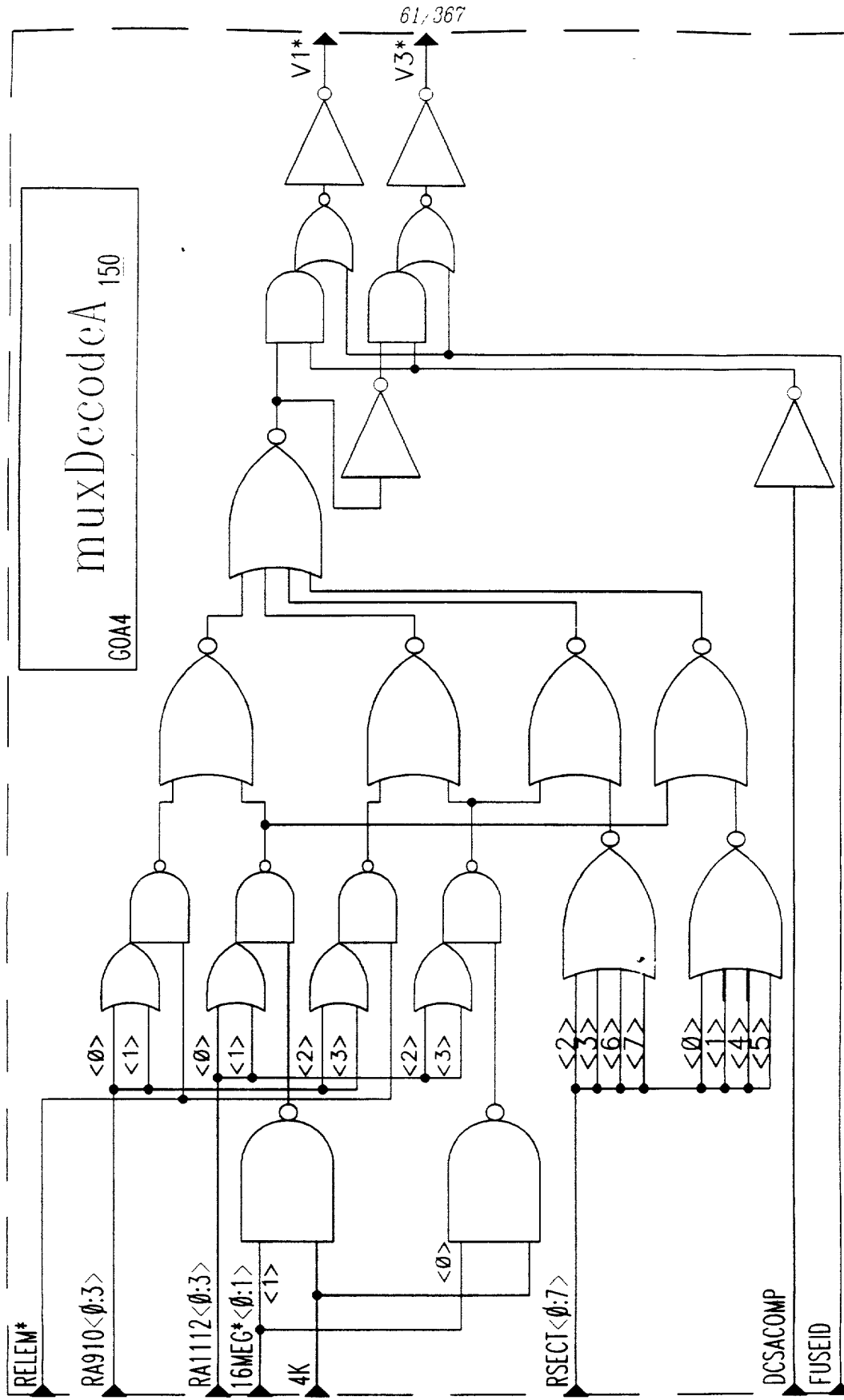


FIG. 11

FIG 15-1

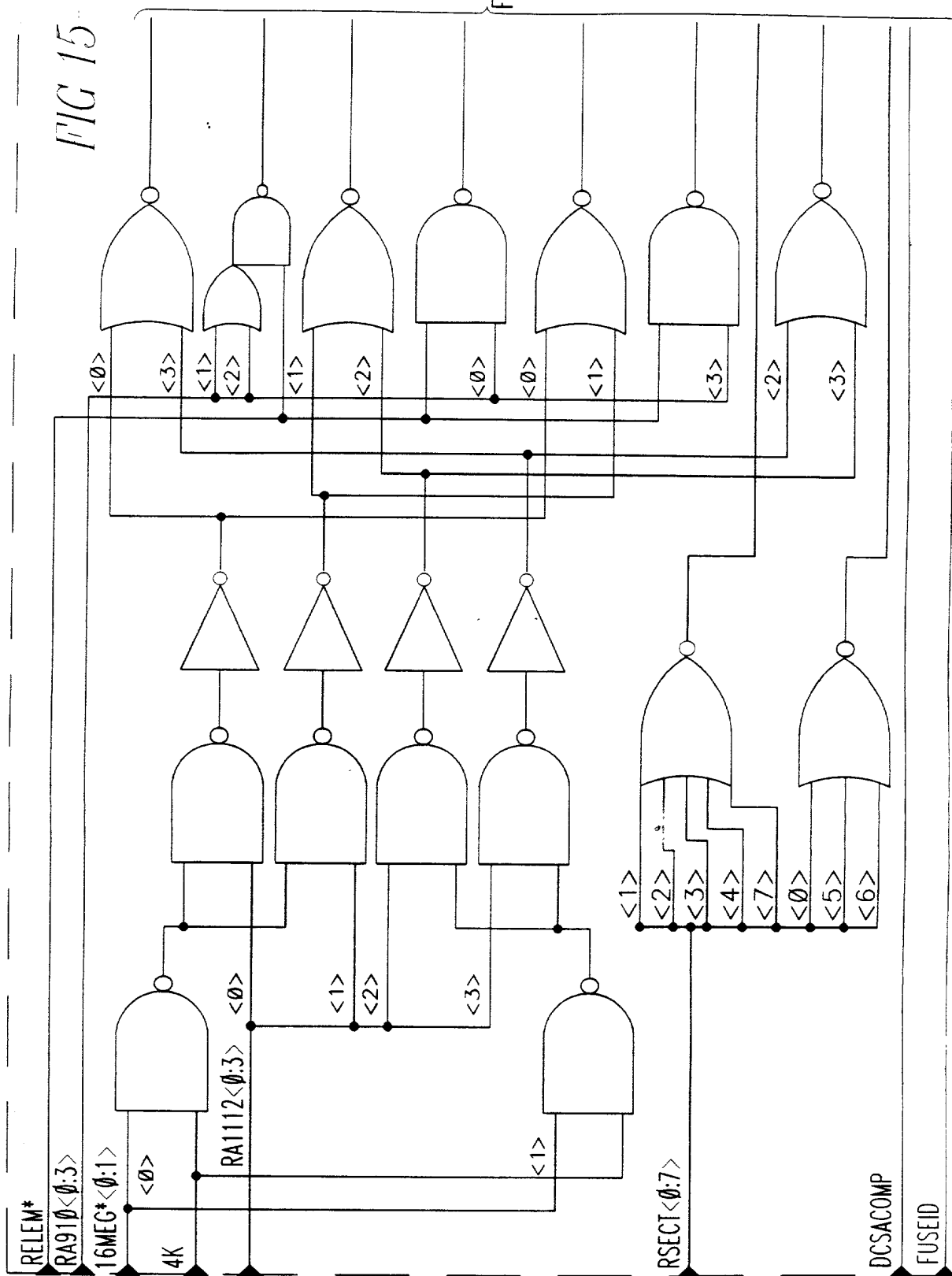


FIG. 15-2

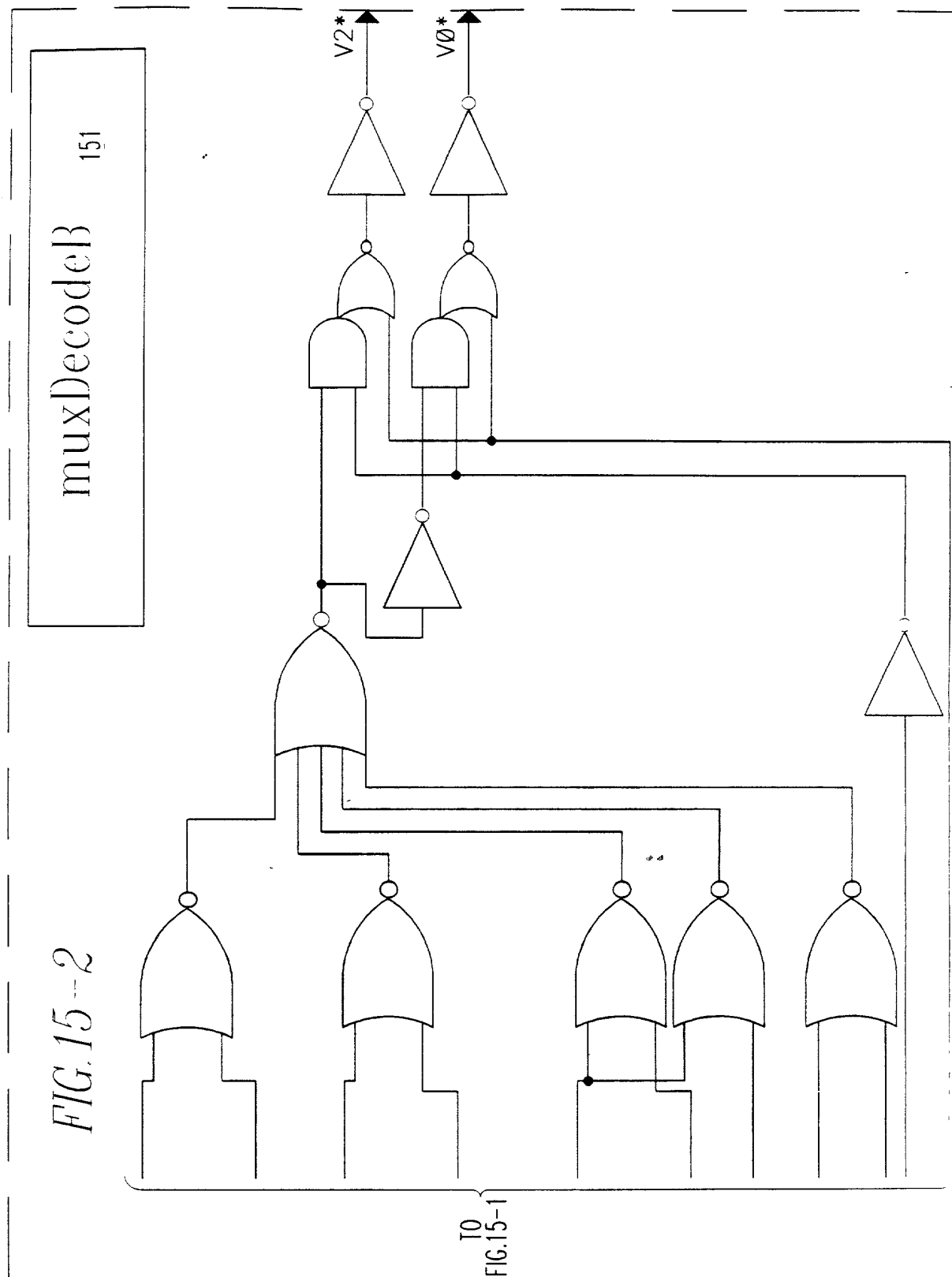
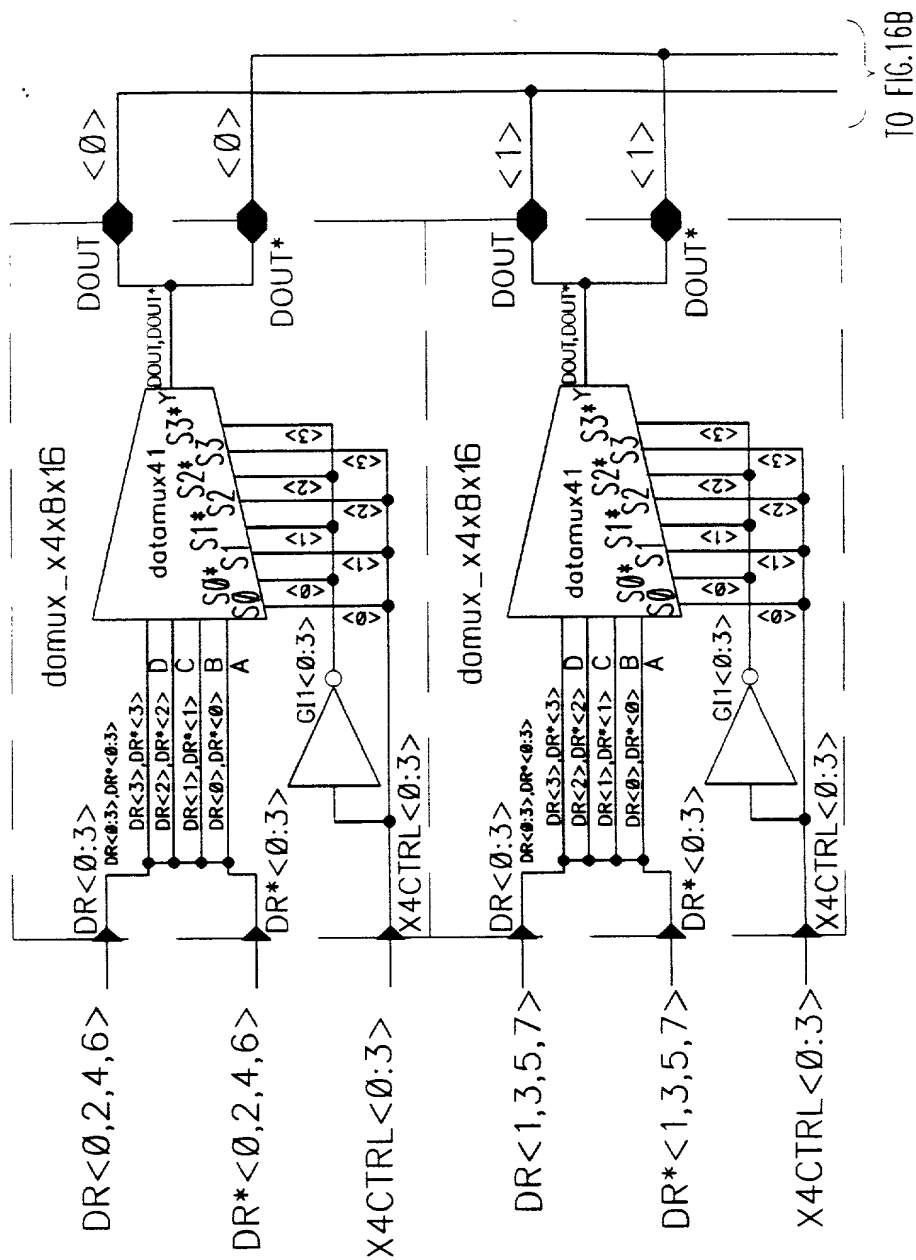
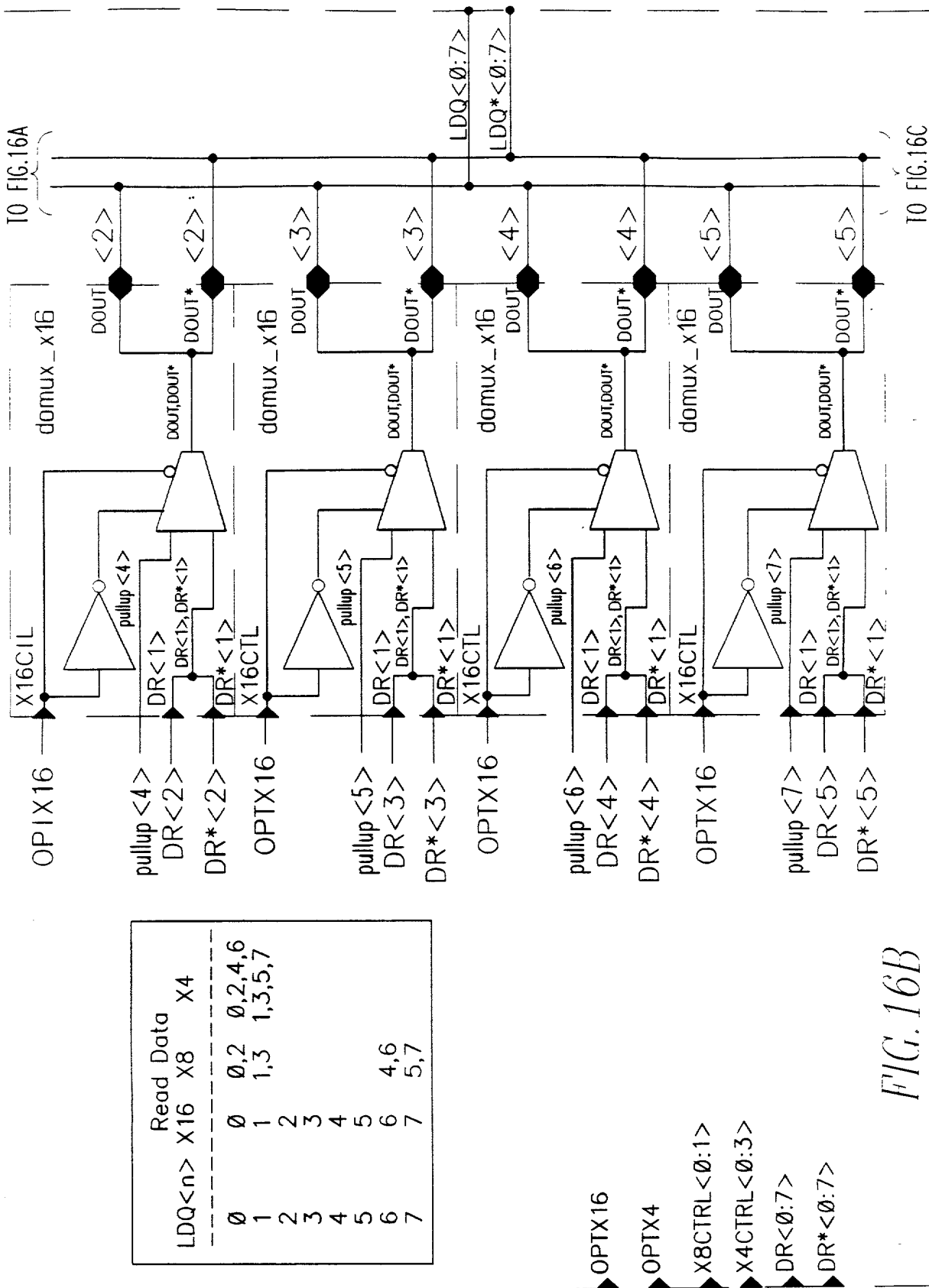
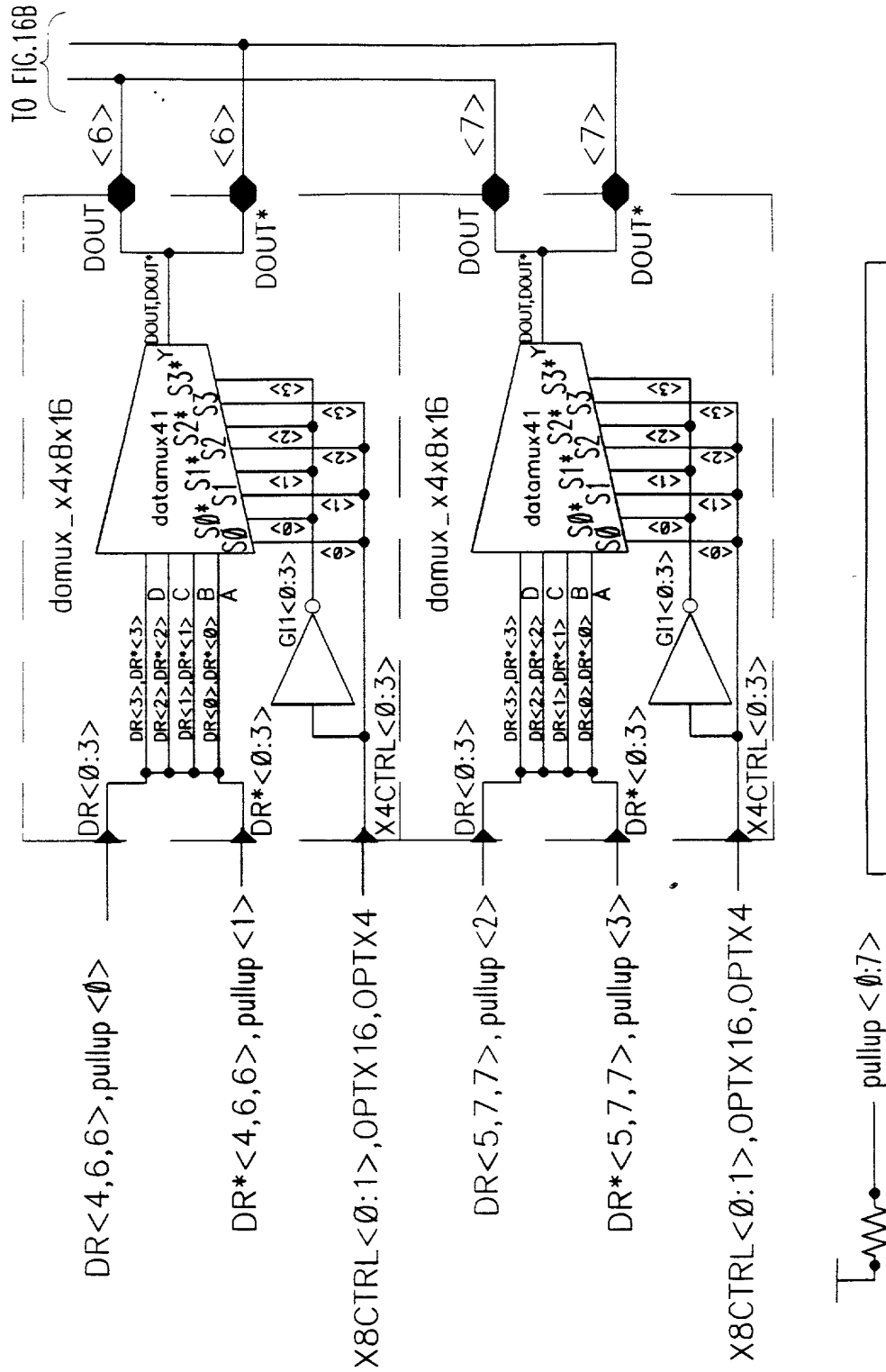


FIG. 16A









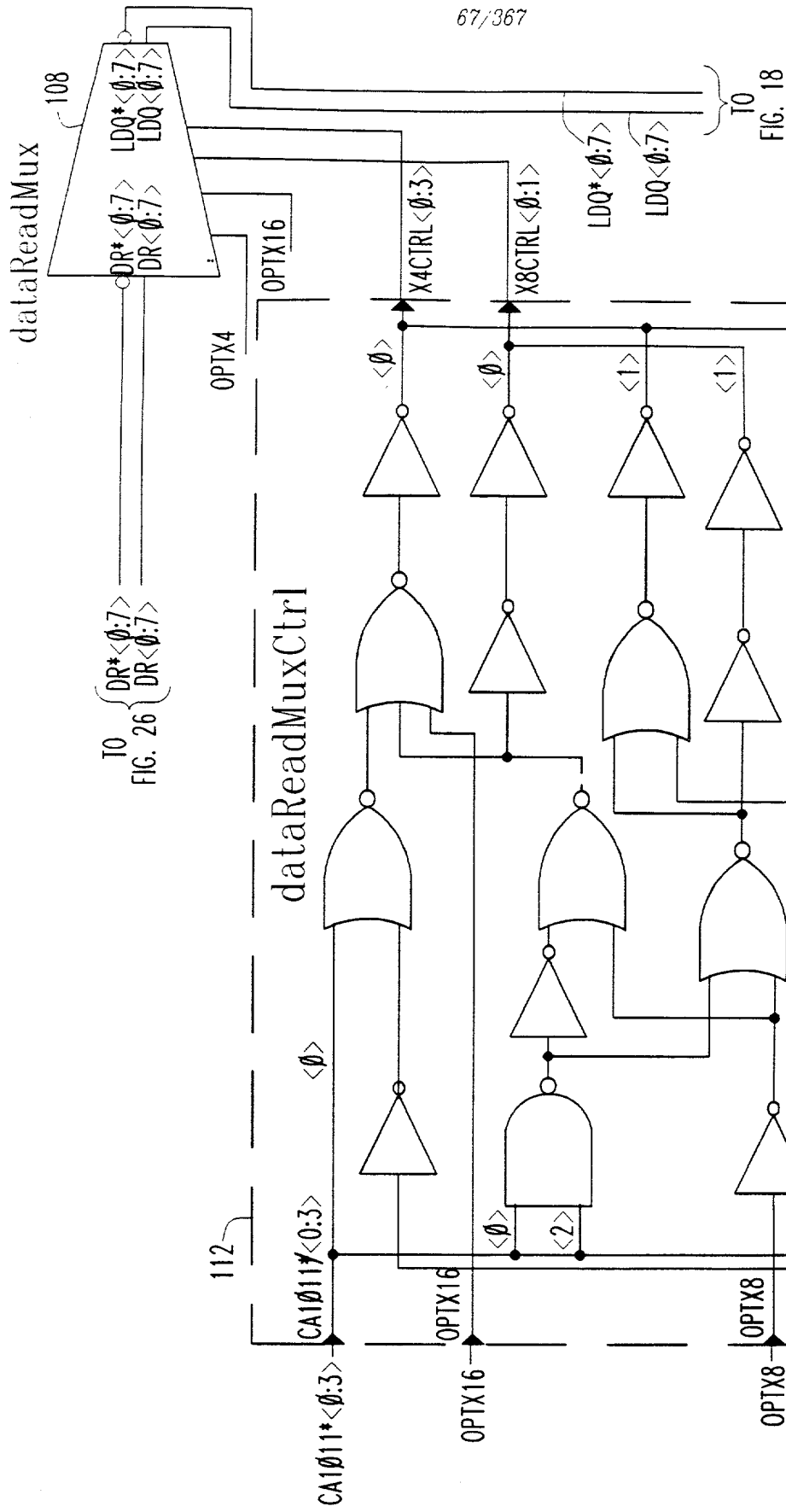
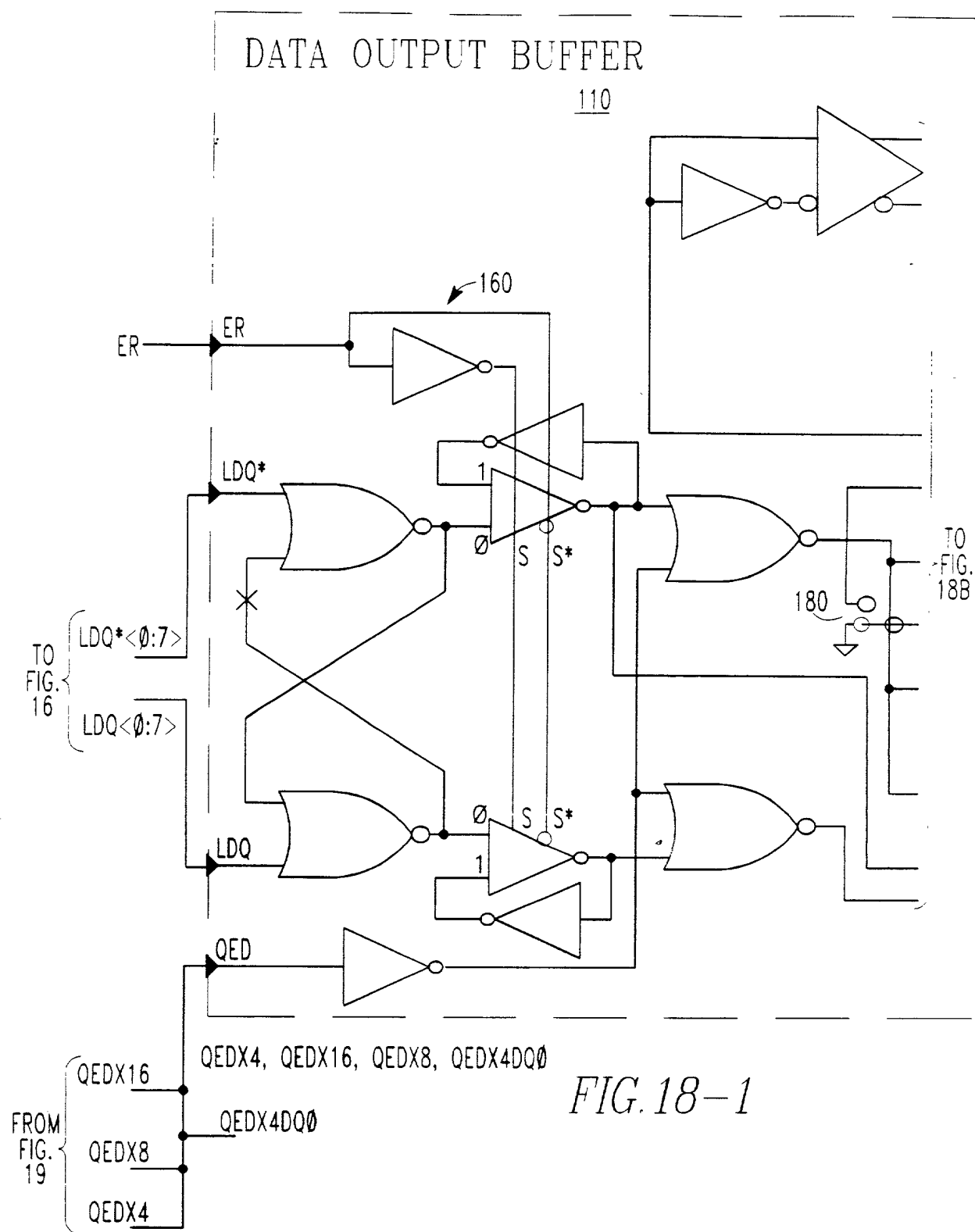


FIG. 17



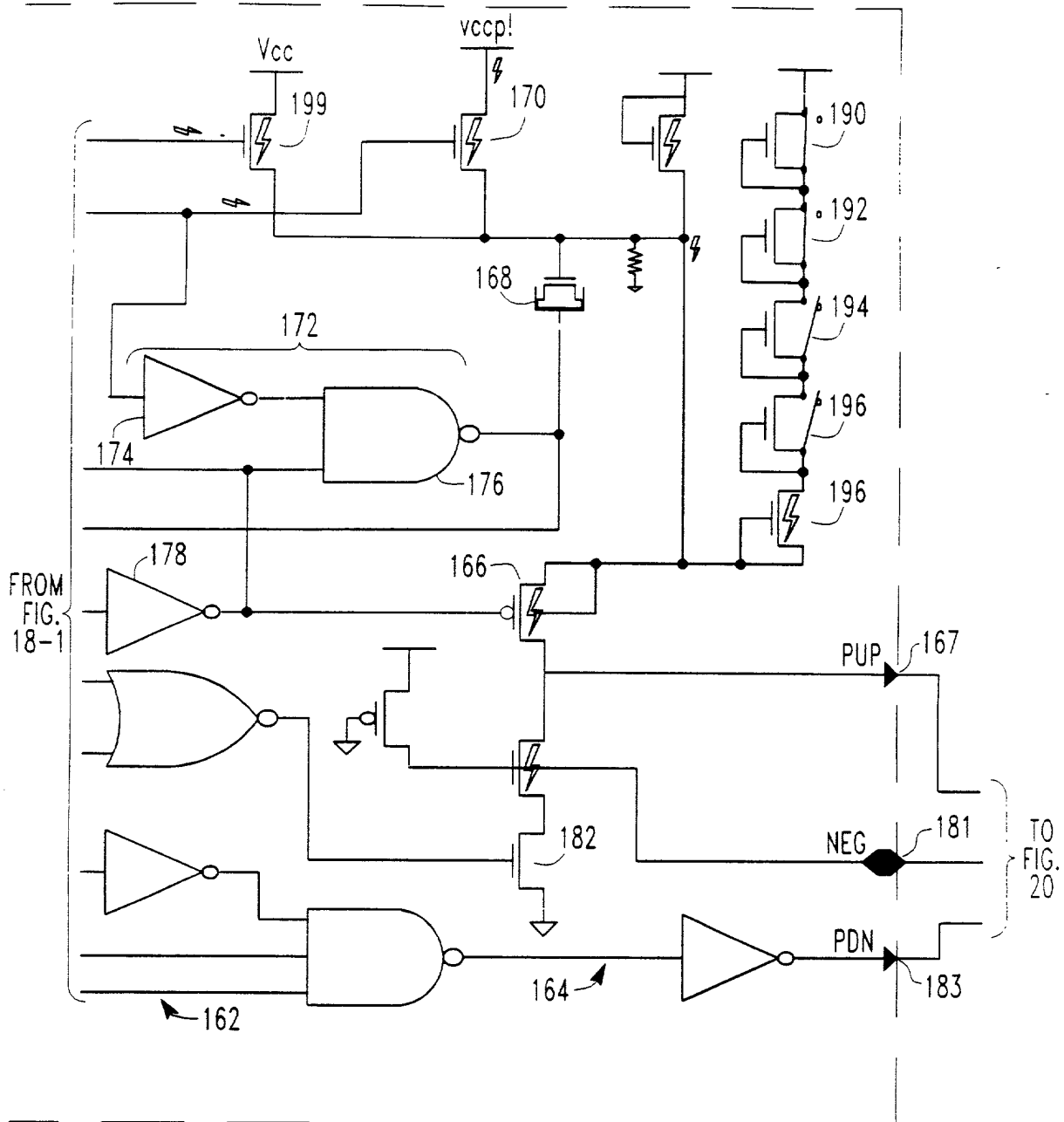


FIG. 18-2

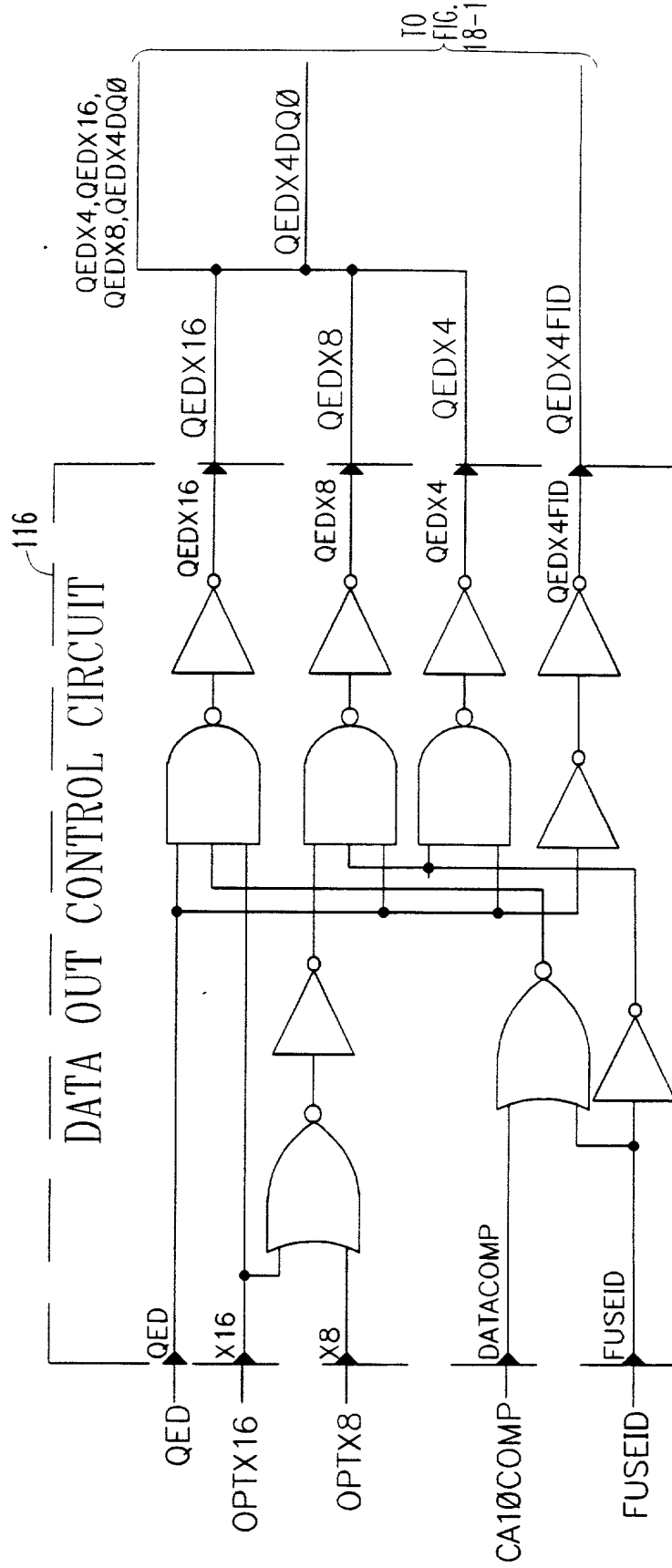


FIG. 19

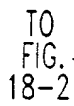


FIG. 20

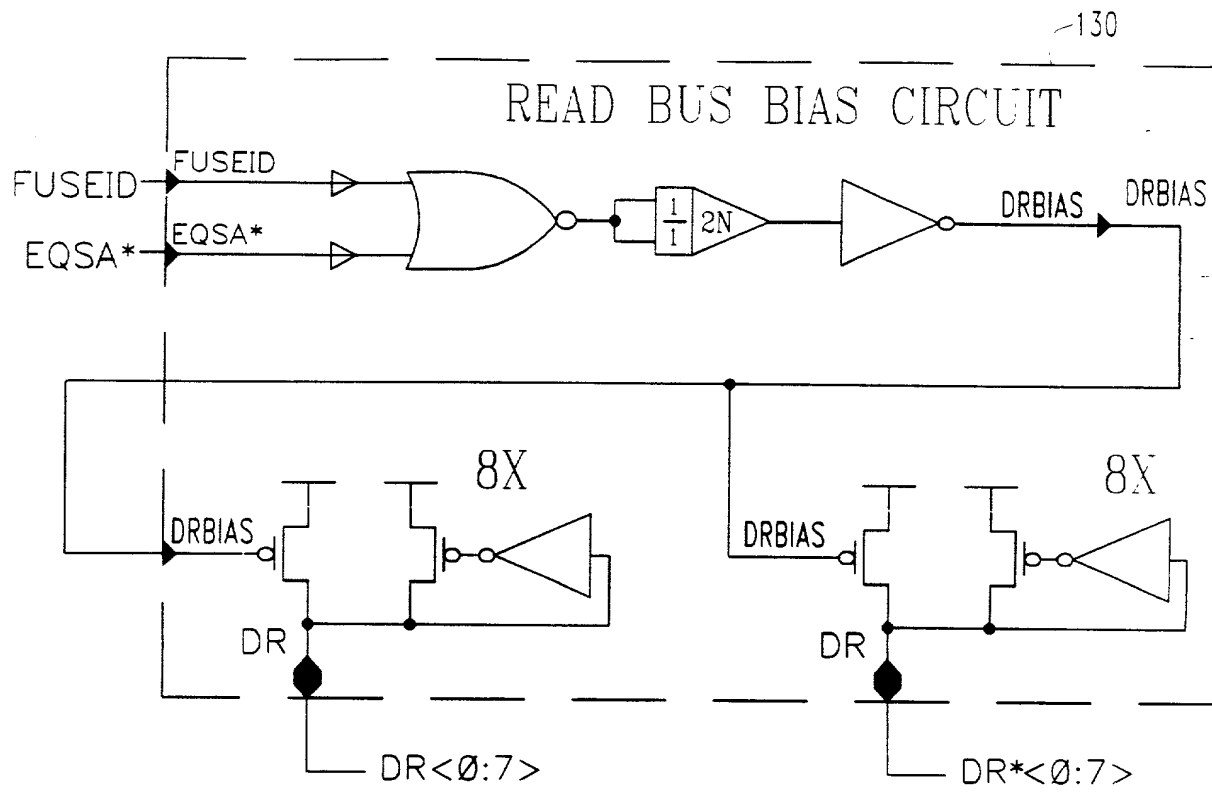


FIG. 21



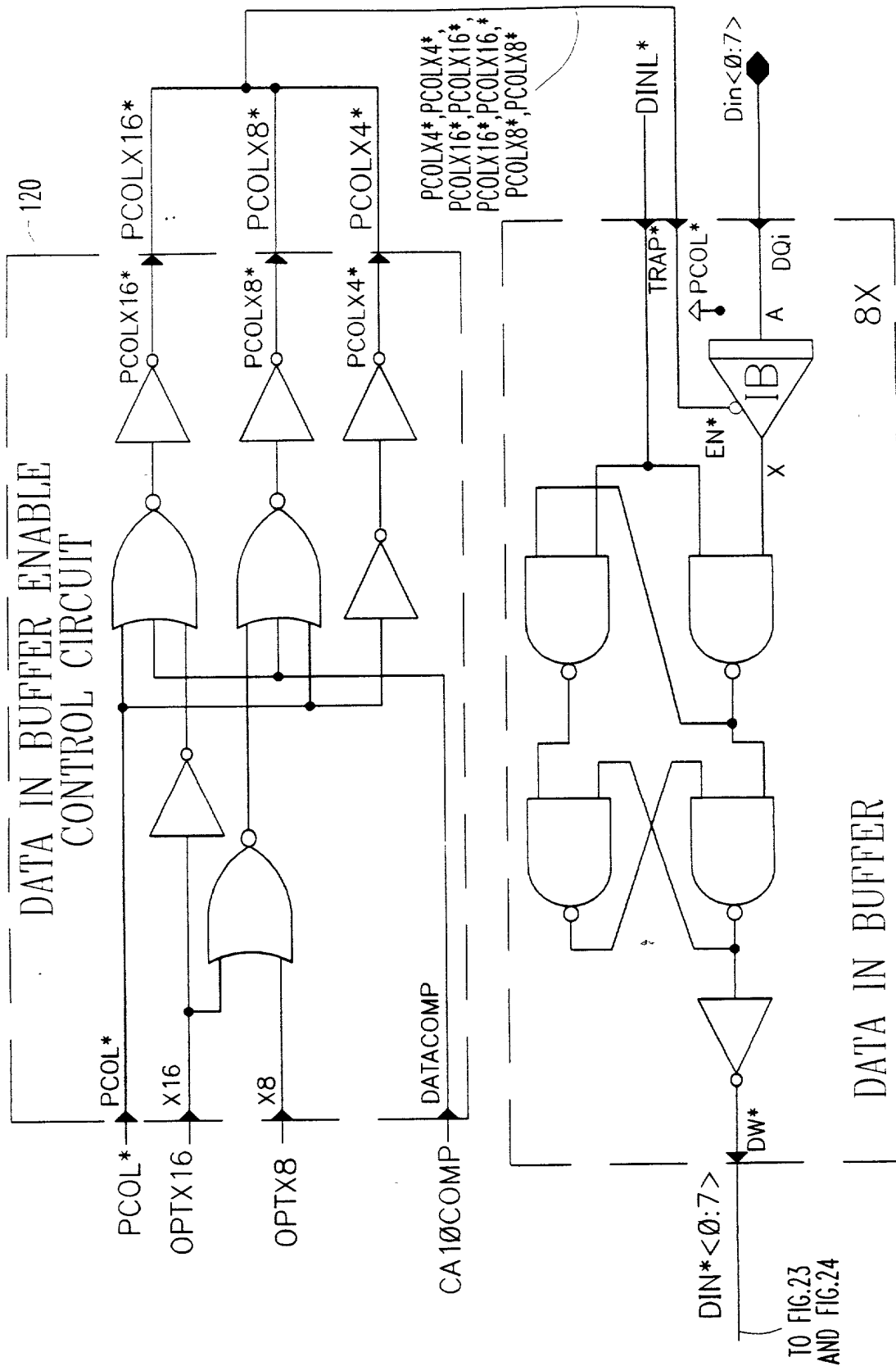
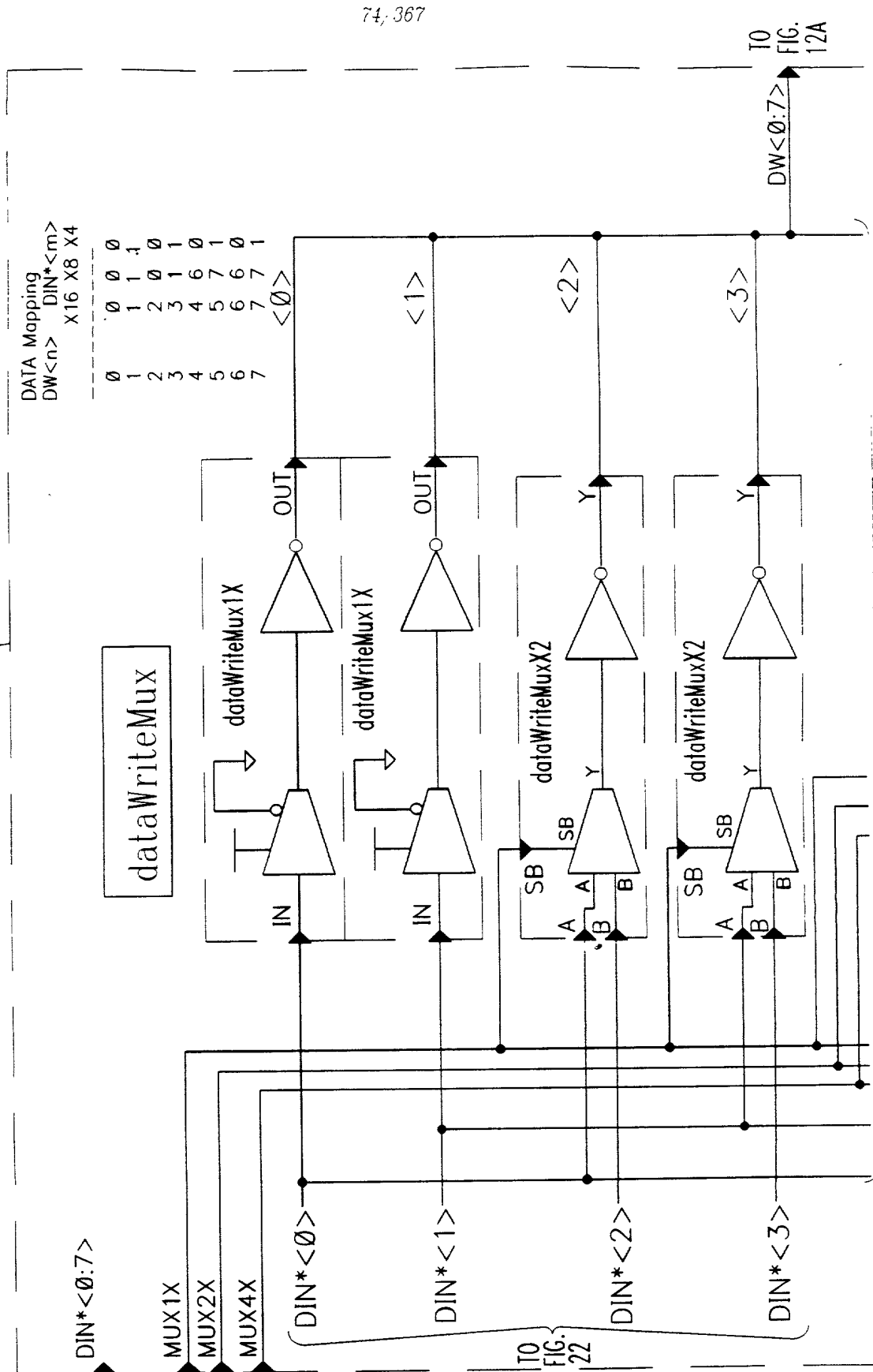


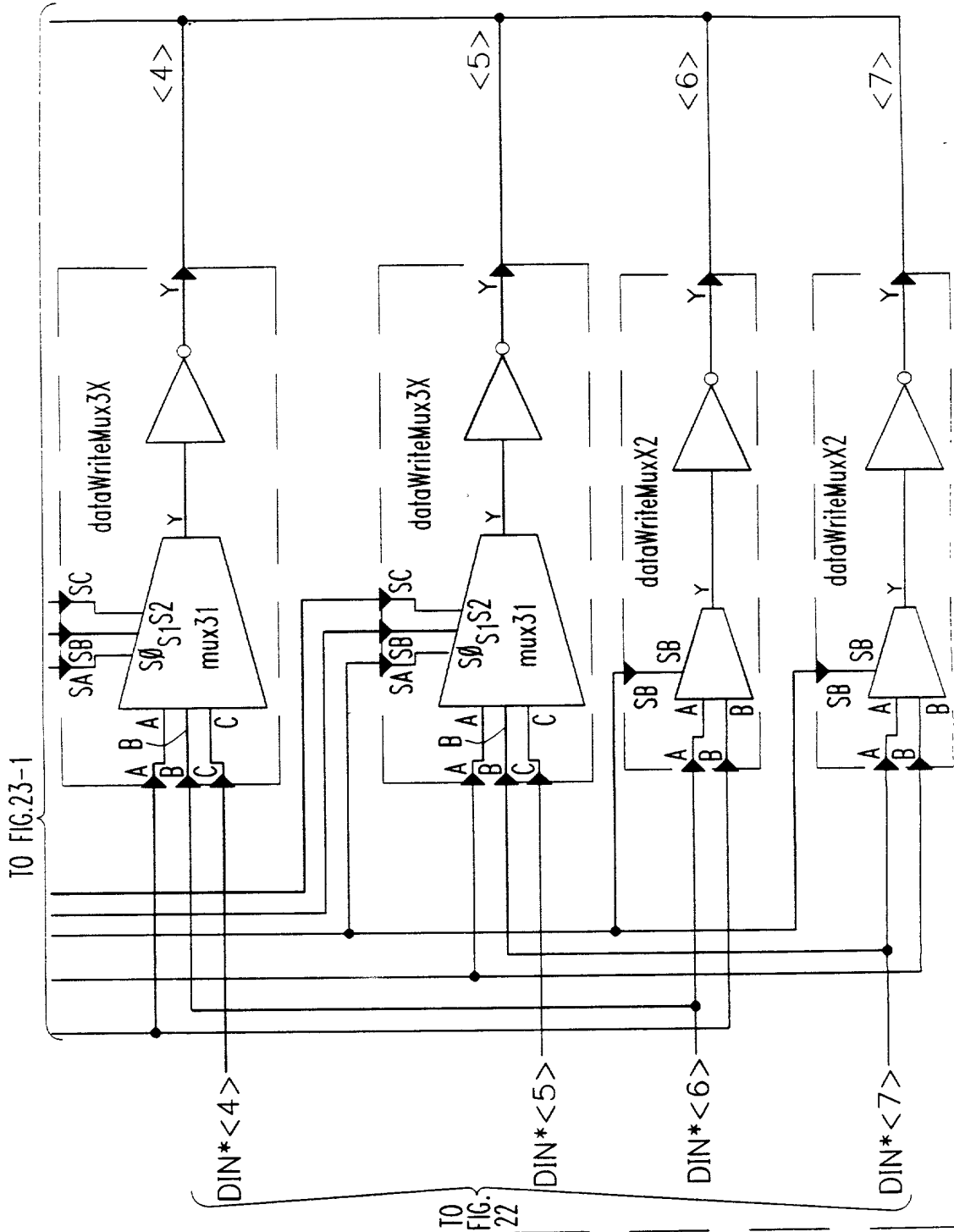
FIG. 23-1

122



TO FIG. 23-2

TO FIG. 12A



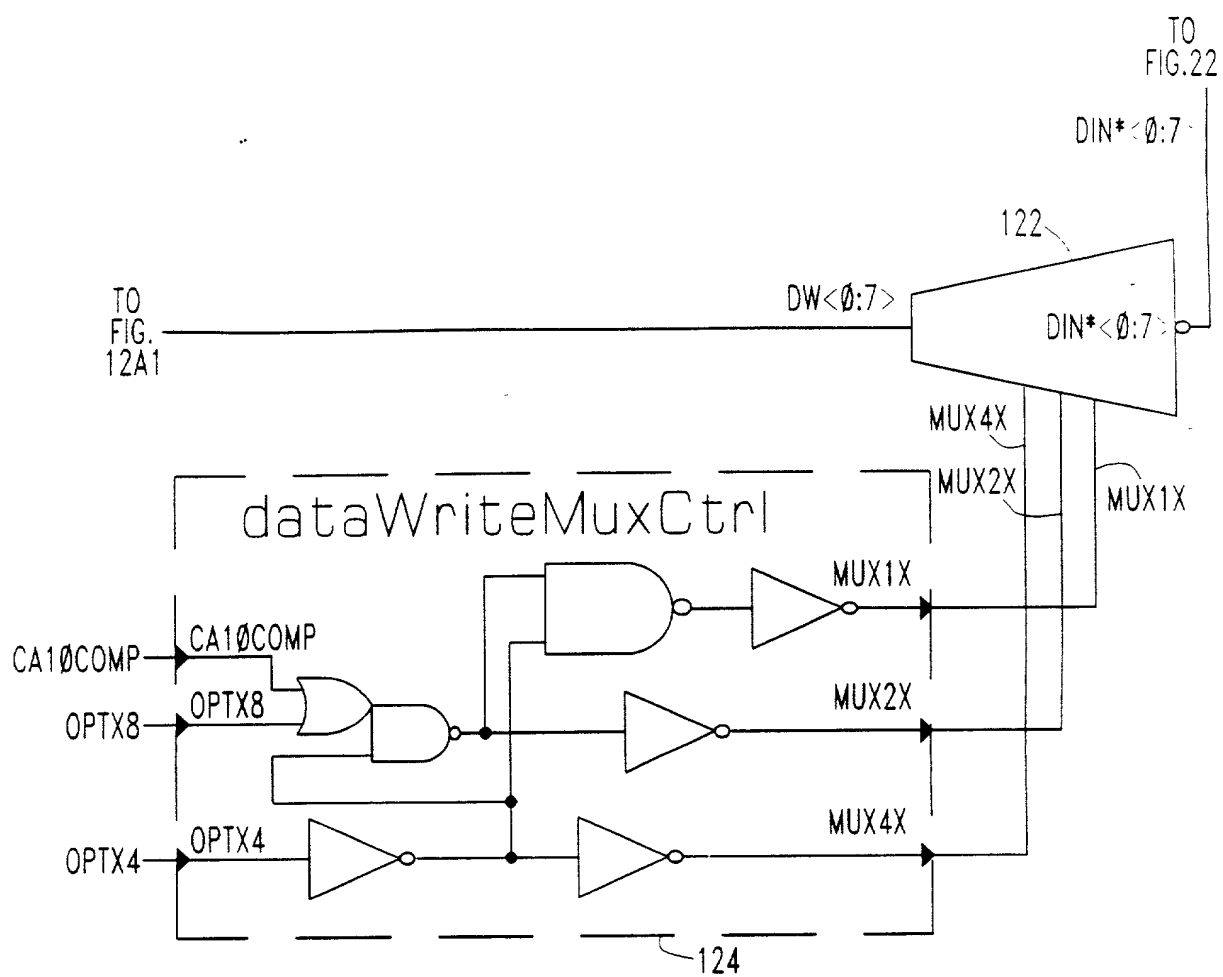


FIG. 24

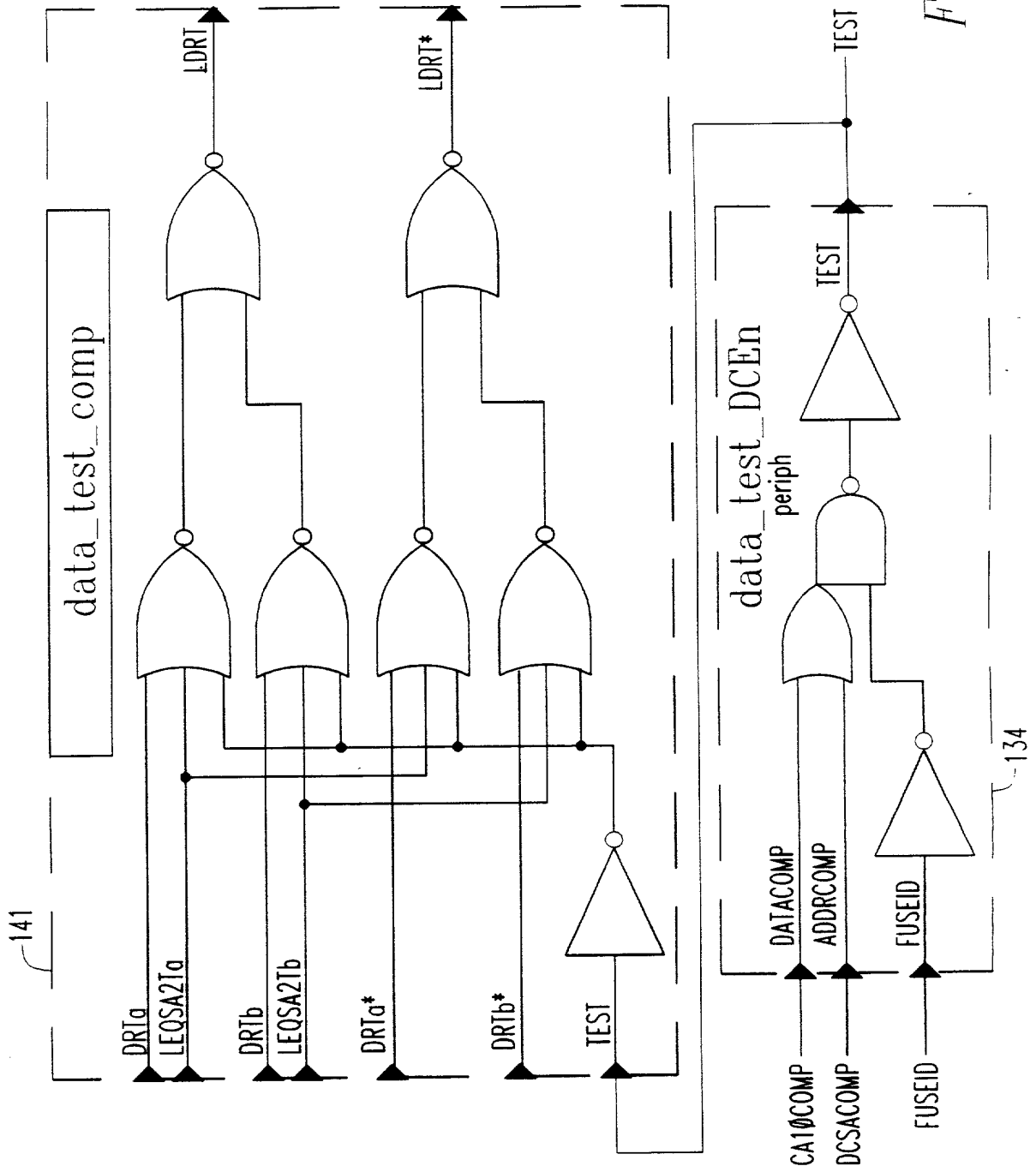
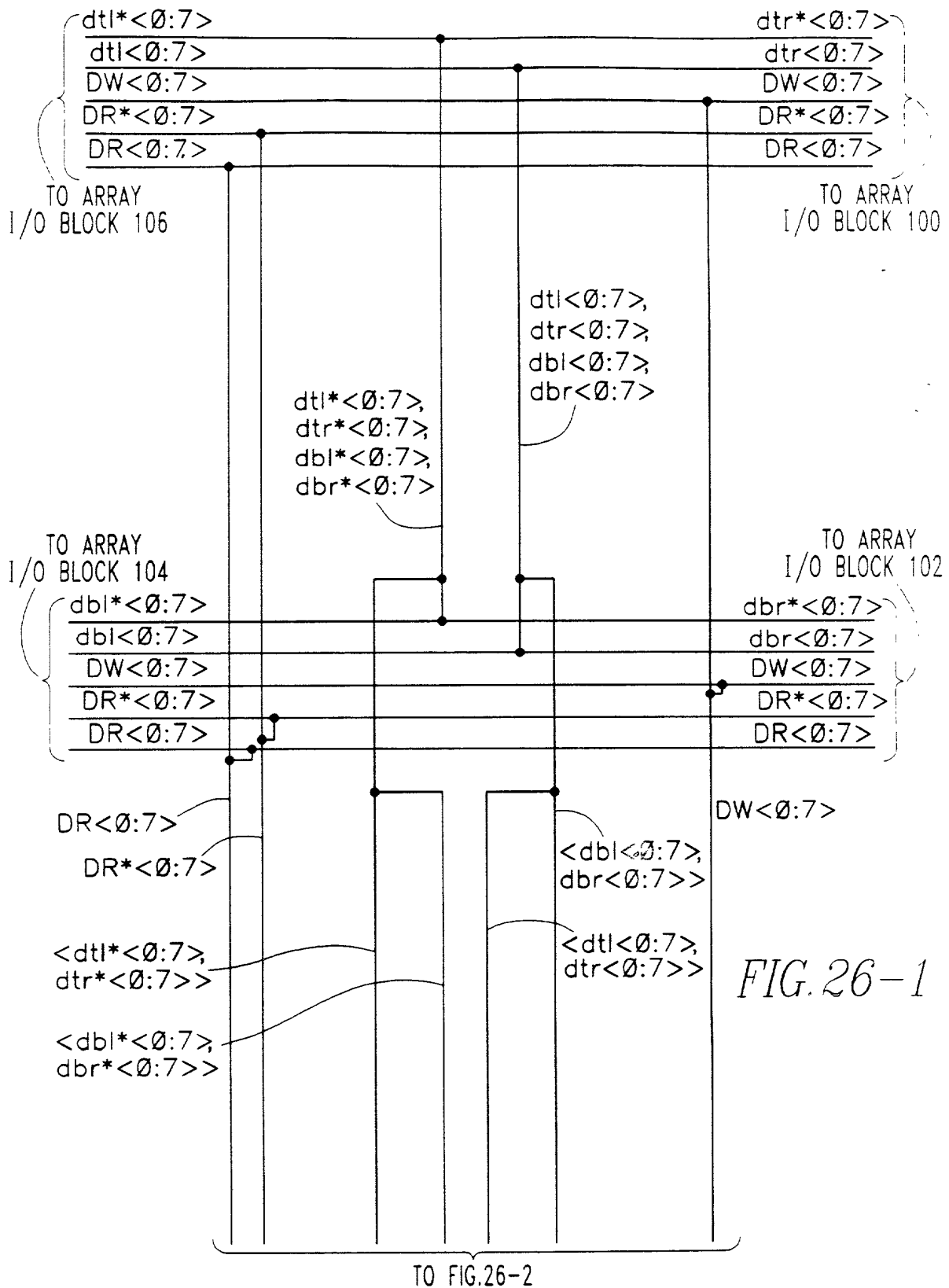


FIG. 25



FROM FIG.26-1

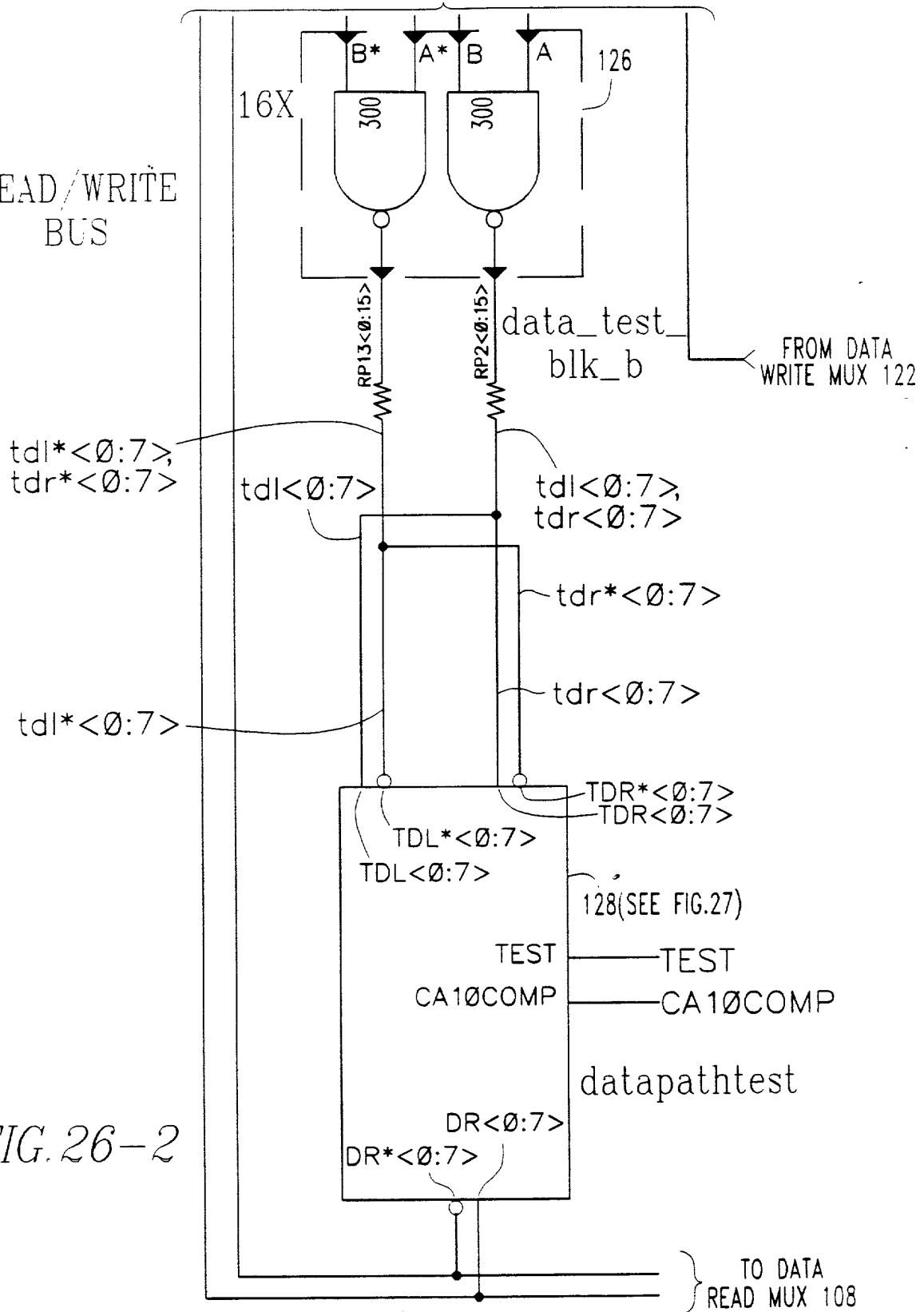
READ/WRITE  
BUS

FIG.26-2

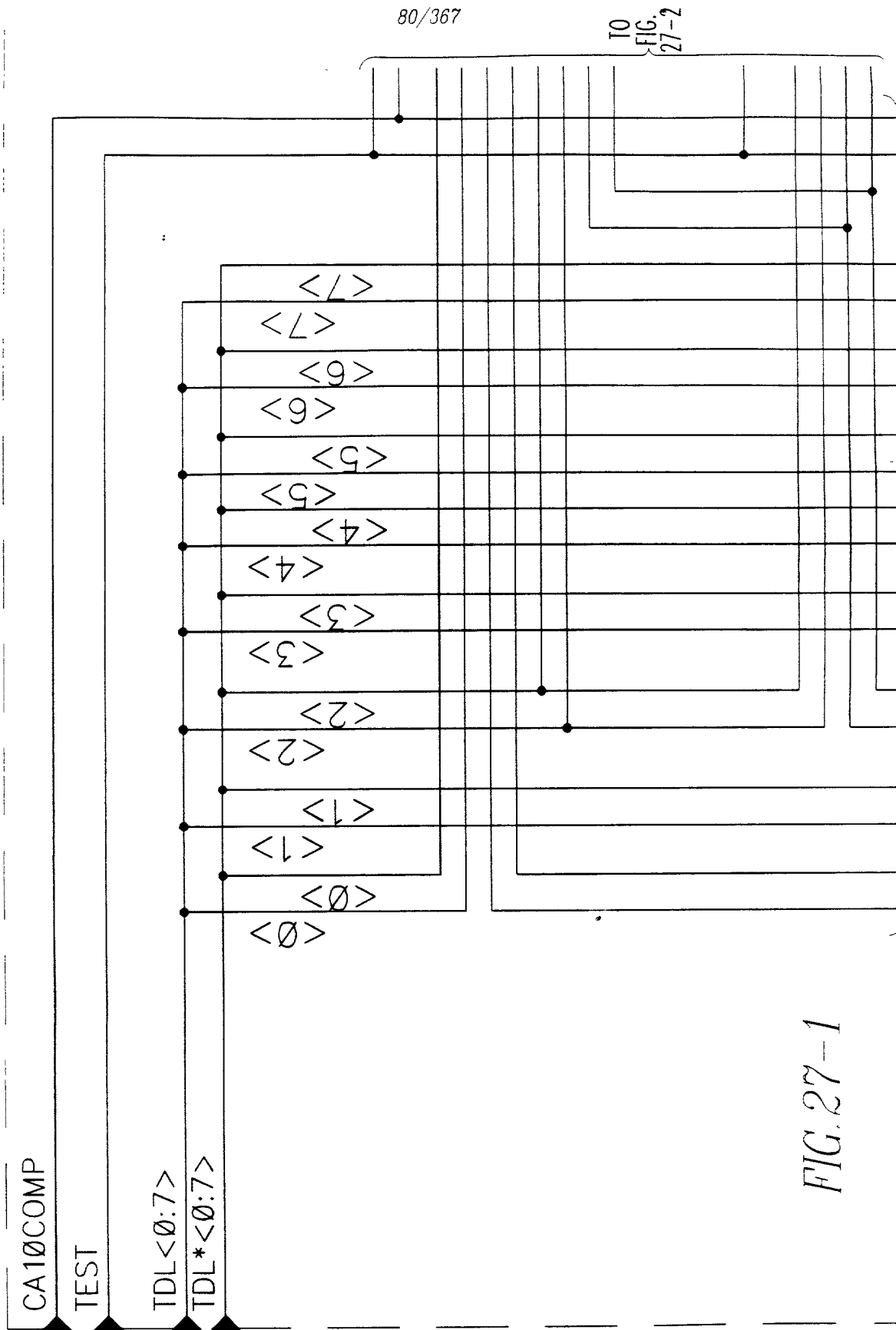


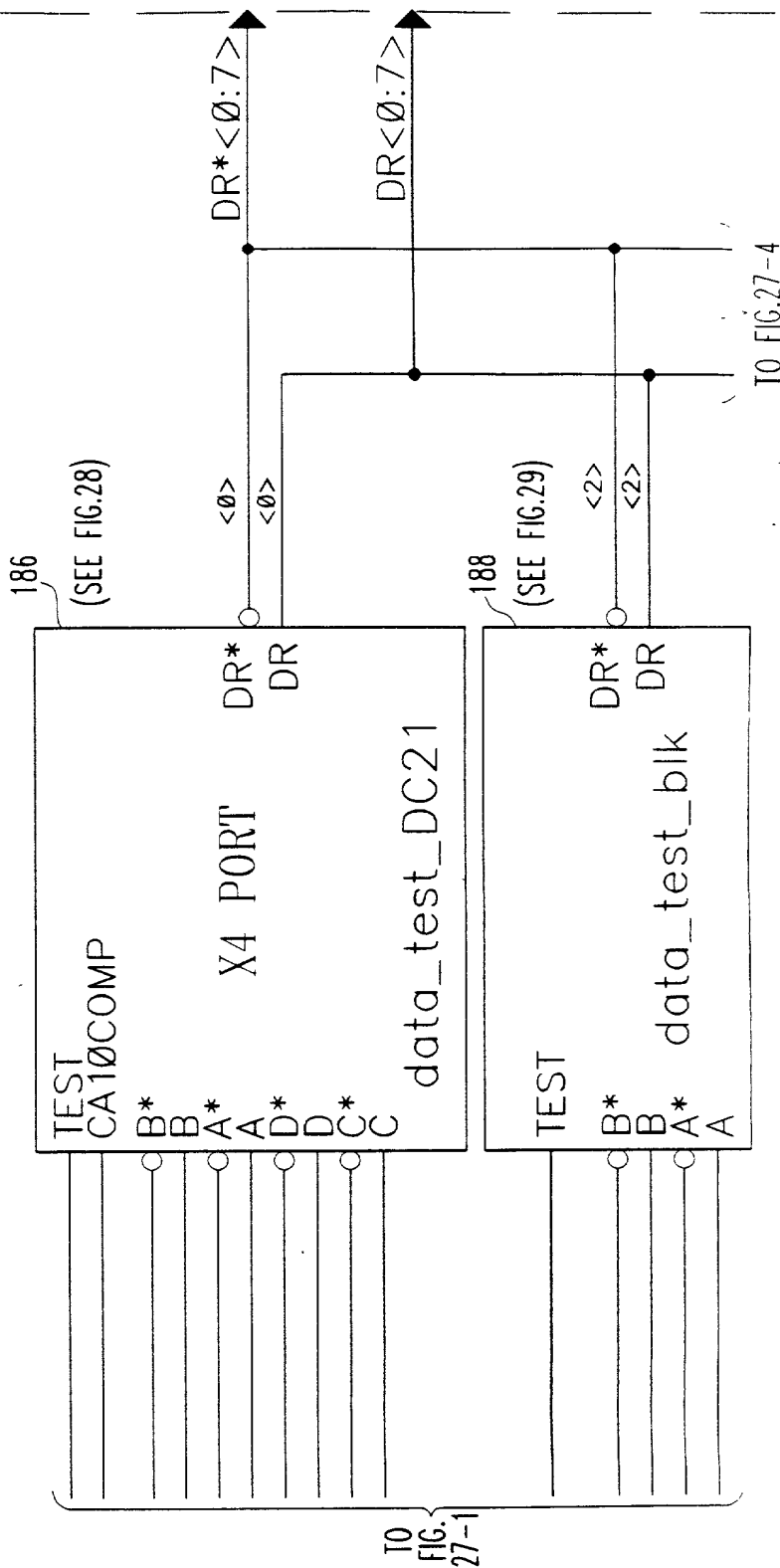
FIG. 27-1

TO FIG. 27-3

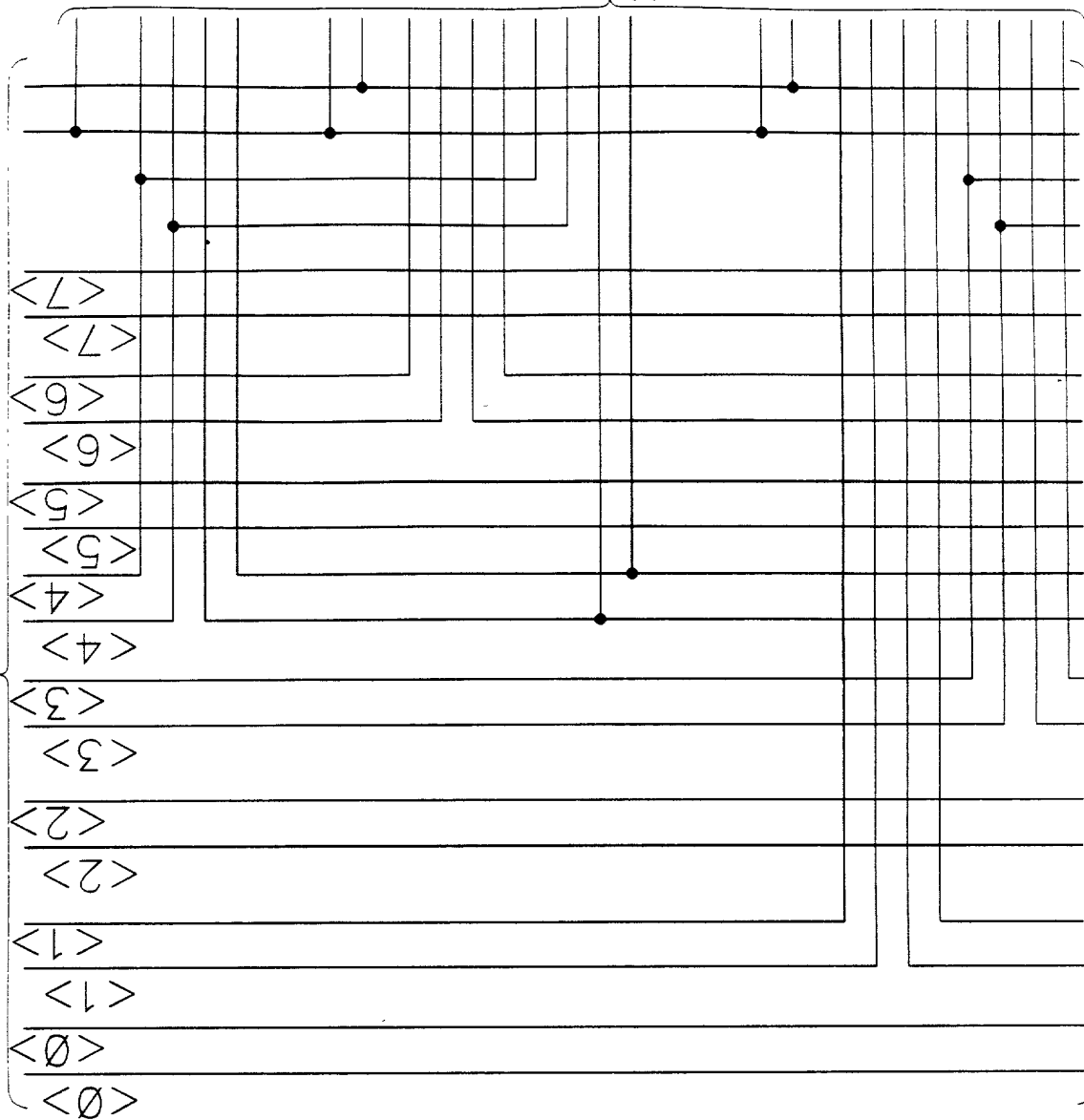


FIG. 27-2

dataPathTest  
Block 128



TO FIG. 27-1

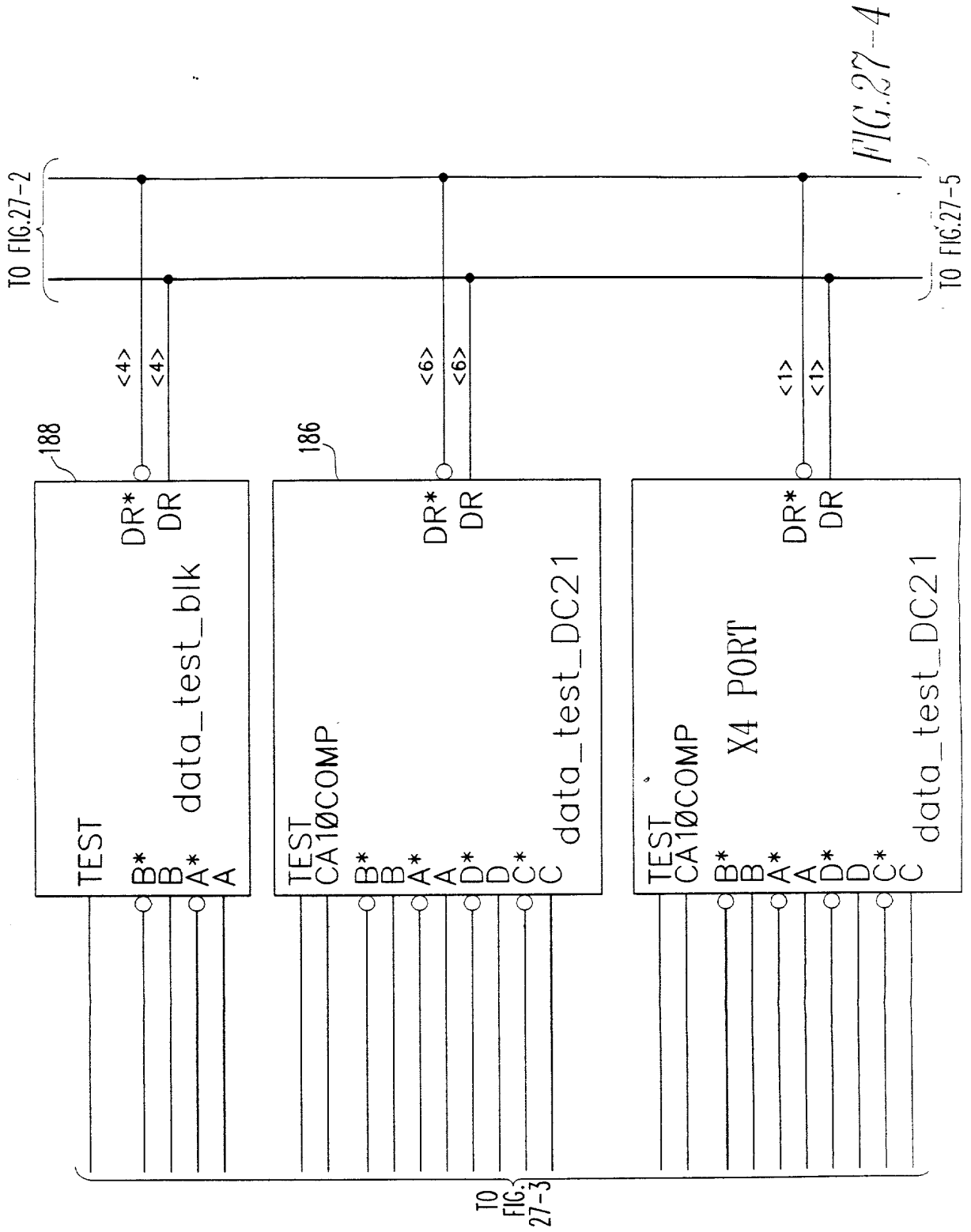


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TO FIG. 27-4

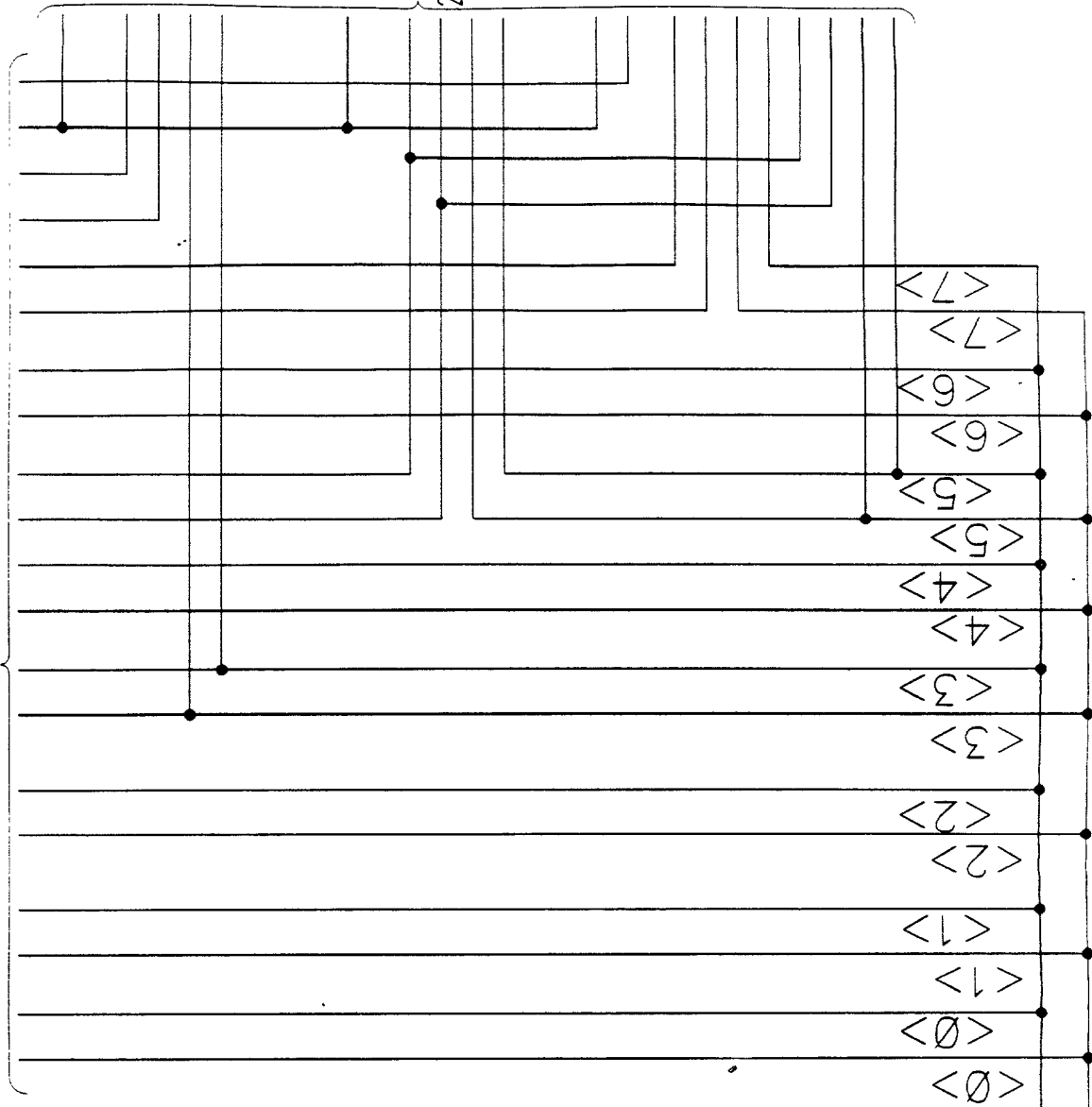
FIG. 27-3

TO FIG. 27-5

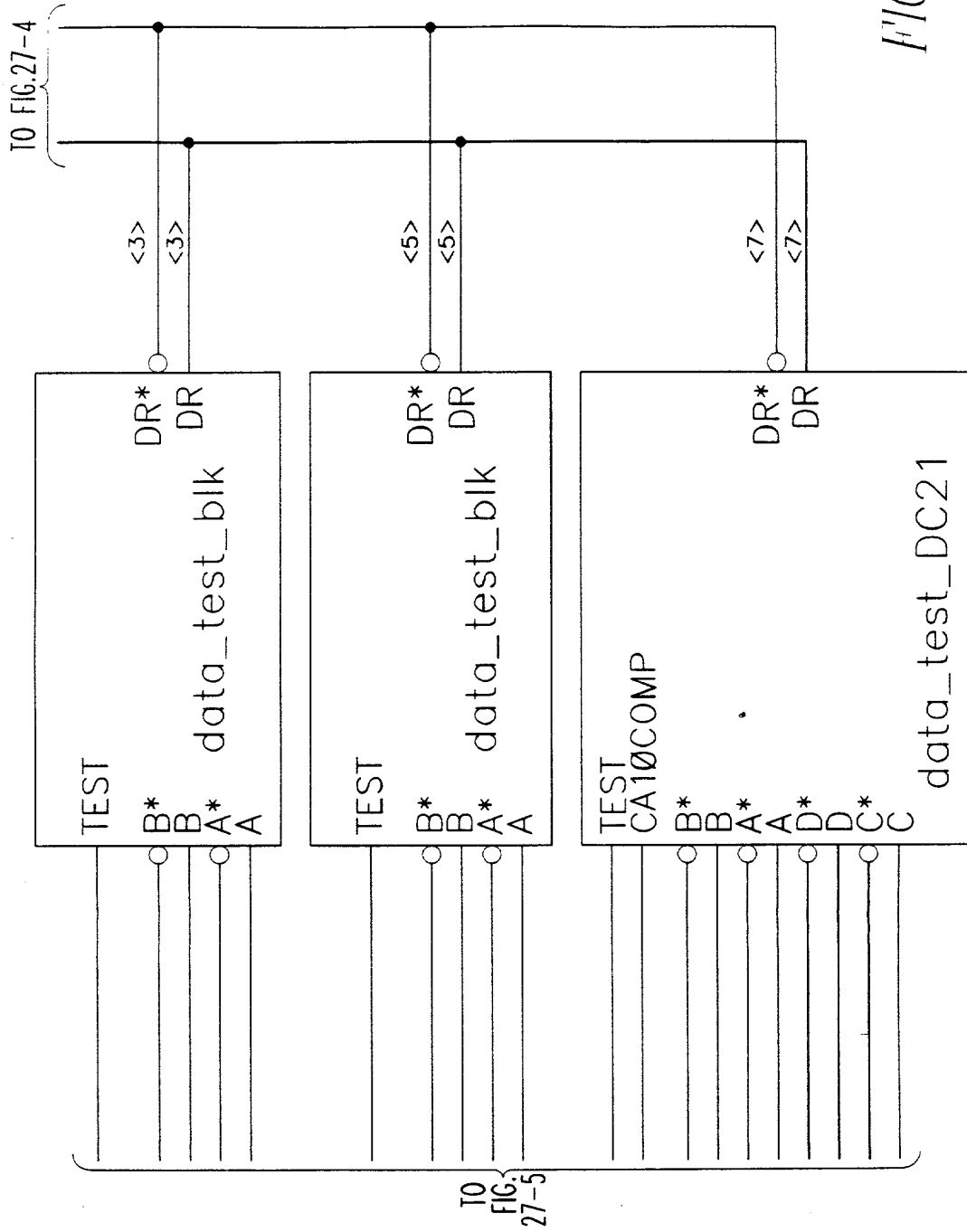
FIG. 27-3



10 FIG. 27-3



TDR<0:7>  
TDR\*<0:7>



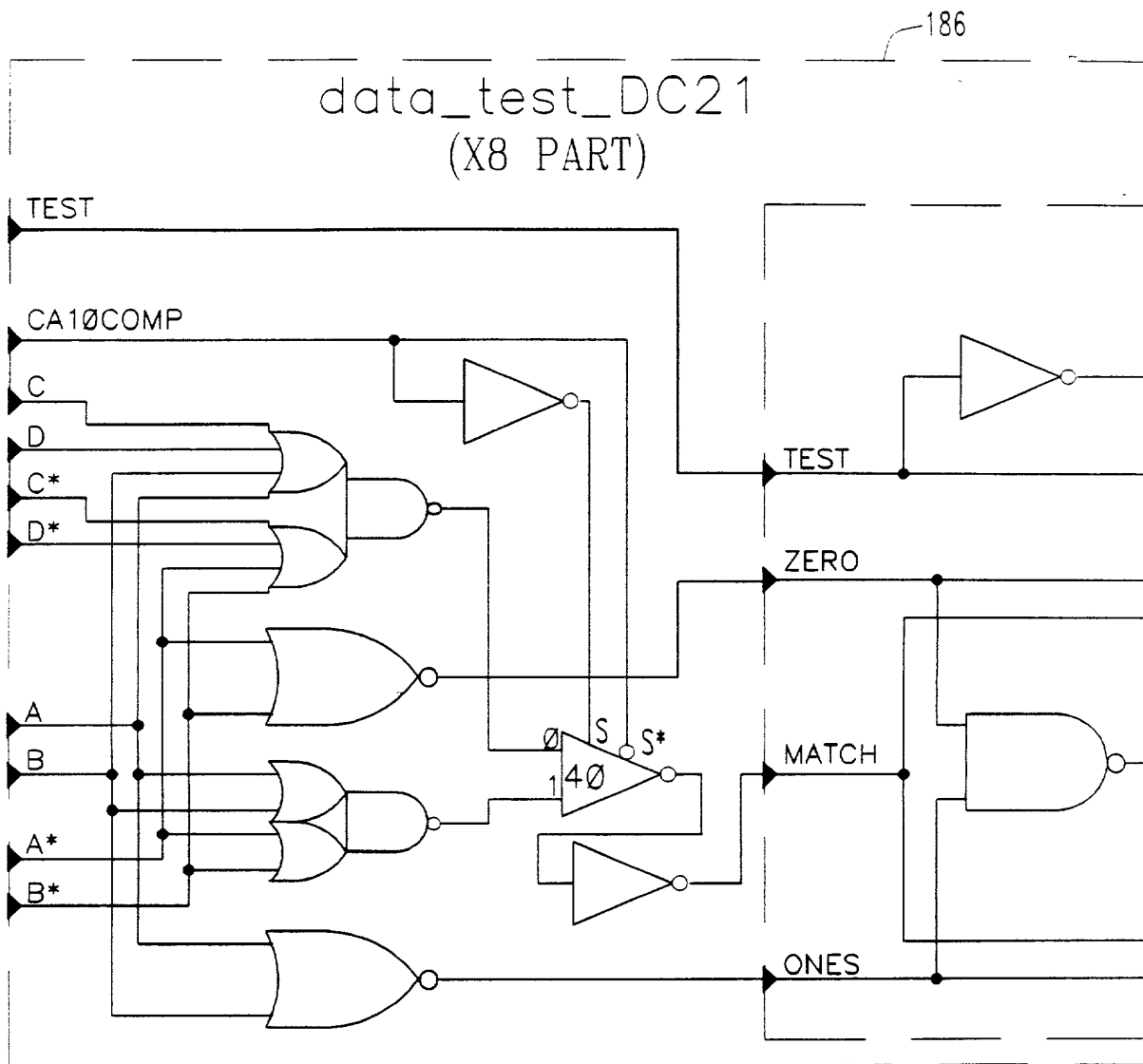


FIG. 28-1

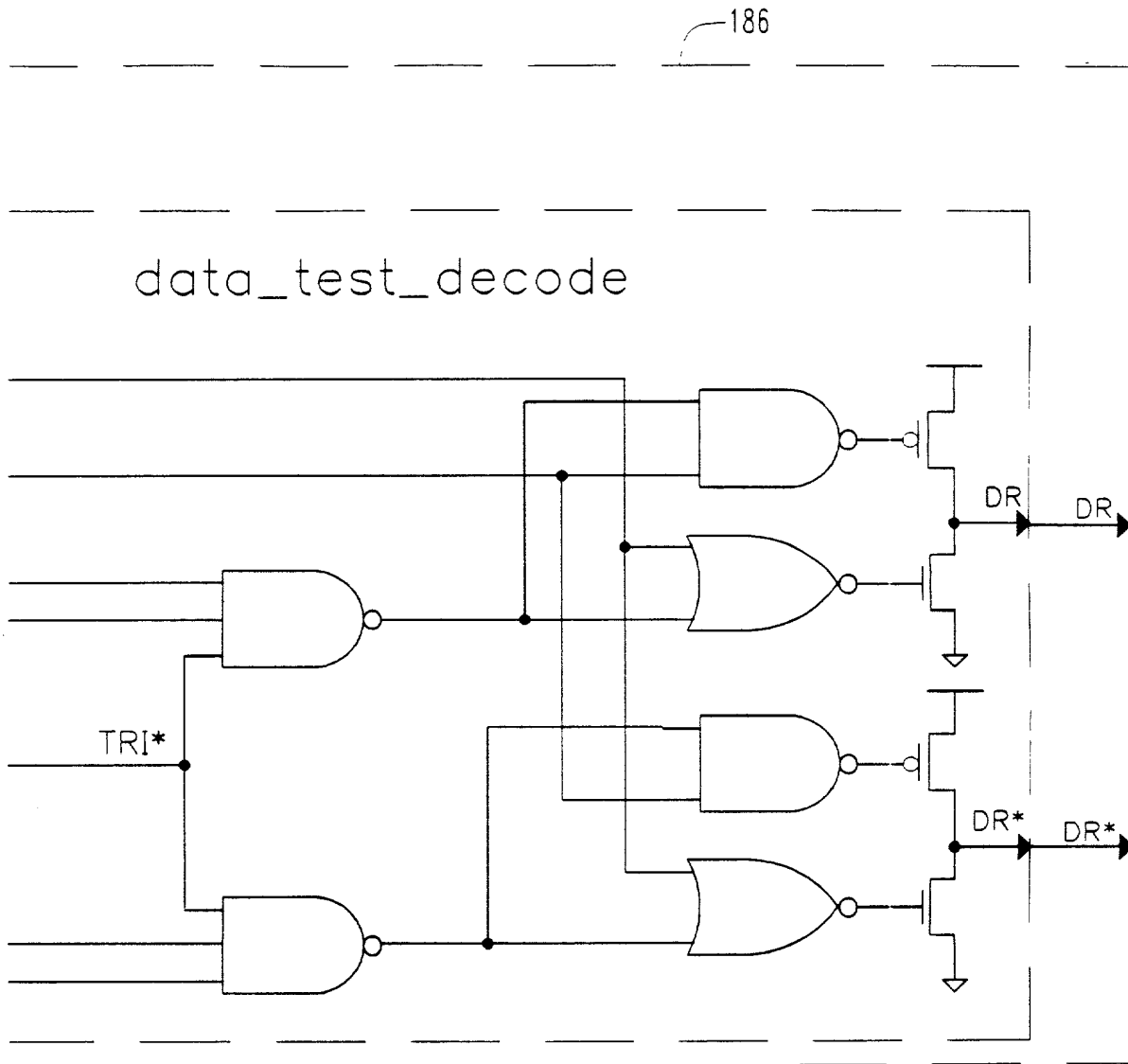


FIG. 28-2

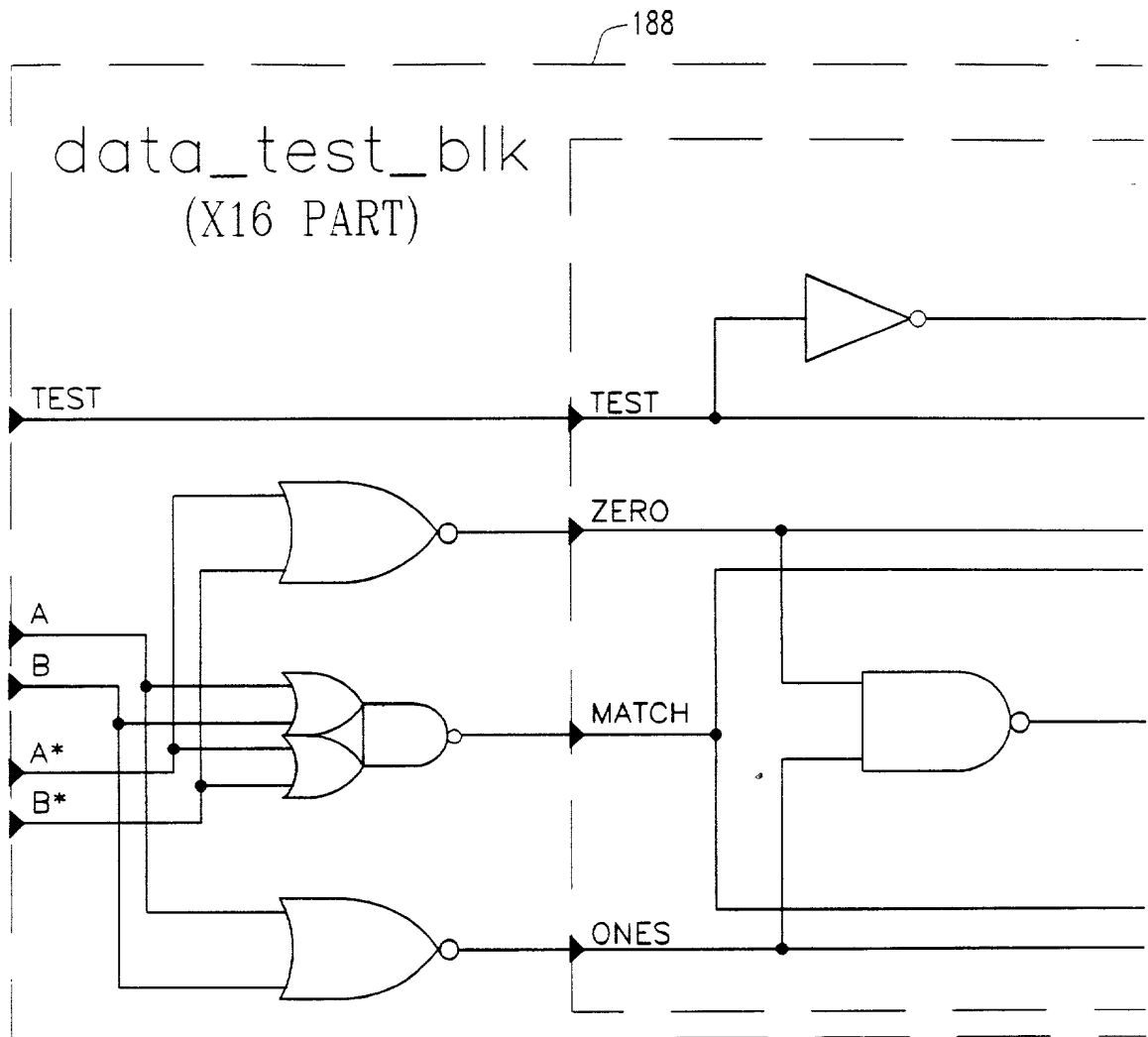


FIG. 29-1



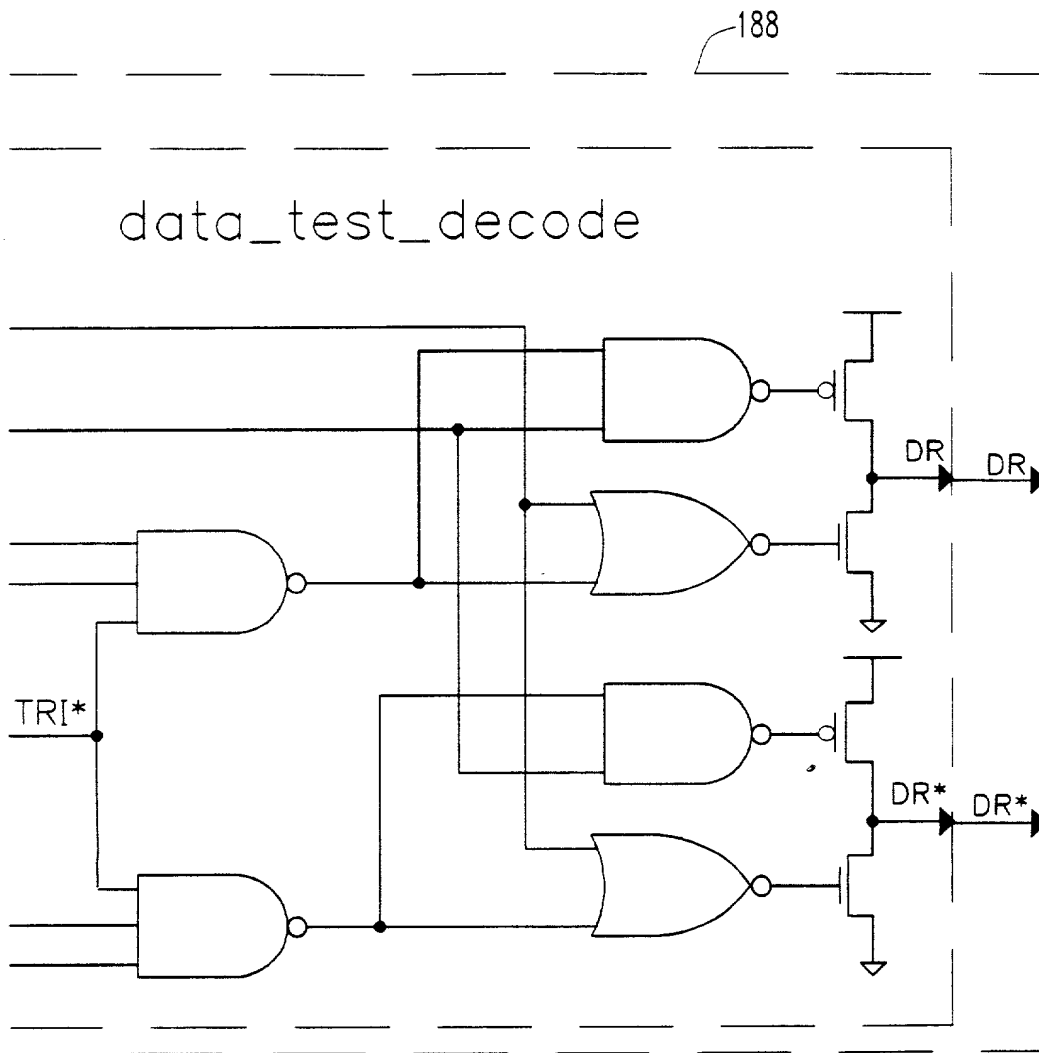
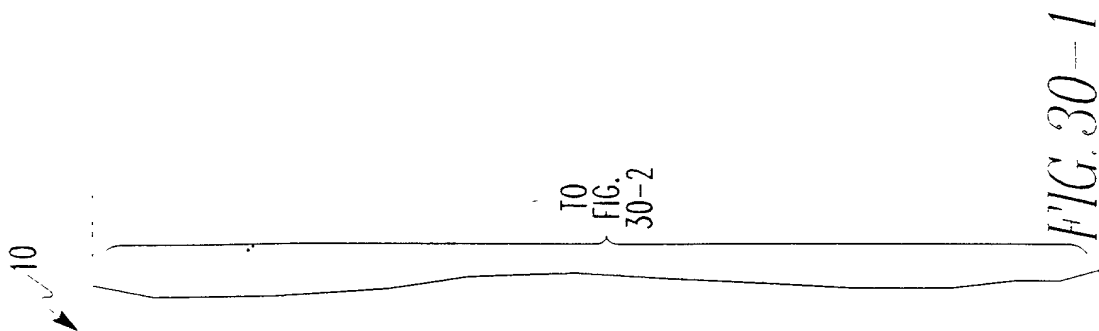


FIG. 29-2



TO  
FIG.  
30-2

FIG. 30-1

DQ1 (01)	DQ3 (03)	DQ5 (05)	DQ7 (07)
DQ0 (00)	DQ2 (02)	DQ4 (04)	DQ6 (06)
DQ1 (01)	DQ3 (03)	DQ5 (05)	DQ7 (07)
DQ0 (00)	DQ2 (02)	DQ4 (04)	DQ6 (06)
DQ1 (01)	DQ3 (03)	DQ5 (05)	DQ7 (07)
DQ0 (00)	DQ2 (02)	DQ4 (04)	DQ6 (06)

X16

X8

X4

TO FIG. 30-3

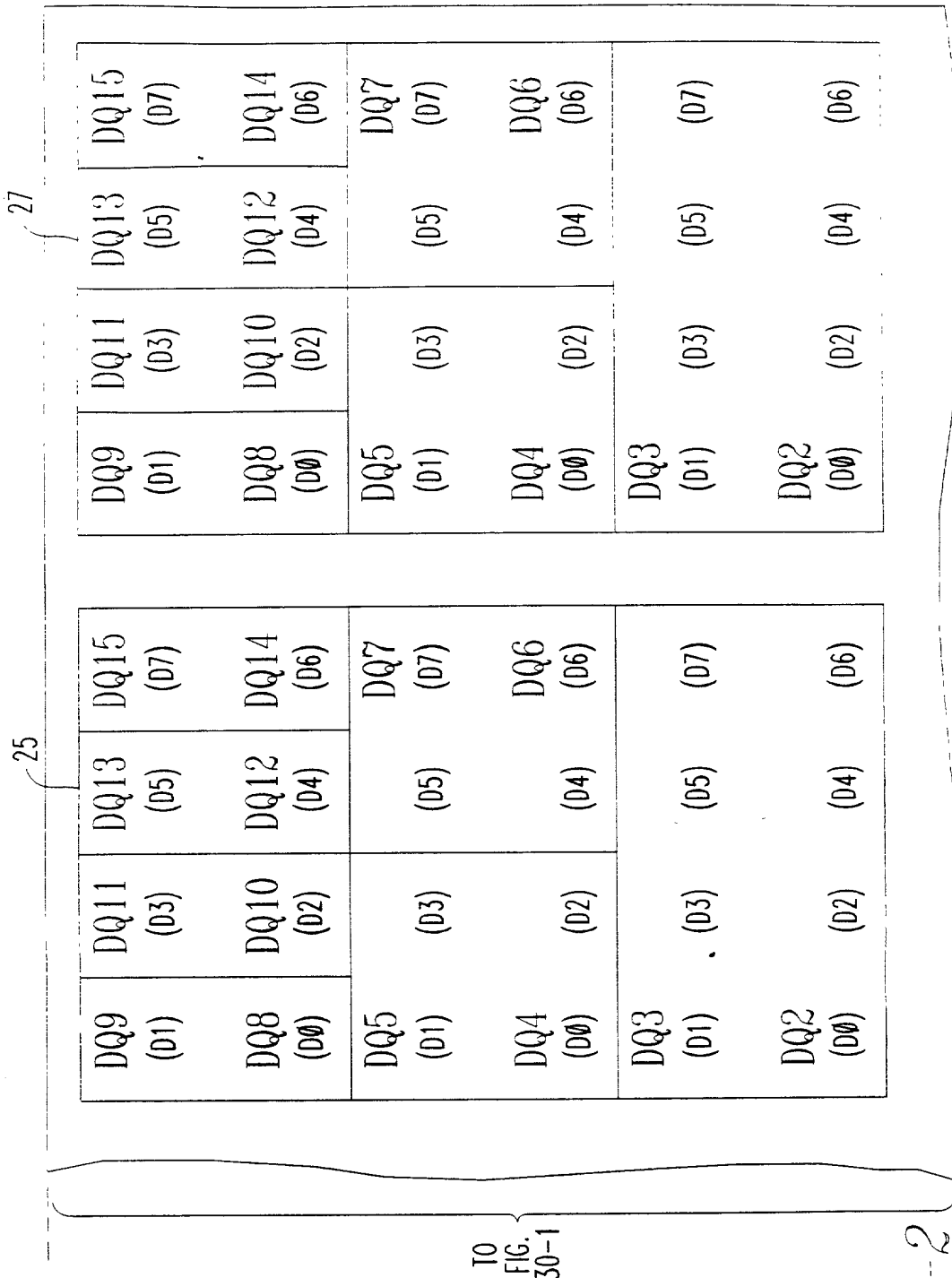


FIG. 30-2

FIG. 30-4

TO FIG. 30-1

	38			40		
	(00) DQ0	(02) DQ2	(04) DQ4	(06) DQ6	(01) DQ1	(03) DQ3
X4	(00) DQ0	(02) DQ2	(04) DQ4	(06) DQ6	(01) DQ1	(03) DQ3
X8	(00) DQ0	(02) DQ2	(04) DQ4	(06) DQ6	(01) DQ1	(03) DQ3
X16	(00) DQ0	(02) DQ2	(04) DQ4	(06) DQ6	(01) DQ1	(03) DQ3

TO  
FIG.  
30-4

FIG. 30 3

TO FIG. 30-2

31			33				
(00) DQ2	(02)	(04)	(06)	(00) DQ2	(02)	(04)	(06)
(01) DQ3	(03)	(05)	(07)	(01) DQ3	(03)	(05)	(07)
(00) DQ4	(02)	(04)	(06) DQ6	(00) DQ4	(02)	(04)	(06) DQ6
(01) DQ5	(03)	(05)	(07) DQ7	(01) DQ5	(03)	(05)	(07) DQ7
(00) DQ8	(02) DQ10	(04) DQ12	(06) DQ14	(00) DQ8	(02) DQ10	(04) DQ12	(06) DQ14
(01) DQ9	(03) DQ11	(05) DQ13	(07) DQ15	(01) DQ9	(03) DQ11	(05) DQ13	(07) DQ15

TO  
FIG.  
30-3

FIG. 30-4

<b>X4</b>	VSSQ	N/C	VSSQ	N/C	N/C						
<b>X8</b>	VSSQ	DQ3	VSSQ	DQ2	N/C						
<b>X16</b>	VSSQ	DQ7	VSSQ	DQ6	DQ5						
	1	2	3	4	5	6	7	8	9	10	11
<b>X16</b>	VCCQ	DQ0	VCCQ	DQ1	DQ2	VCCQ					
<b>X8</b>	VCCQ	DQ0	VCCQ	DQ1	N/C	N/C					
<b>X4</b>	VCCQ	DQ0	VCCQ	DQ1	N/C	N/C					

FIG. 31A1

TO FIG. 31A2

TO FIG. 31A1

N/C	N/C	VSSQ	VSS	VCC1 Probe							
N/C	N/C	VSSQ	VSS	NCSV							
VSSQ	DQ4	VSSQ	VSS	NCSV							
12	13	14	15	16	17	18	19	20	21	22	23
DQ3	VCCQ	VCCQ	VCCX	WE							
N/C	VCCQ	VCCQ	VCCX	WE							
N/C	VCCQ	VCCQ	VCCX	WE							

FIG. 31A2

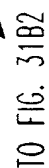


FIG. 31B1



TO FIG. 31B1  
↓

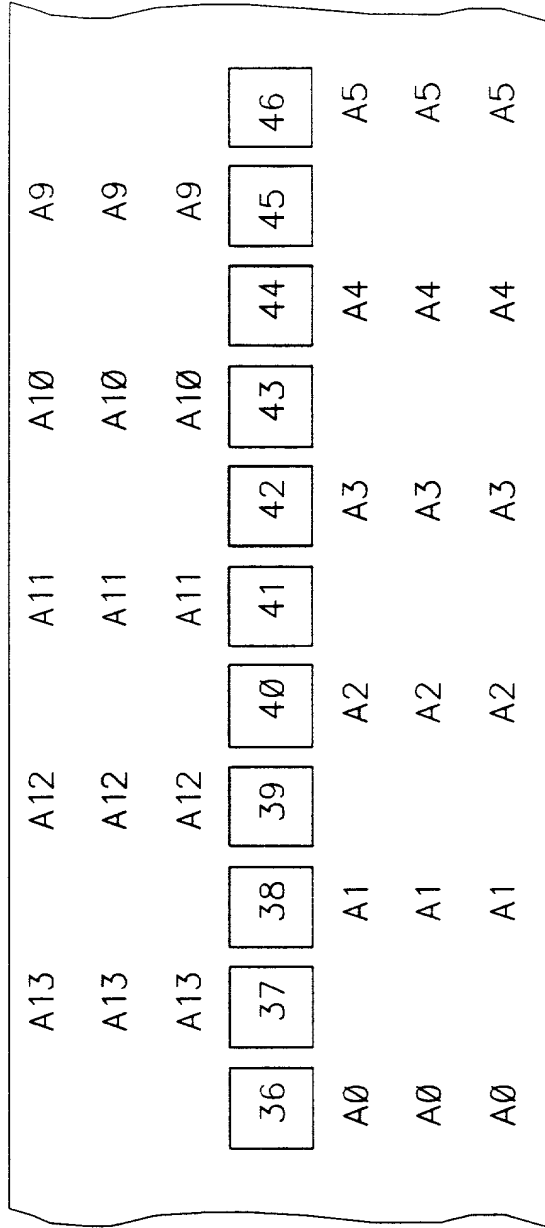


FIG. 31B2

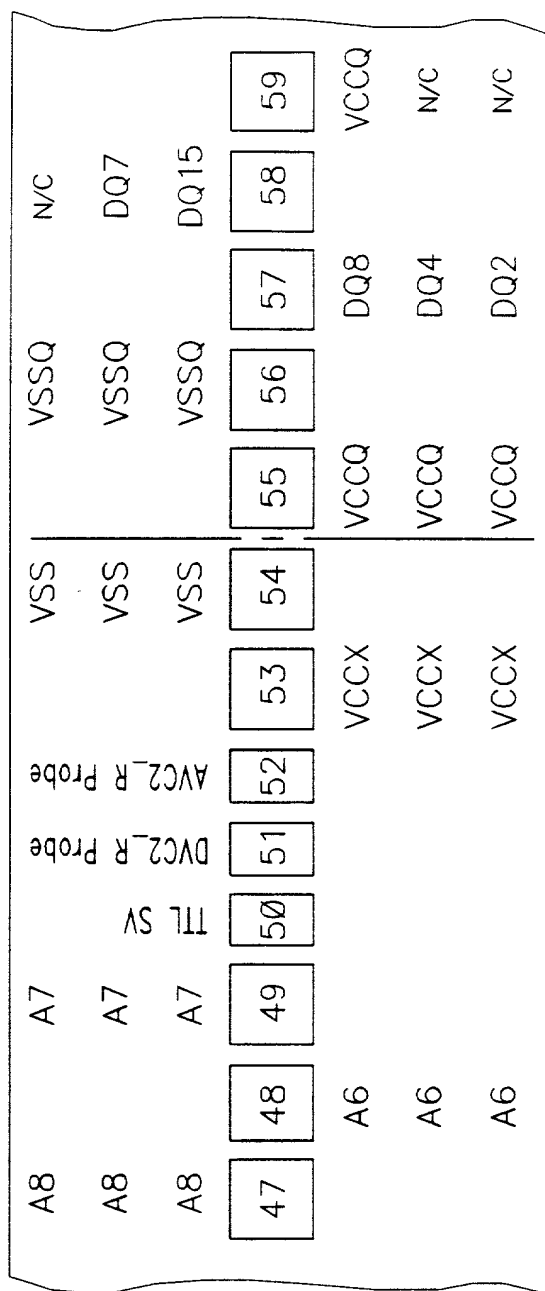


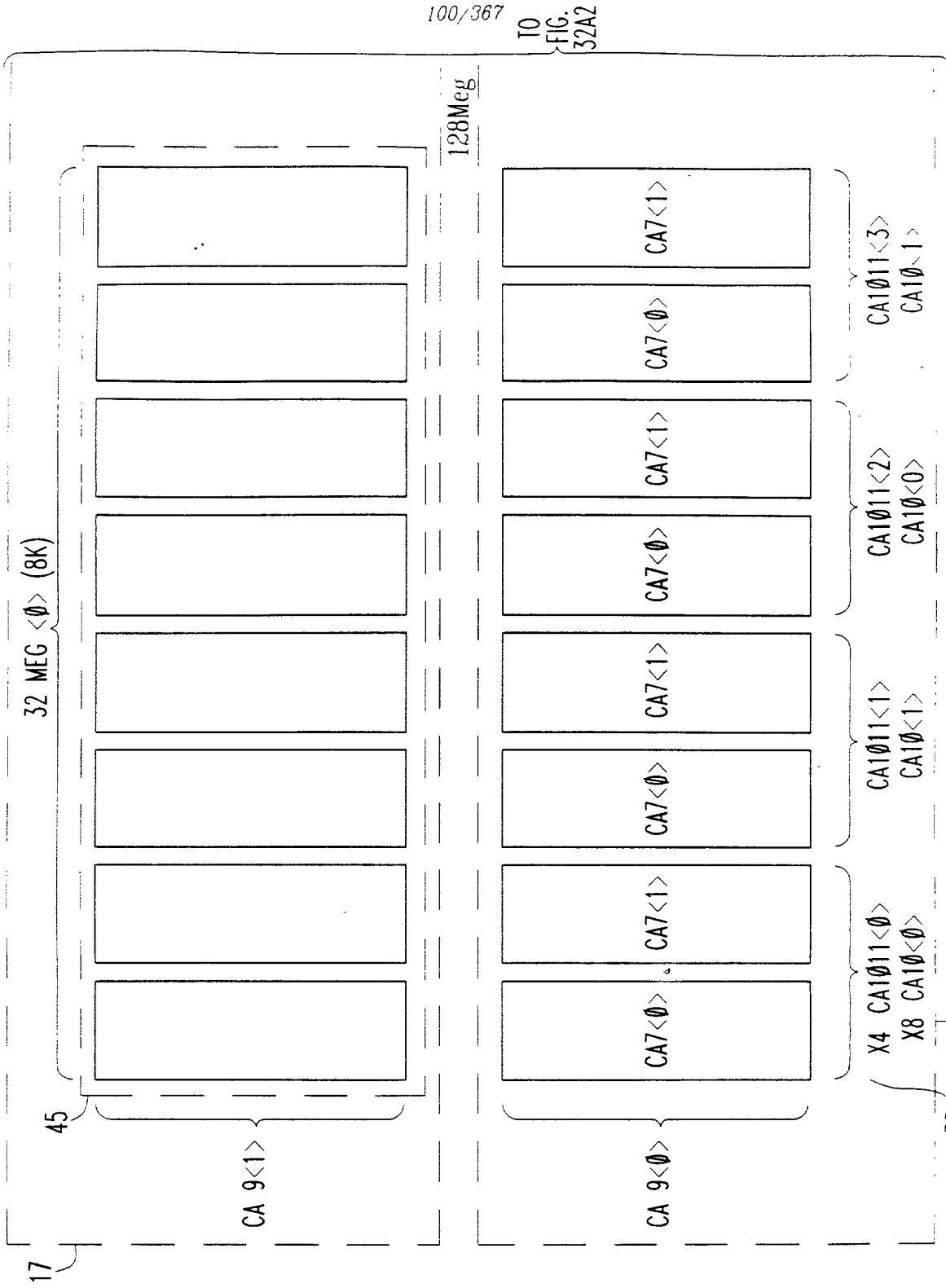
FIG. 31C1

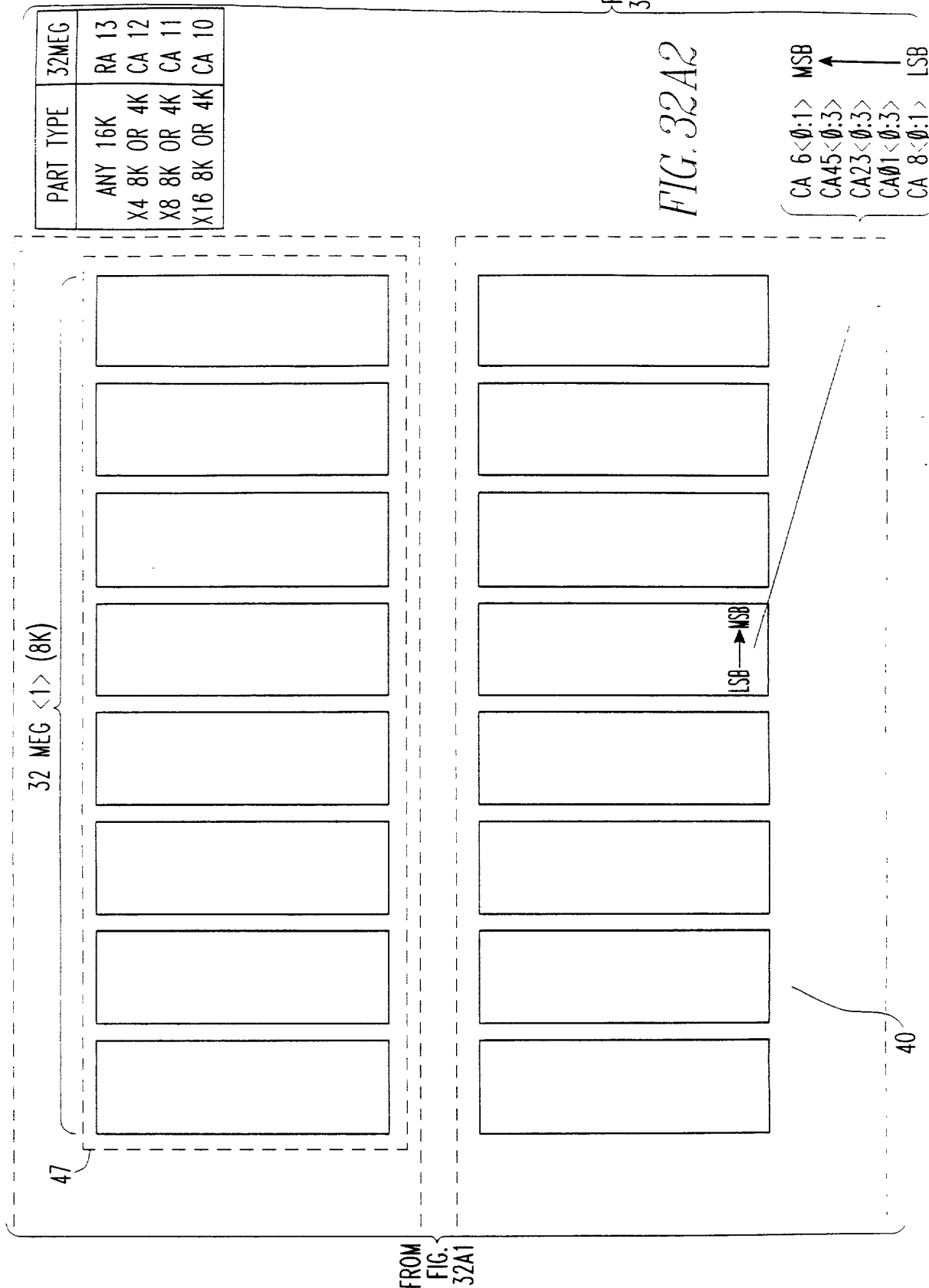
TO FIG. 31C2

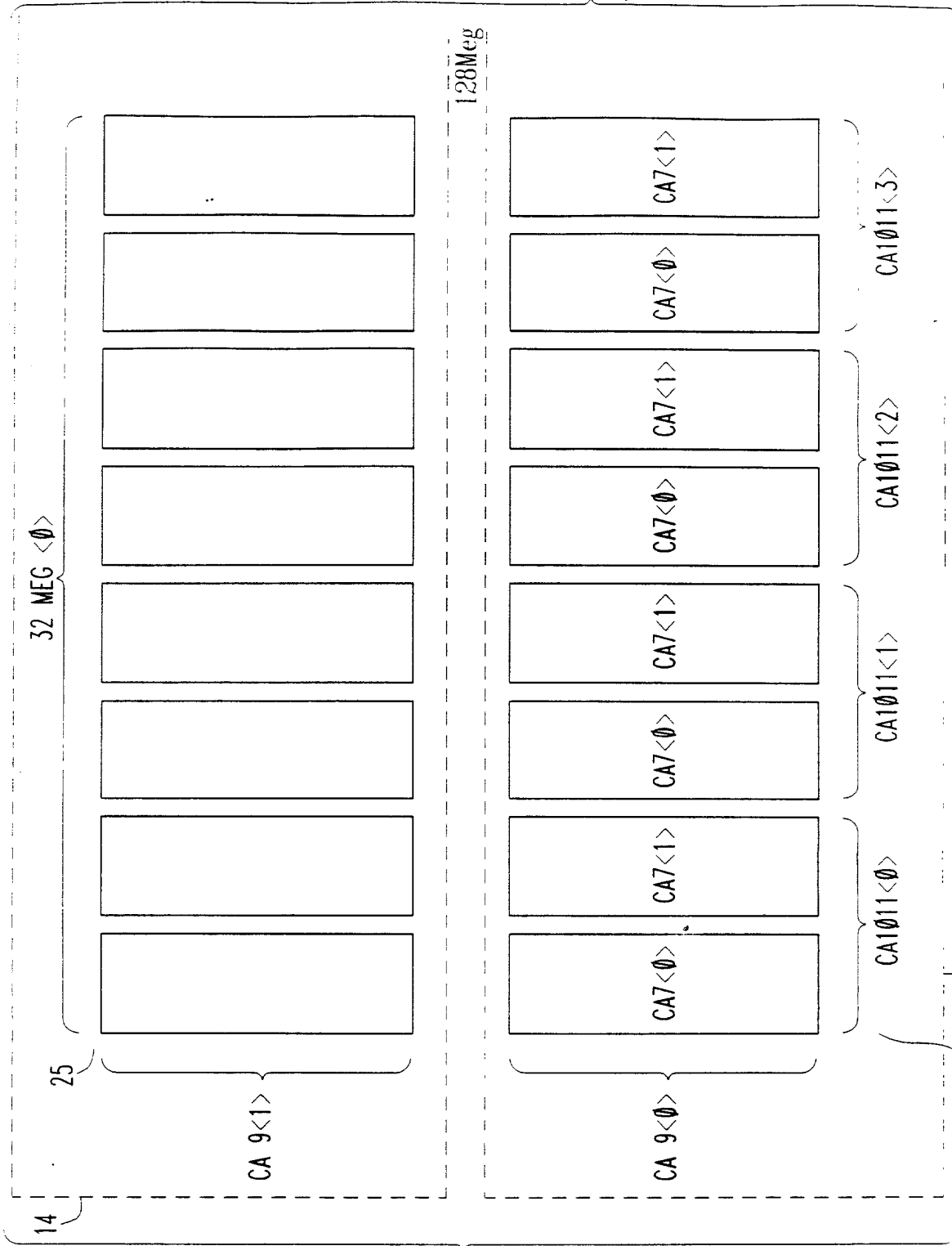
TO FIG. 31C1

[illegible]

FIG. 31C2

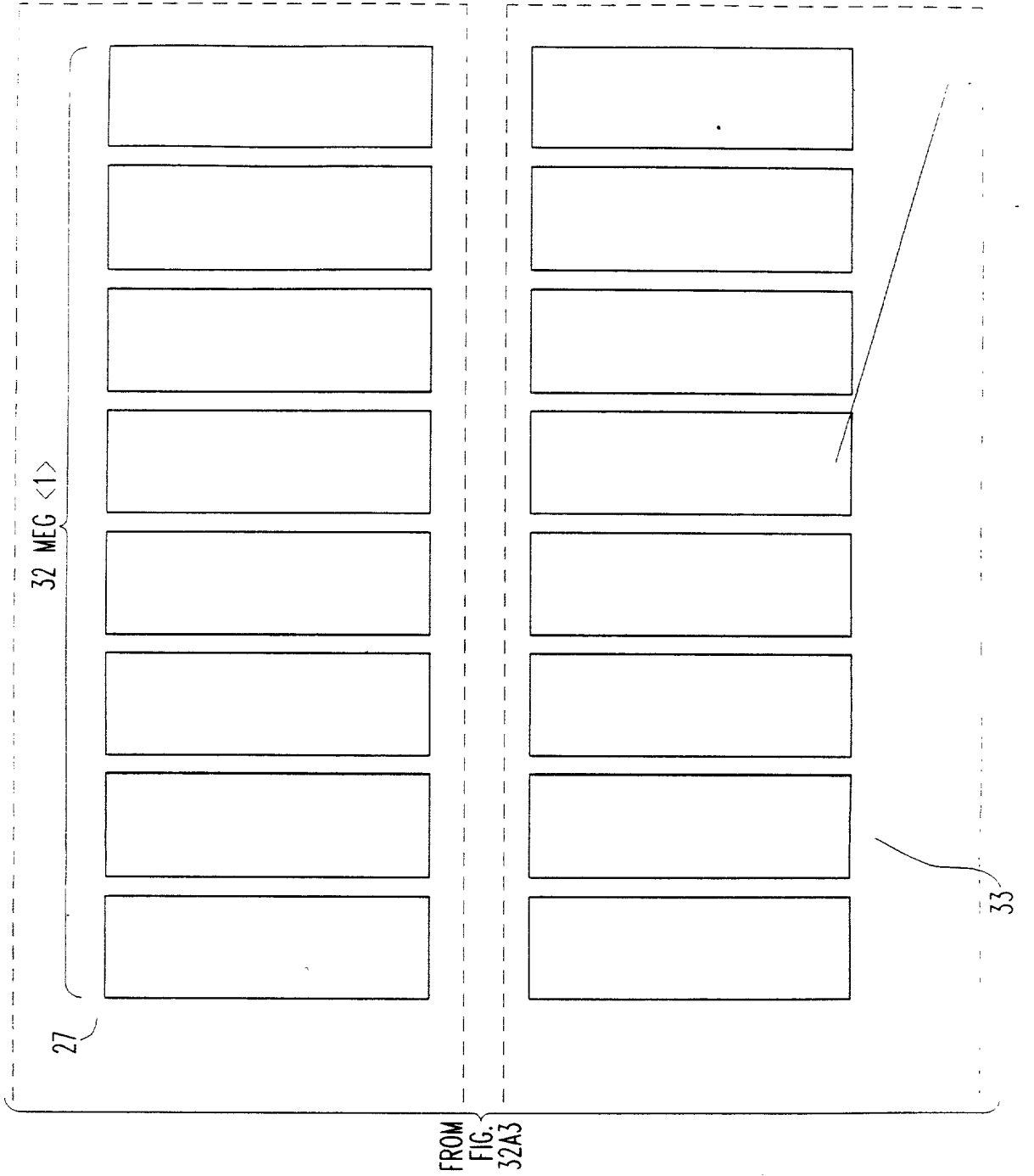






FROM  
FIG.  
32A2

FIG. 32A3



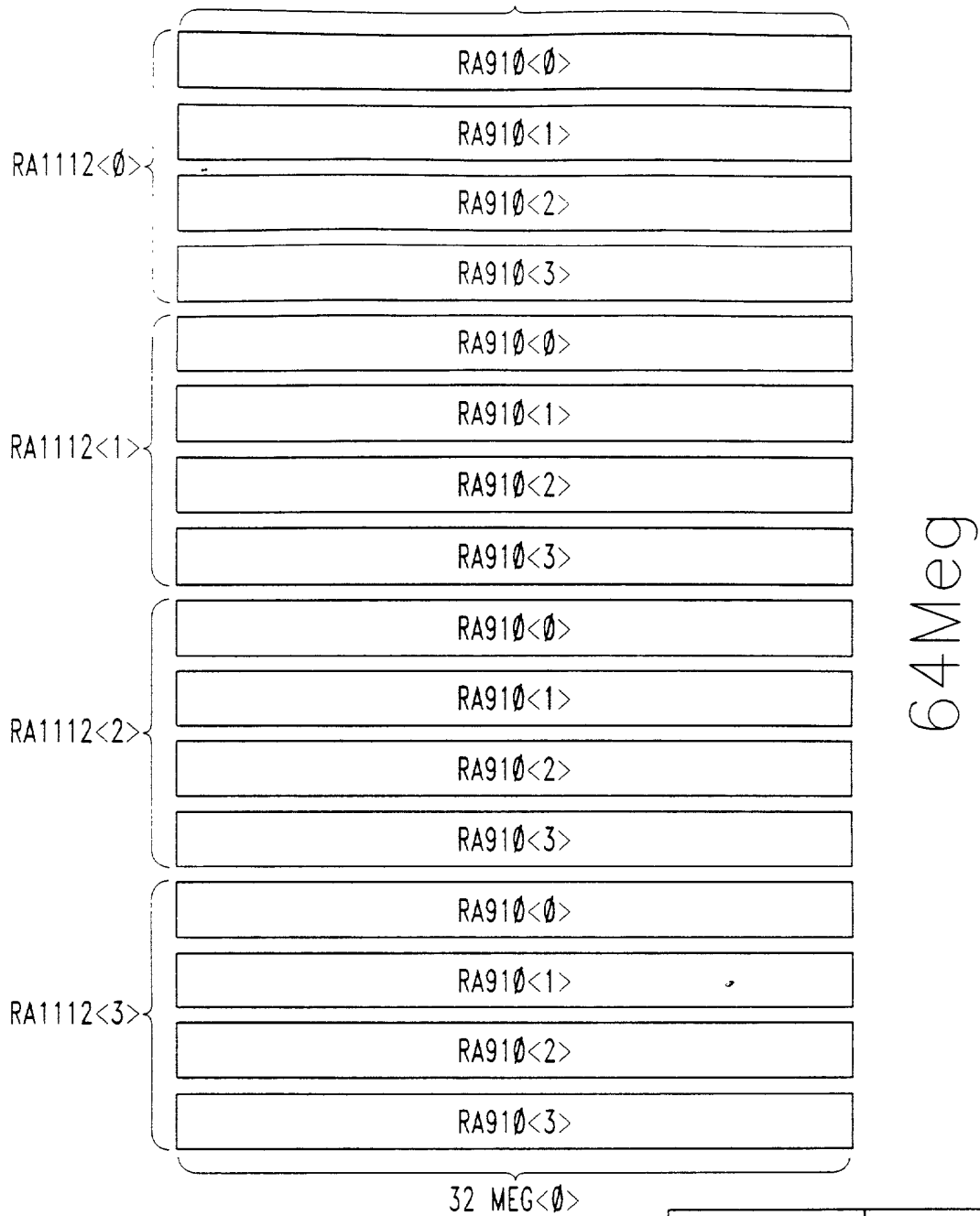


FIG. 32B1

PART TYPE	32MEG
ANY 16K	RA_13
X4 8K OR 4K	CA_12
X8 8K OR 4K	CA_11
X16 8K OR 4K	CA_10



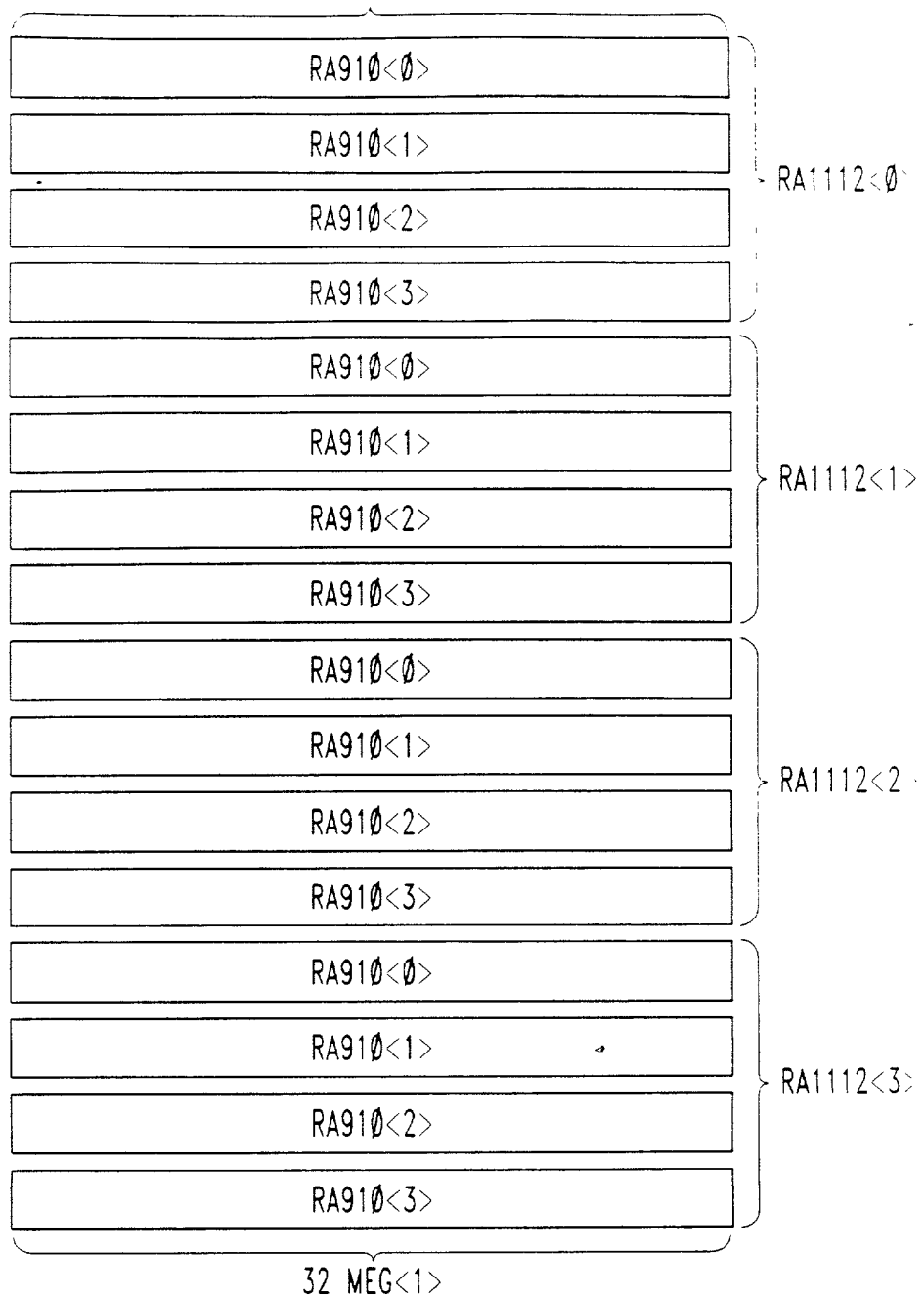
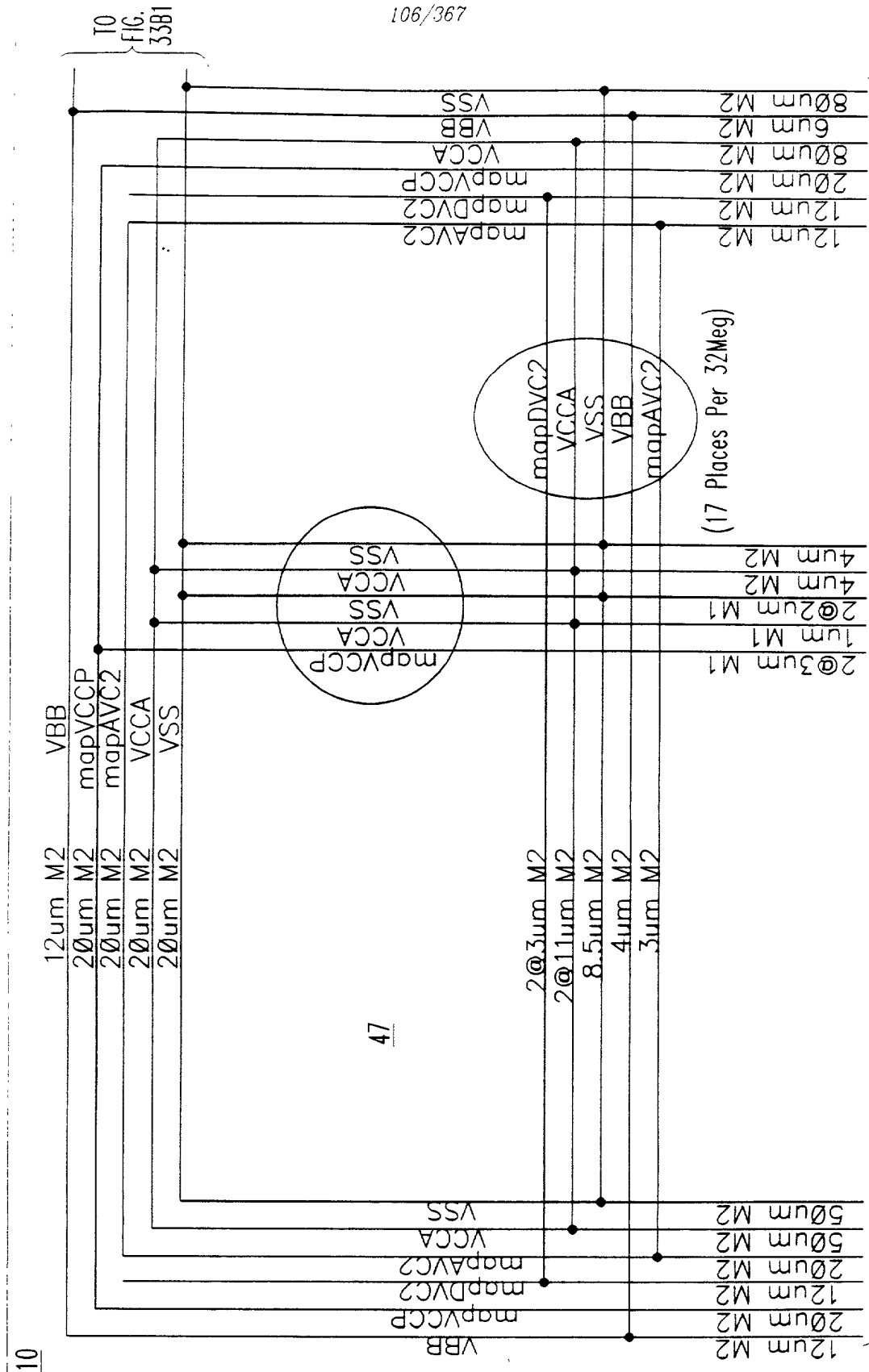


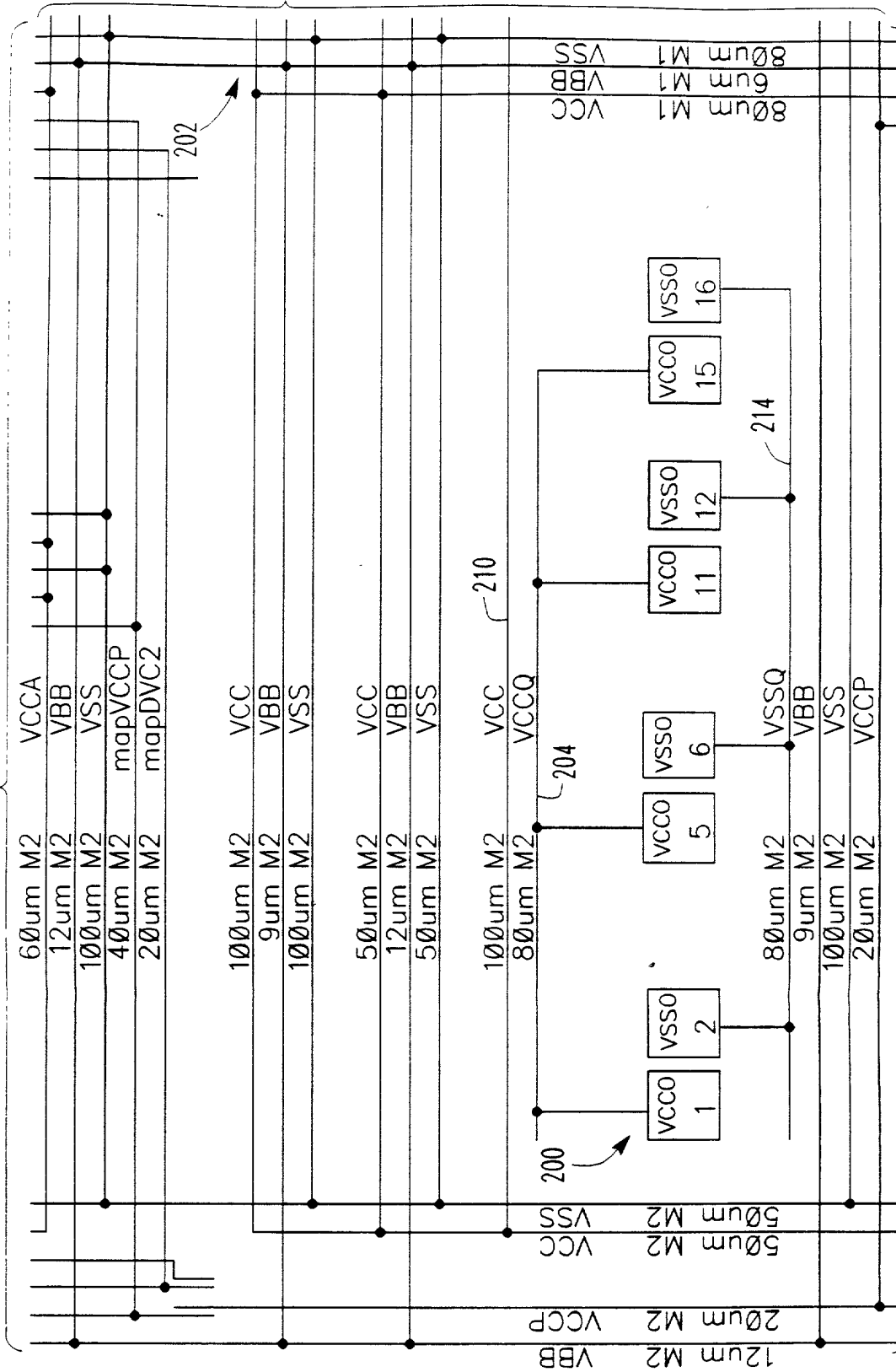
FIG. 32B2



TO FIG. 33A2

FIG. 33A

FROM FIG. 33A1



TO FIG. 33B3

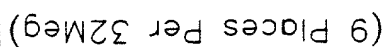
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TO FIG. 33A3

FIG. 33A2

[illegible]

4463313



40

(17 Places Per 32Meg)

3um	M2
8.5um	M2
2@11um	M2
2@3um	M2
4um	M2

(9 Places Per 32Meg)

220μm	M2	VSS
220μm	M2	VCCA
220μm	M2	mapAVC
220μm	M2	mapVCC
12μm	M2	VBB

[illegible]

mapAVC2	VSS	VCCA	mapDVC2	VBB
---------	-----	------	---------	-----

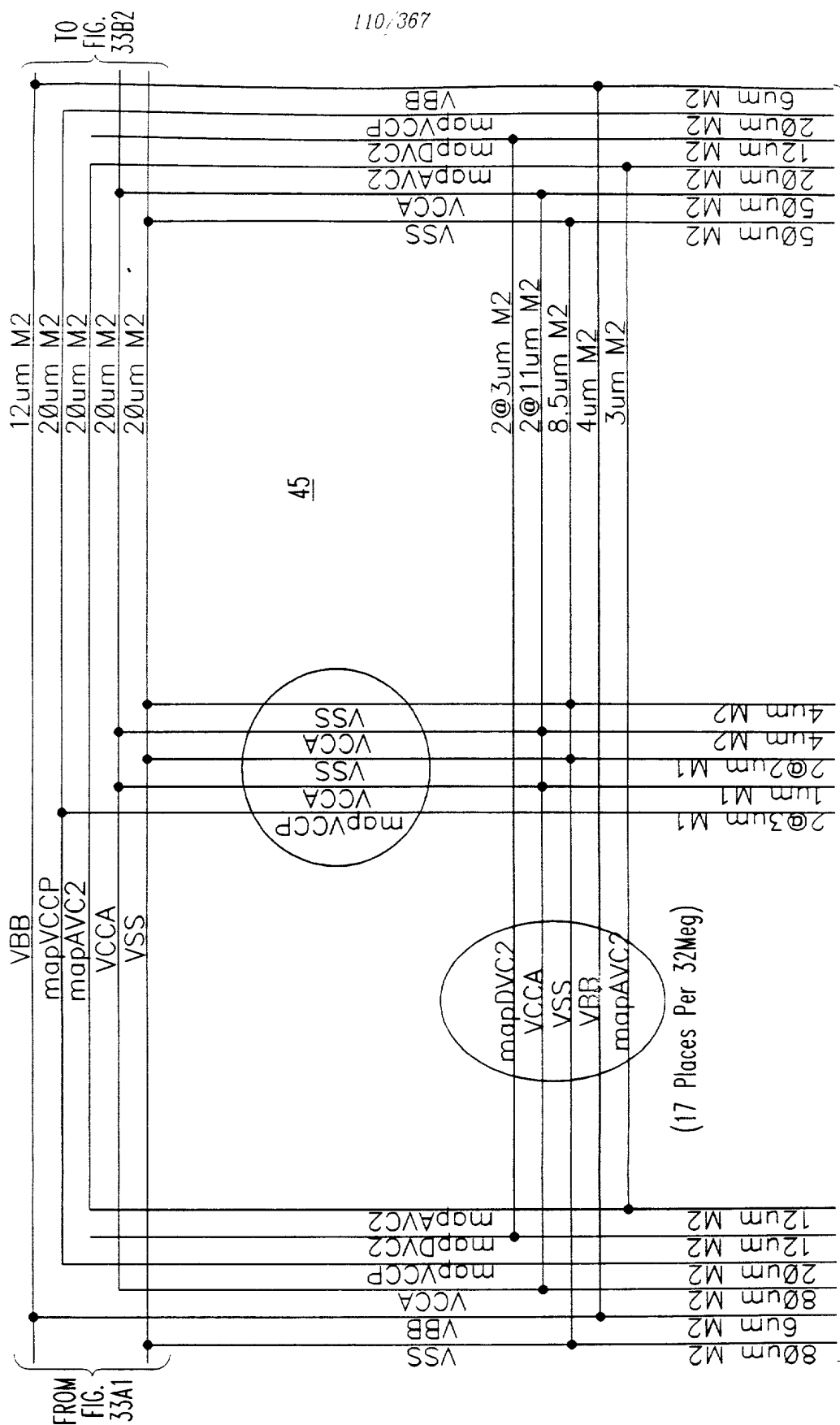
mapVCCP  
VCCA  
VSS  
VCCA  
VSS

2@3um M1  
1um M1  
2@2um M1  
4um M2  
4um M2

TO  
FIG.  
33B7

109 367

17633/1



TO FIG. 33B3

17C.3331

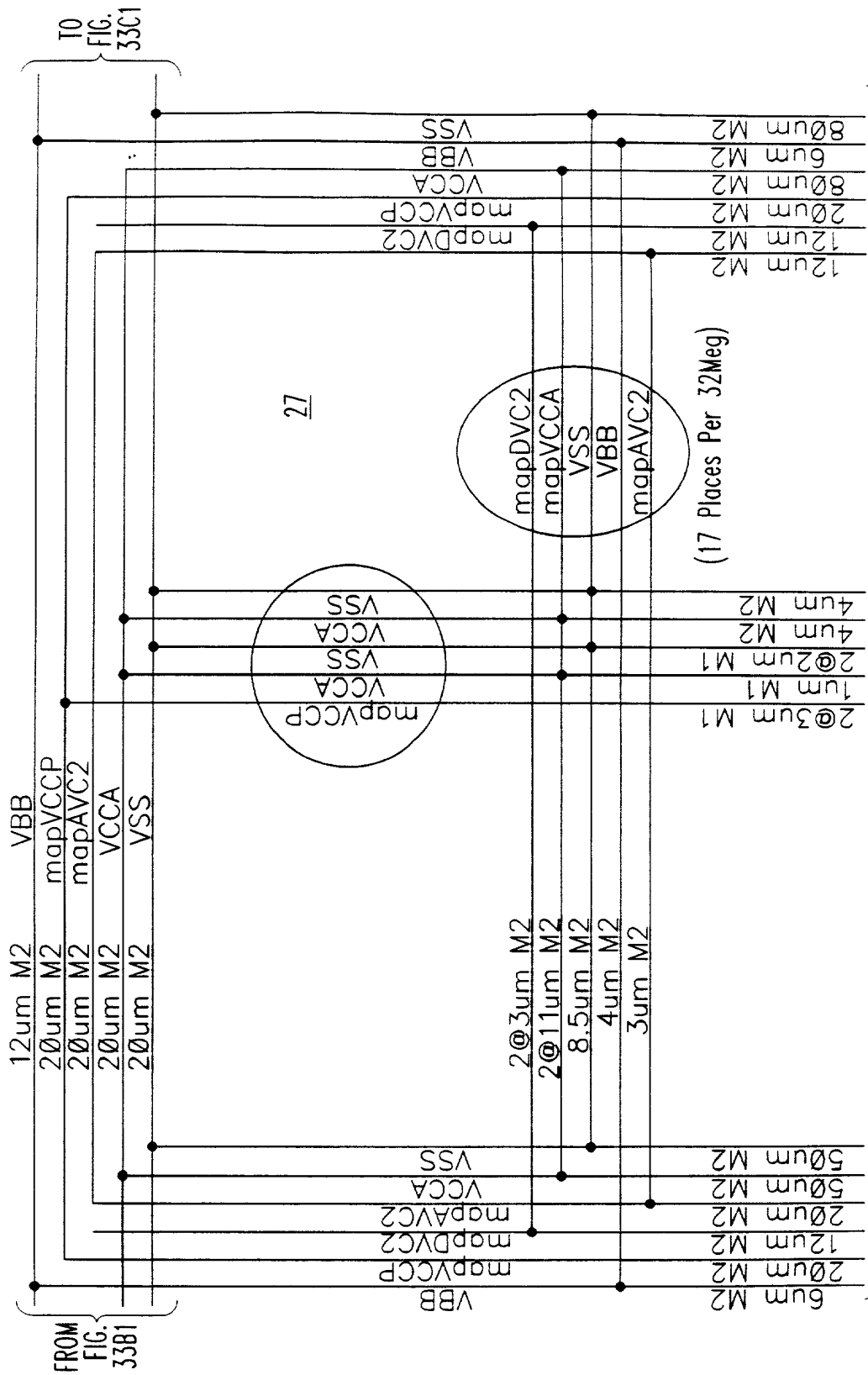
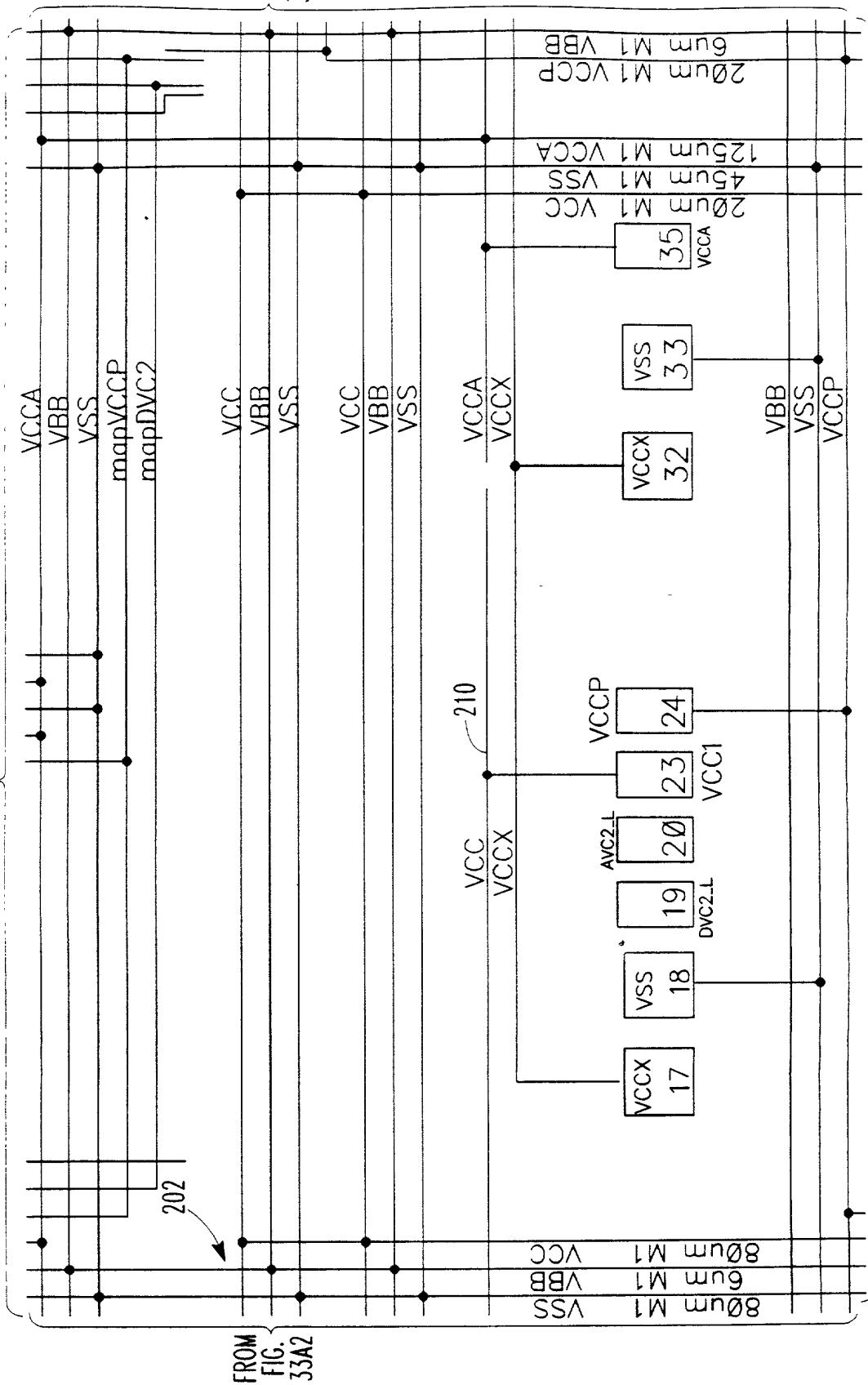


FIG. 33B2

FROM FIG. 33B1



TO FIG. 33B3

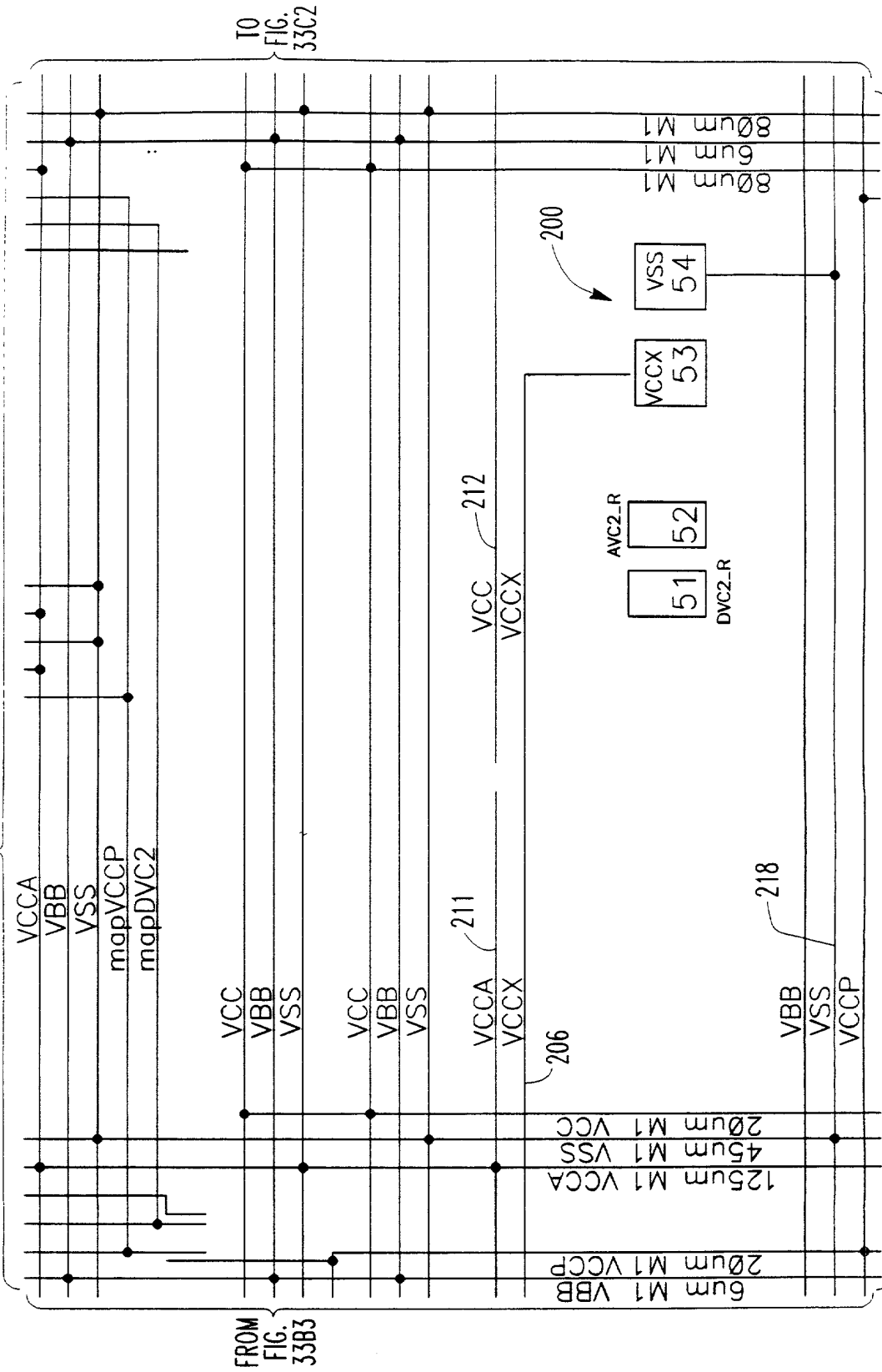
FIG. 33B3

FROM  
FIG.  
33A2

TO  
FIG.  
33B4



FROM FIG. 33B2



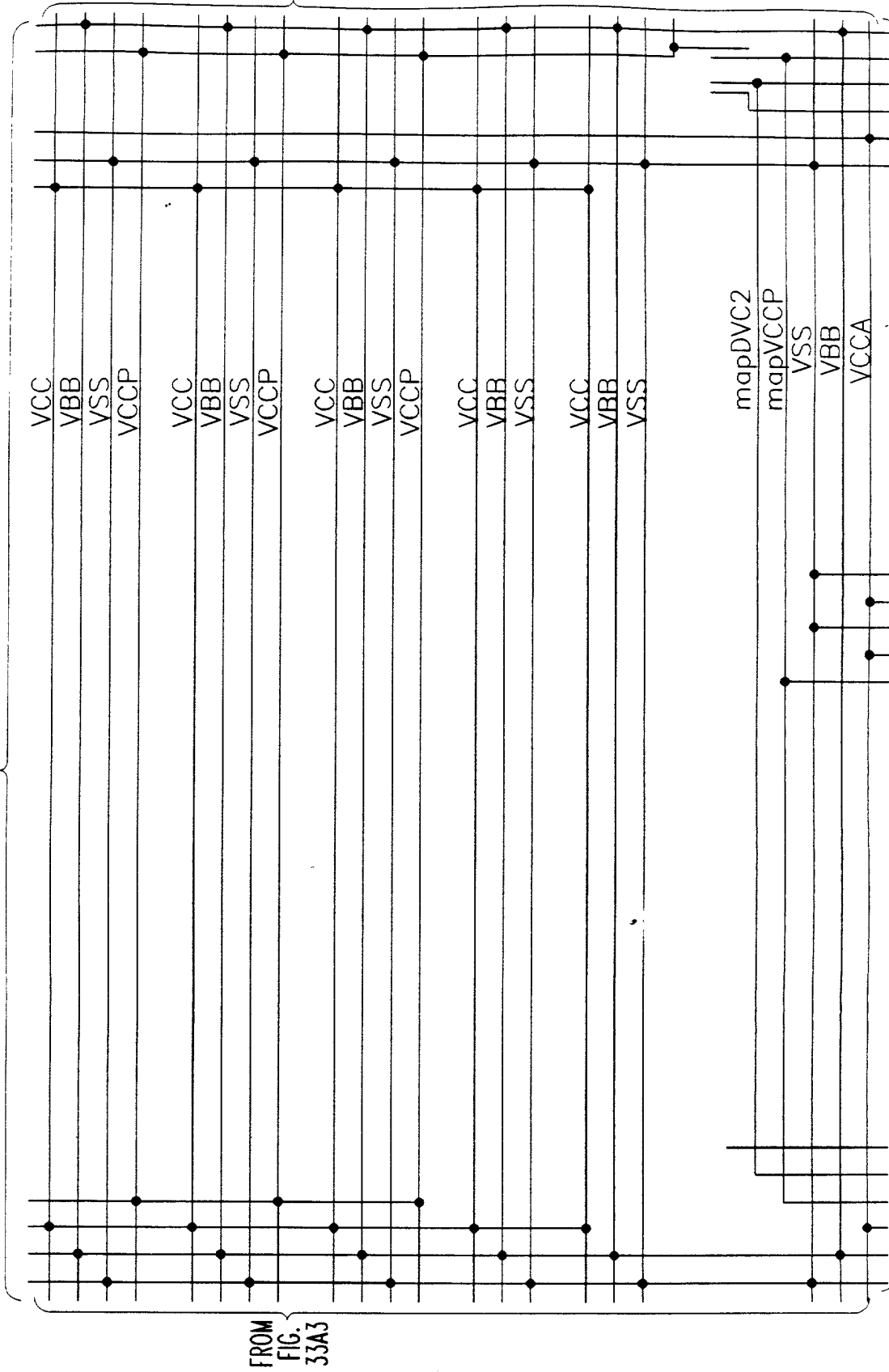
FROM FIG. 33B3

TO FIG. 33C2

TO FIG. 33B6

FIG. 33B1

FROM FIG. 33B3

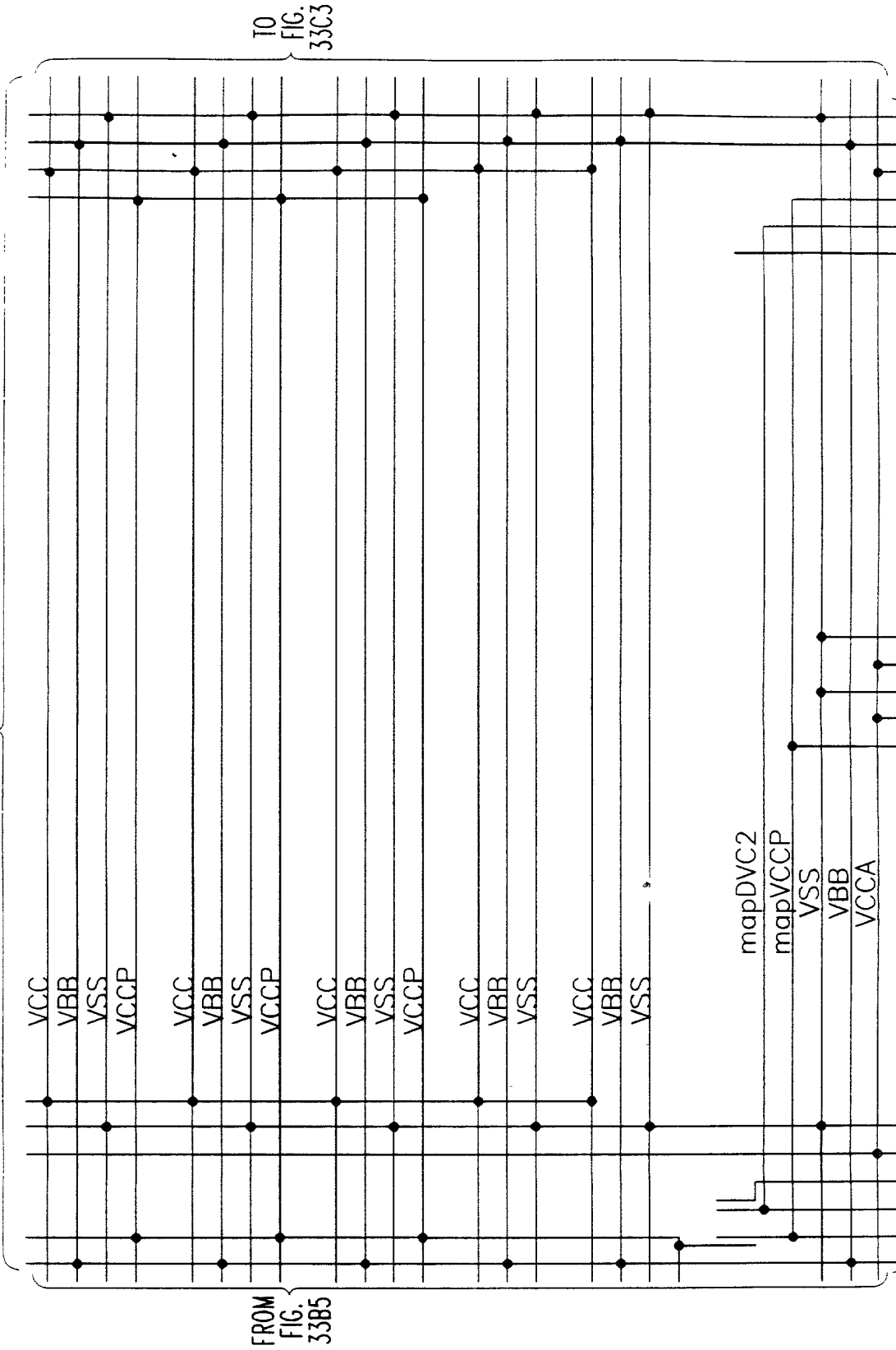


TO FIG. 33B7

FIG. 33B5

FIG. 33B6

FROM FIG. 33B4



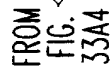
TO FIG. 33B8

FIG. 33B6

38

(17 Places Per 32Meg)

(9 Places Per 32Meg)



11/16 3337

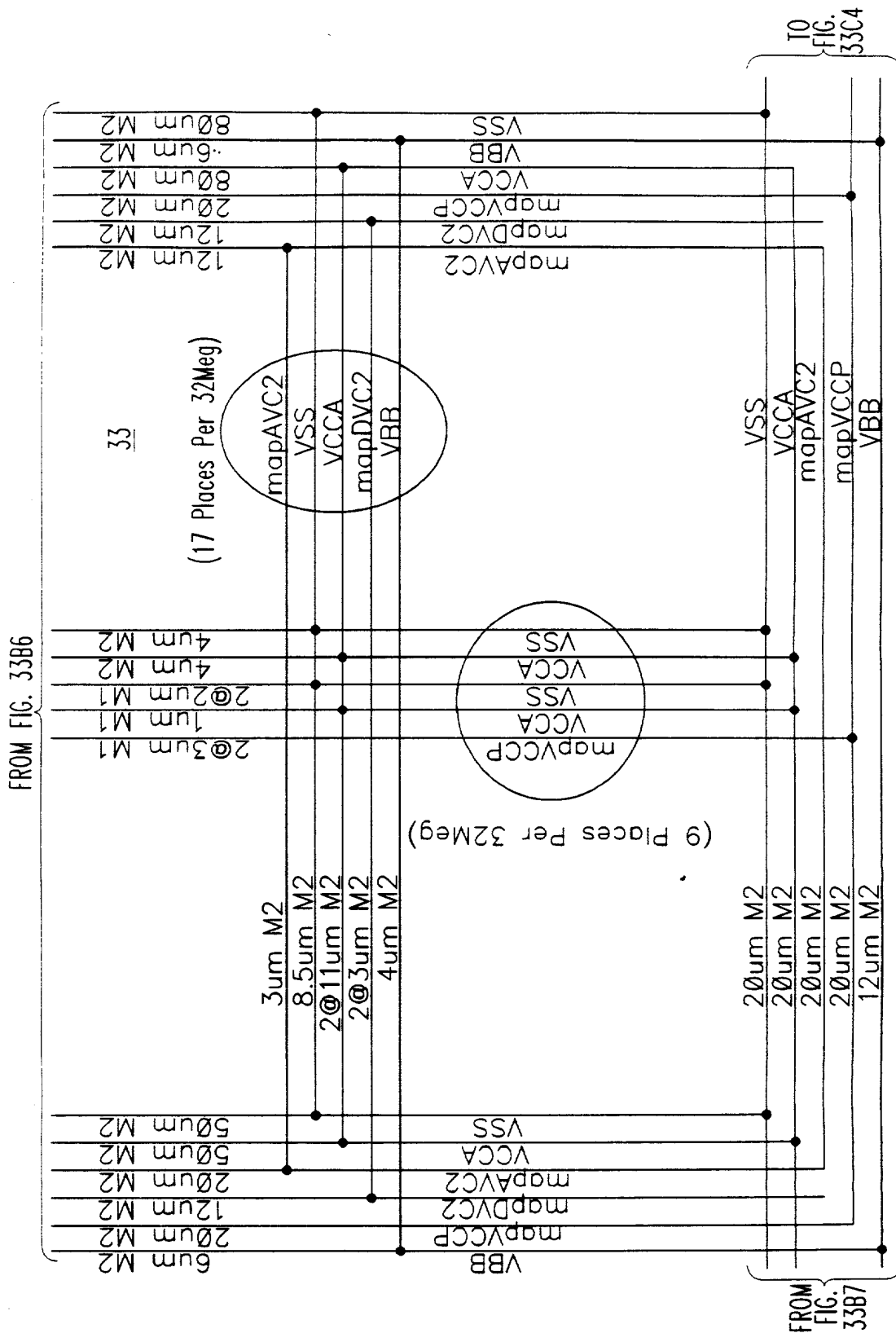


FIG. 33B8

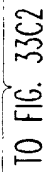


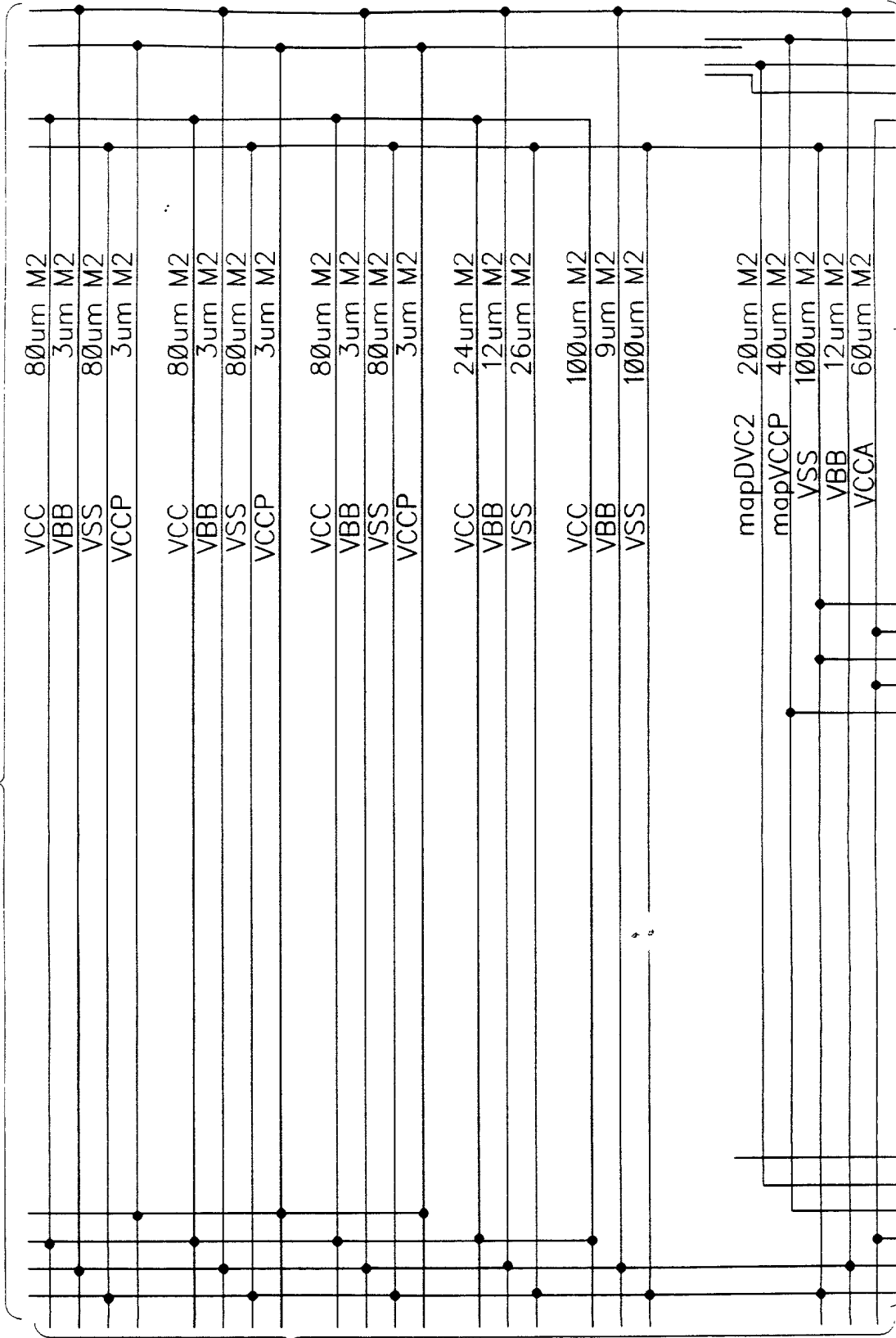
FIG. 33C1

FROM  
FIG.  
33B3



FIG. 33C2

FROM FIG. 33C2



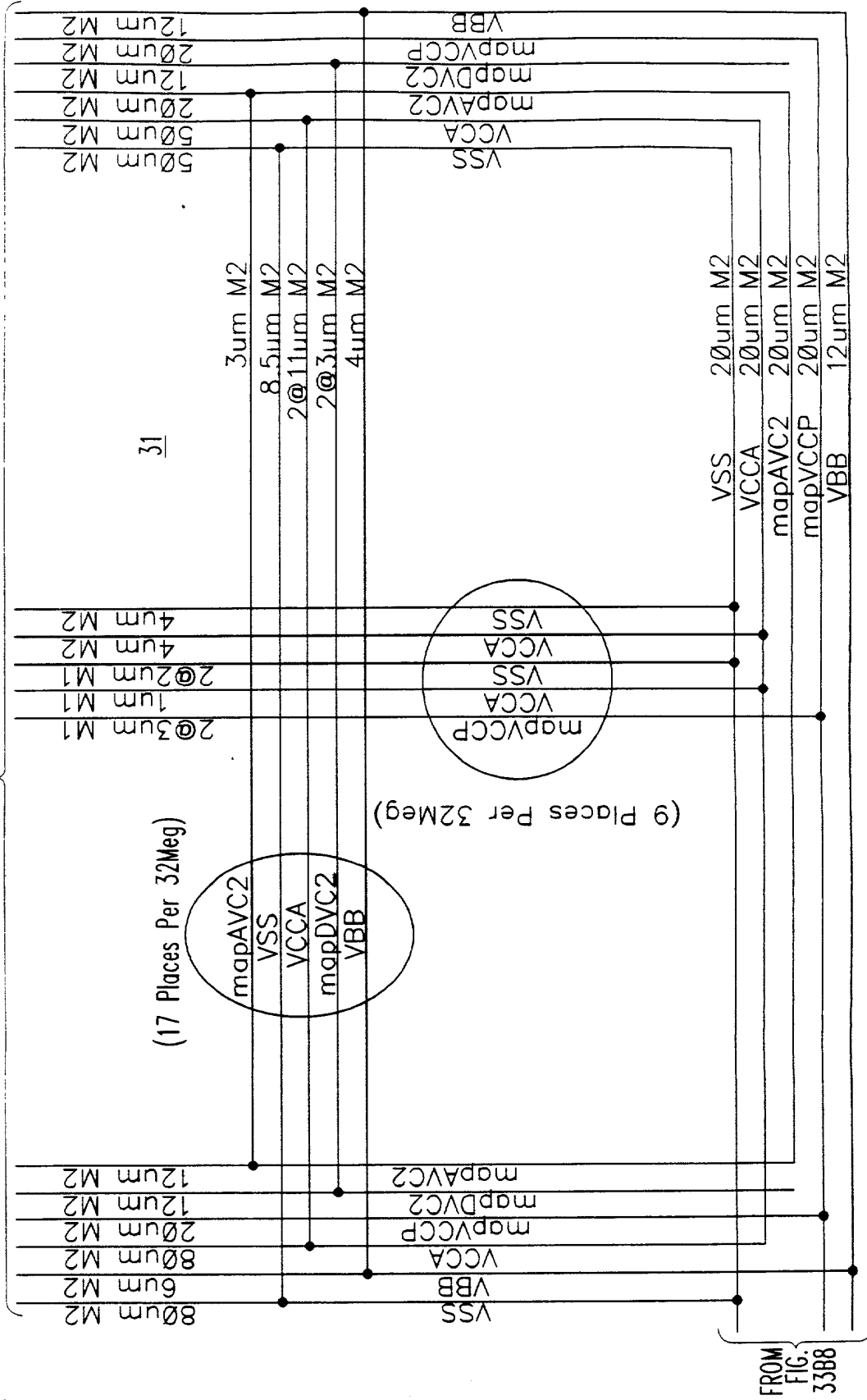
FROM  
FIG.  
33B6

TO FIG. 33C4

FIG. 33C3



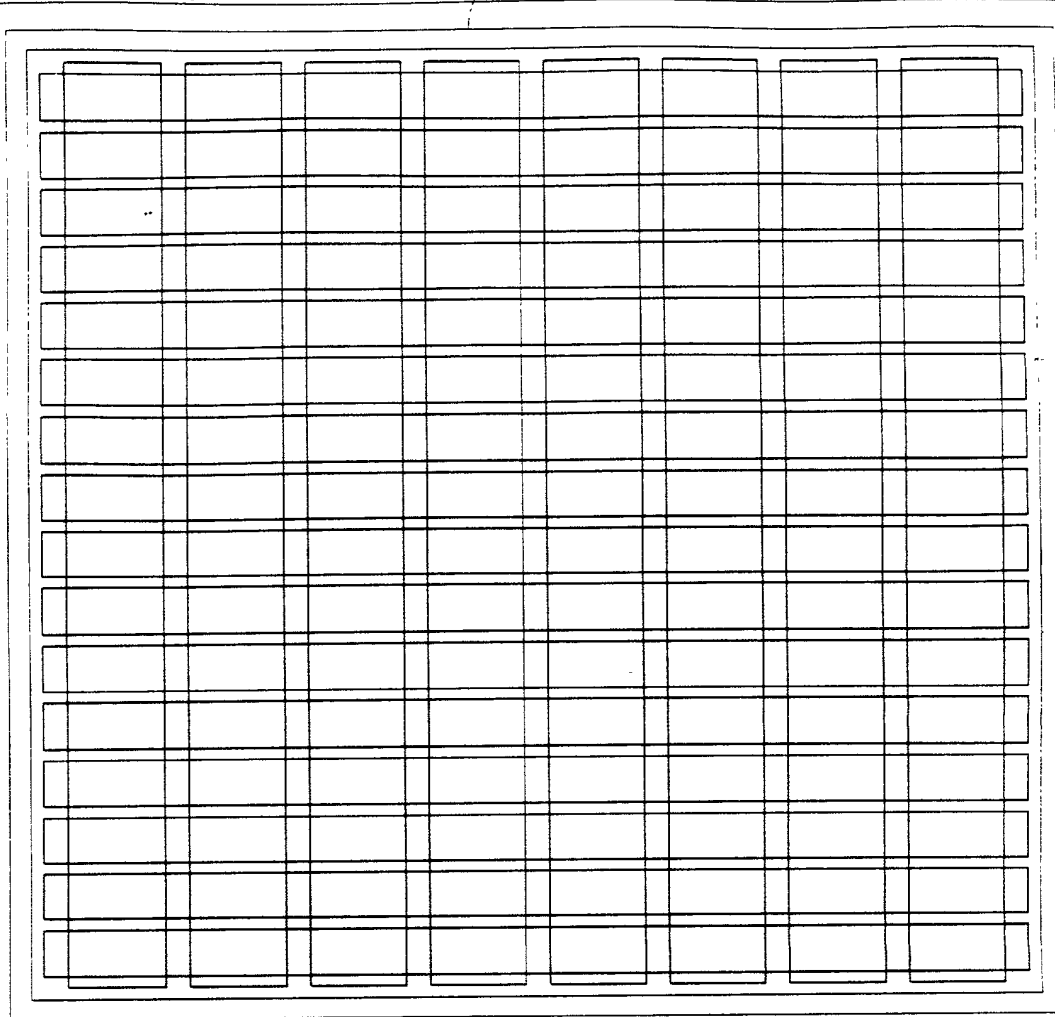
FROM FIG. 33C3



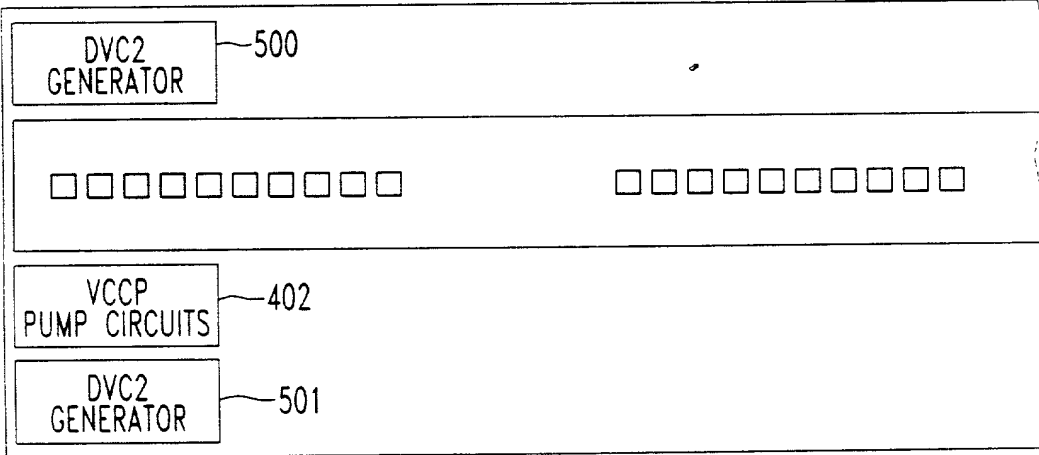
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10

45



200



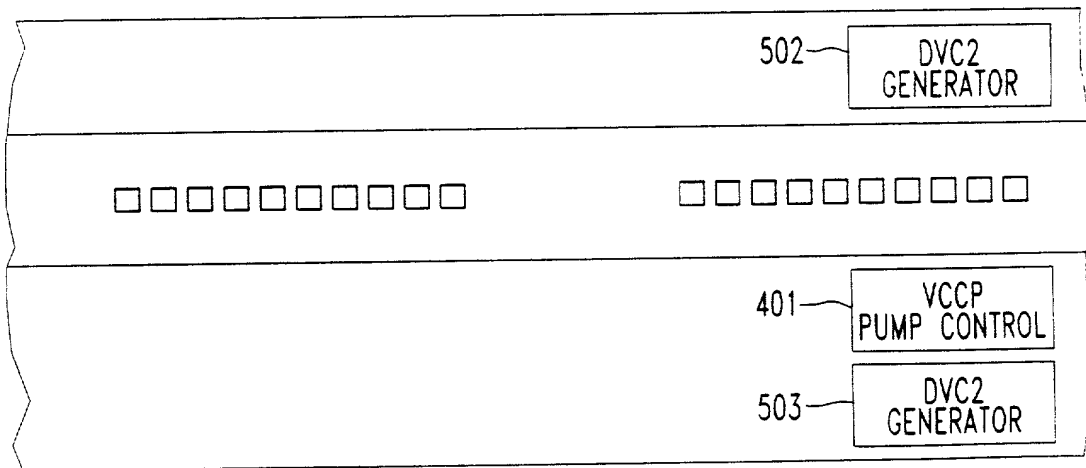
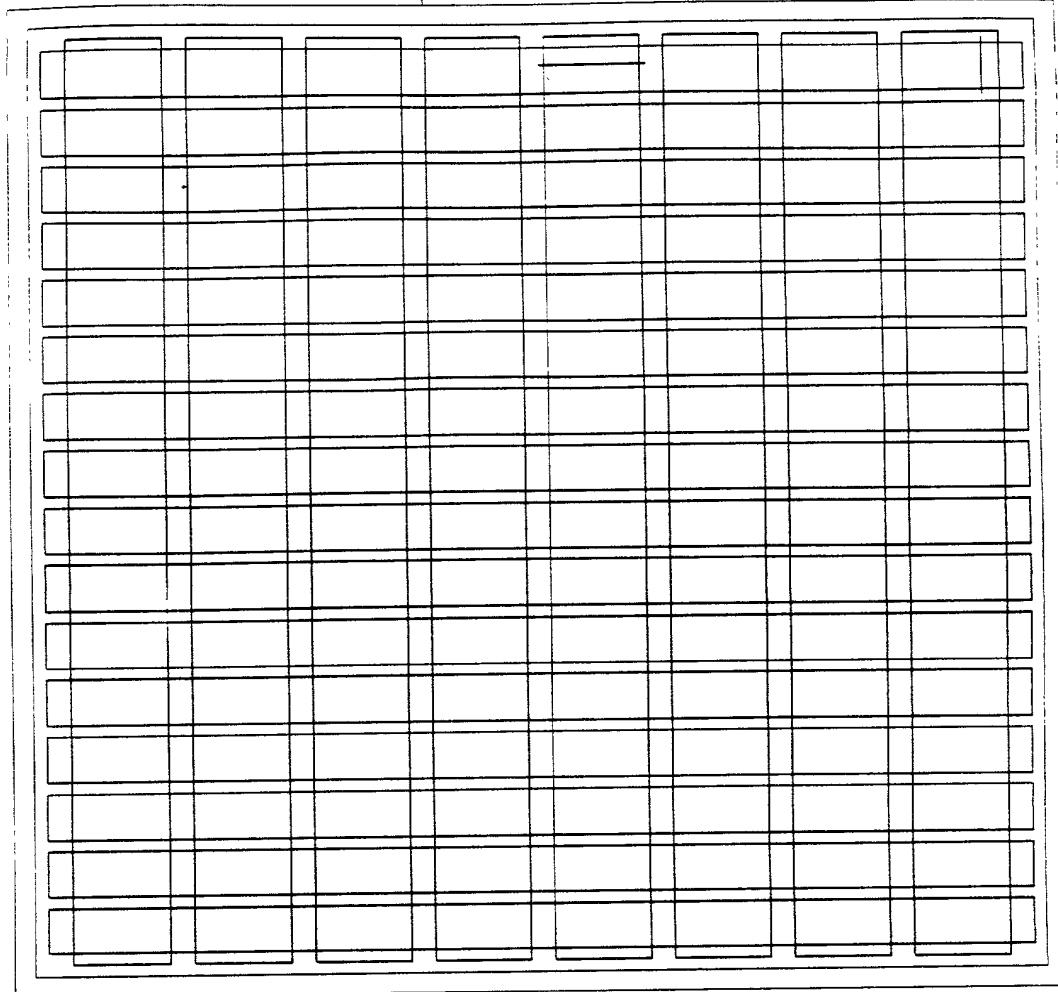
TO  
FIG.  
33D3

TO FIG. 33D2

FIG. 33D1

TO FIG. 33D1

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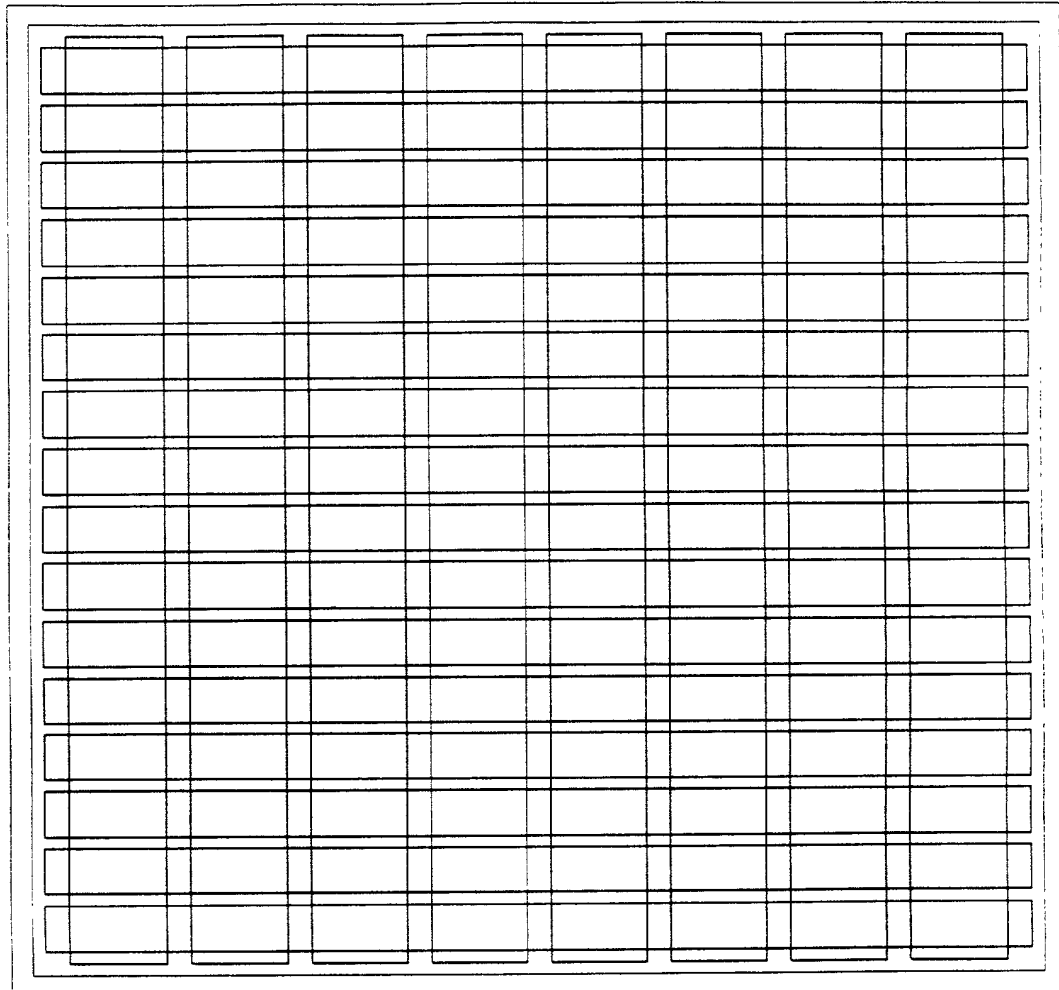


TO  
FIG.  
33D4

FIG. 33D2

TO  
FIG.  
33D1

(SEE FIG. 33E1)

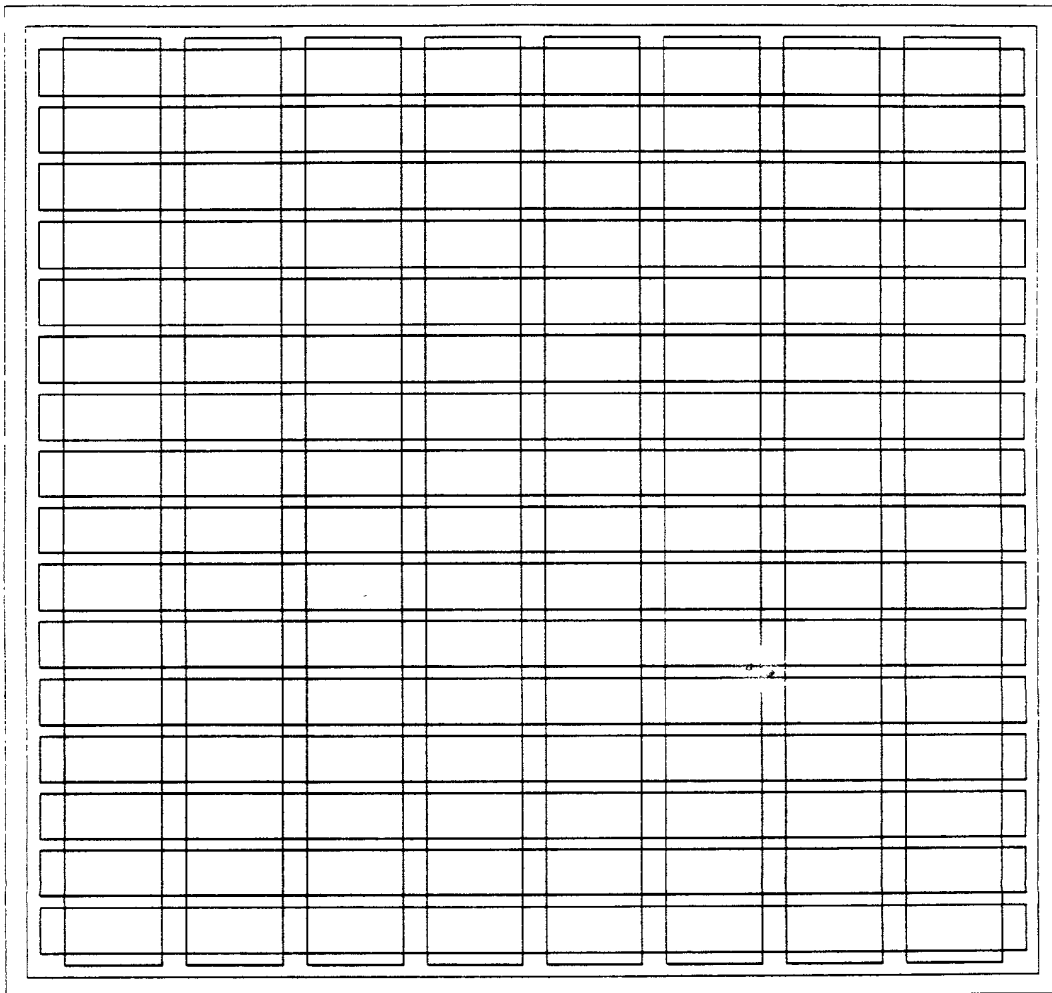


38

TO FIG. 33D4

FIG. 33D3

TO  
FIG.  
33D2



40

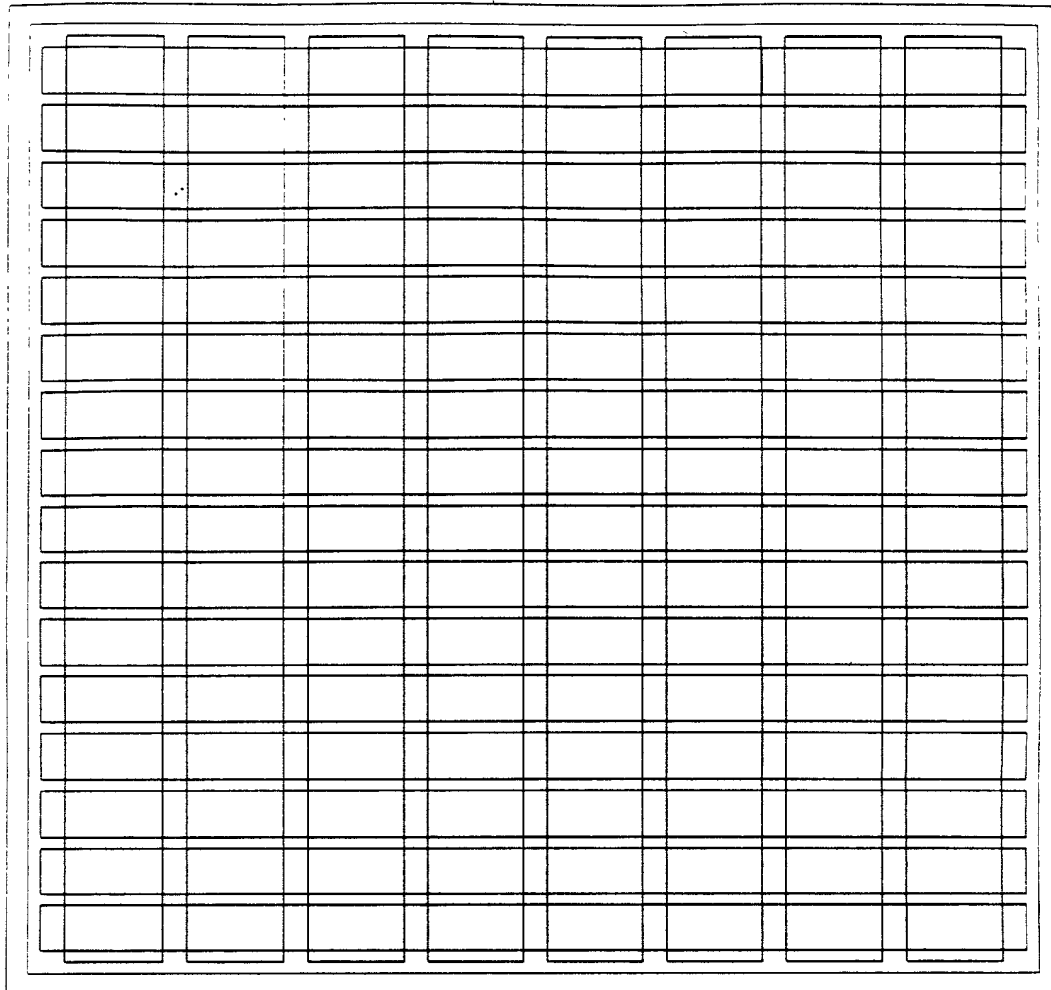
TO FIG. 33D3

FIG. 33D4

(SEE FIG. 33D2)

25

TO FIG. 33E2



DVC2  
GENERATOR 504

□ □ □ □ □ □ □ □ □ □

□ □ □ □ □ □ □ □ □ □

VCCP  
REGULATOR 220

DVC2  
GENERATOR 505

TO  
FIG.  
33E3

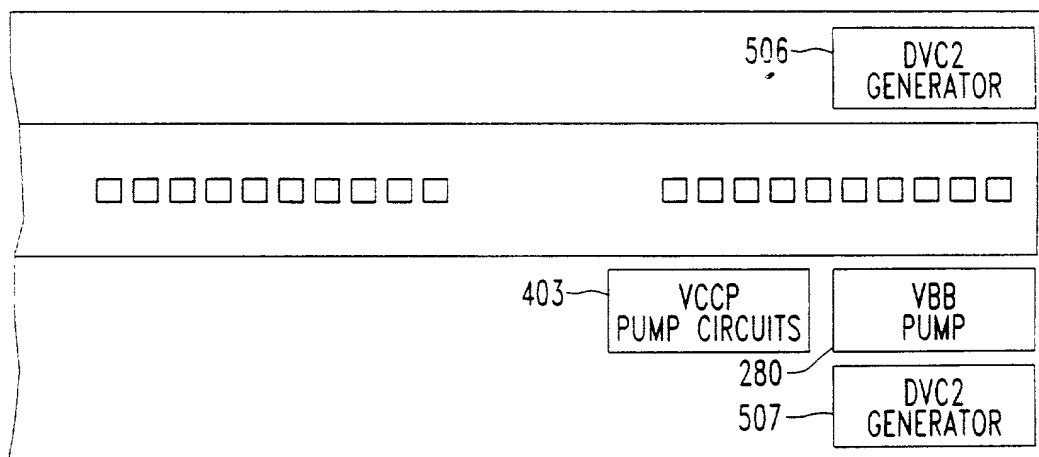
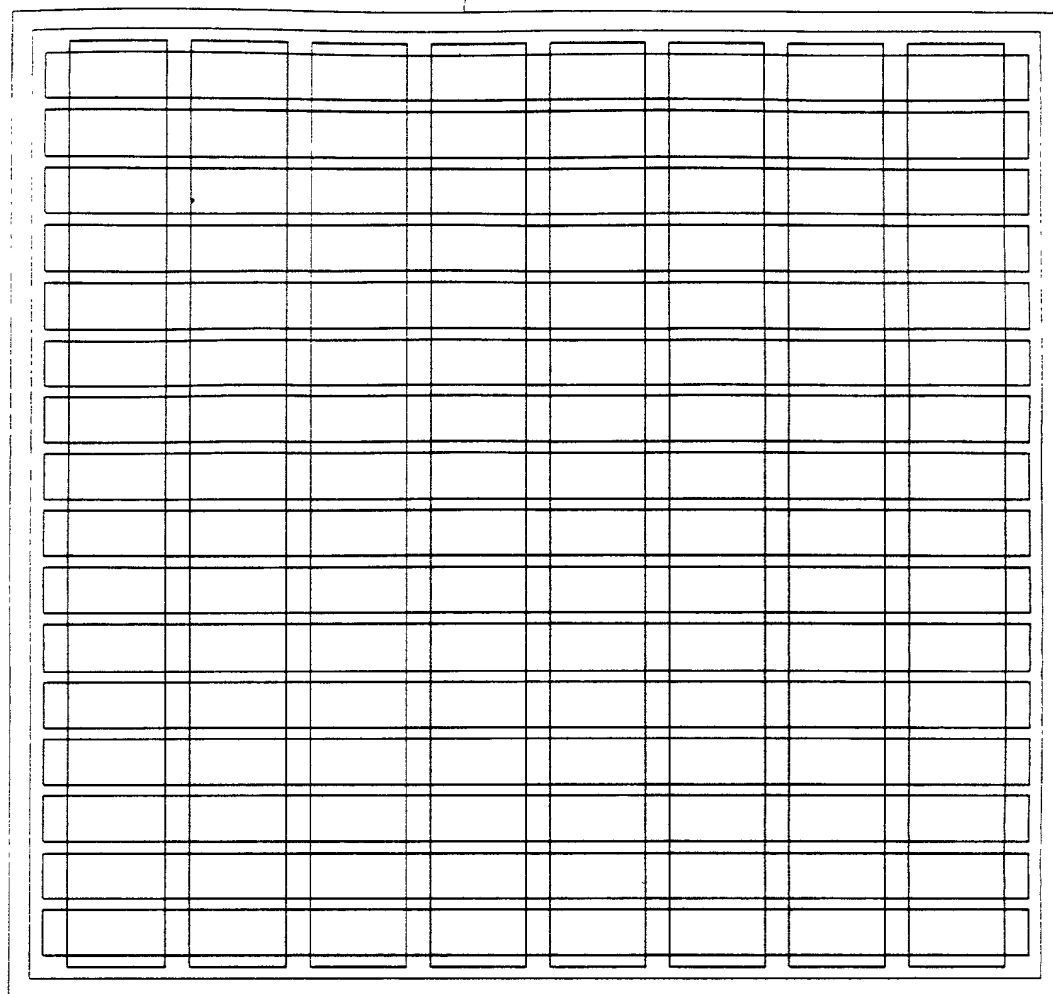
FIG. 33E1

TO FIG. 33E1

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27

10



200

FIG. 33E2

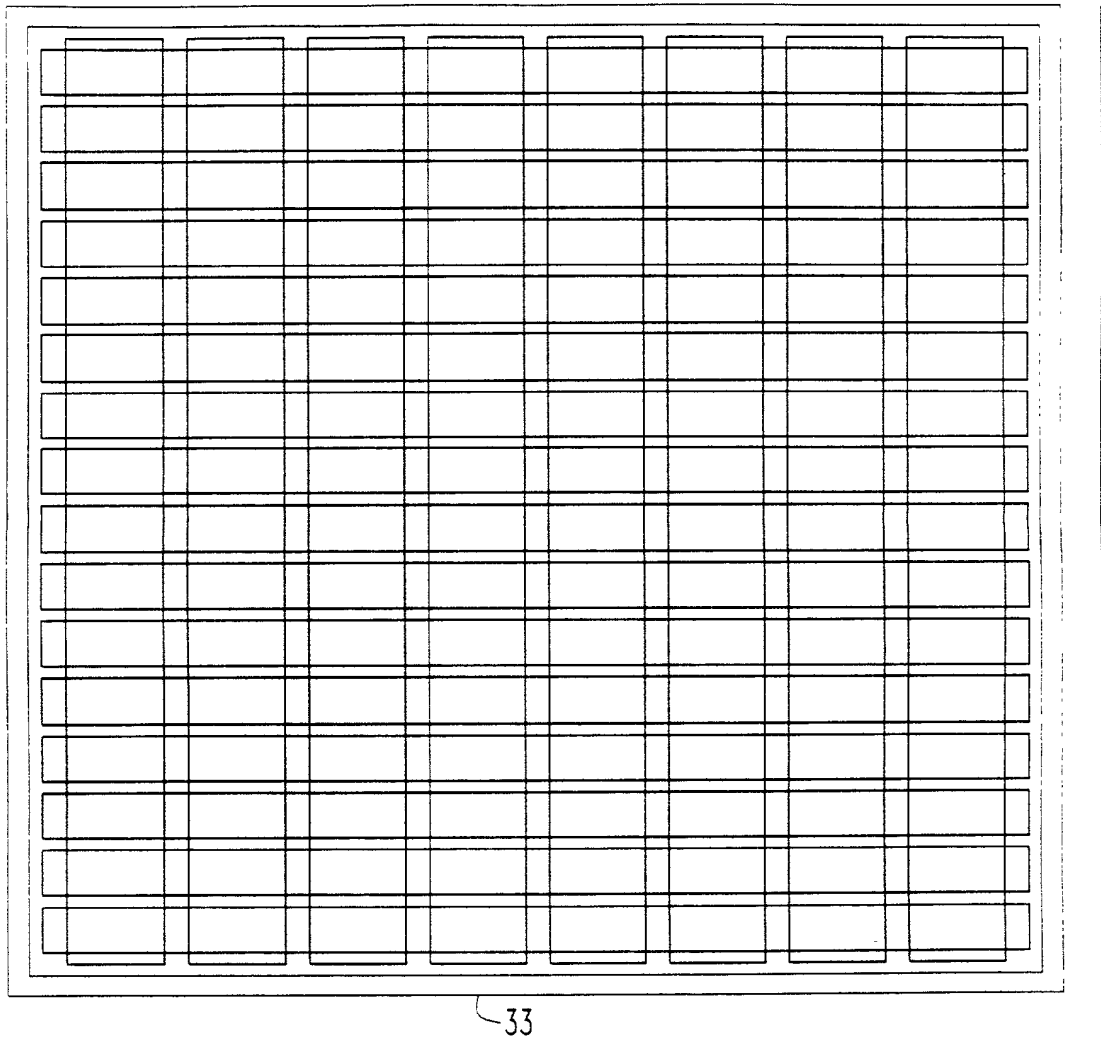
TO FIG. 33E4



FIG. 33E3



FIG. 33E4



TO  
FIG.  
33E2

TO FIG. 33E3

FIG. 33E4

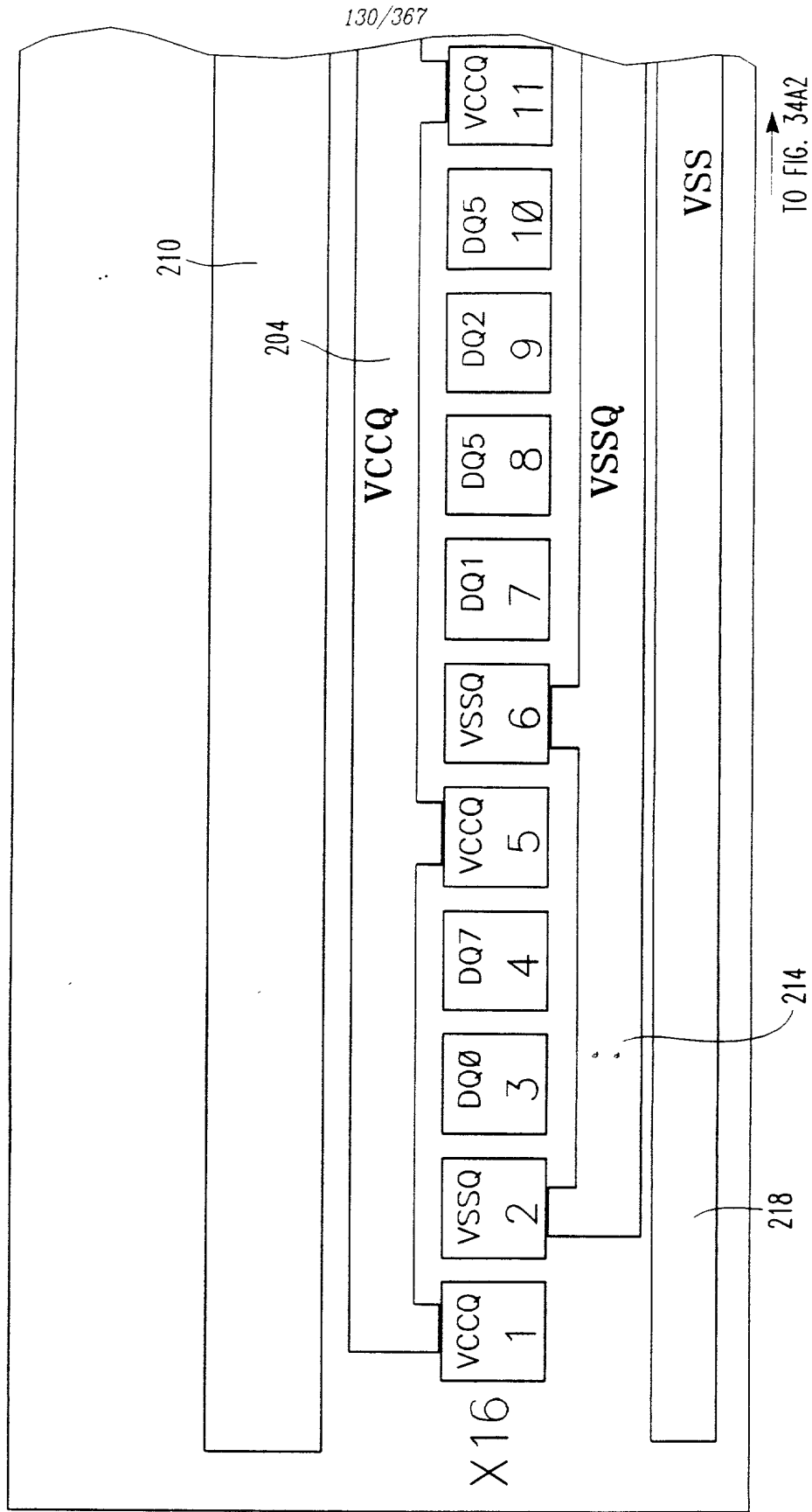
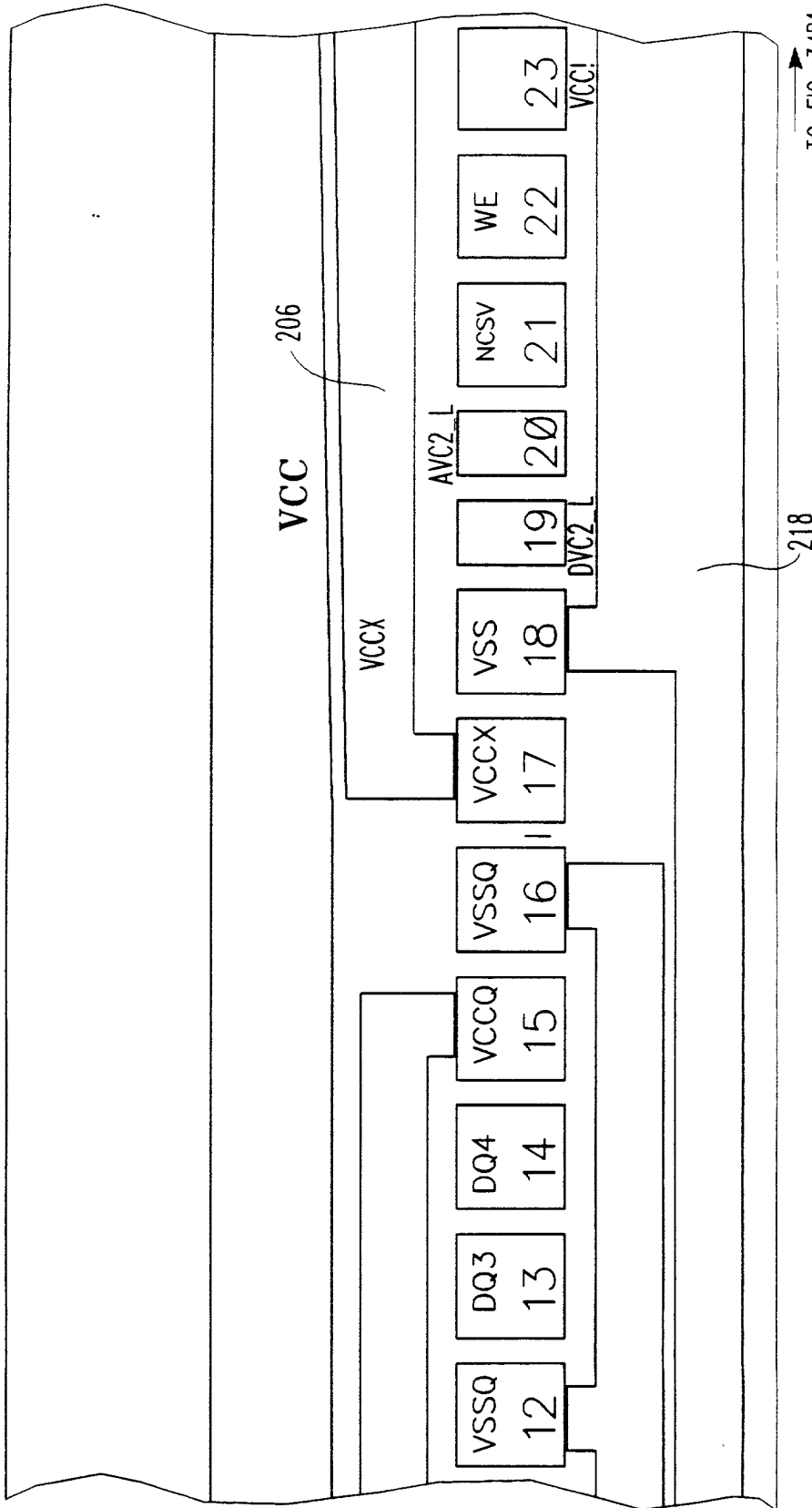


FIG. 34A

FIG. 34A

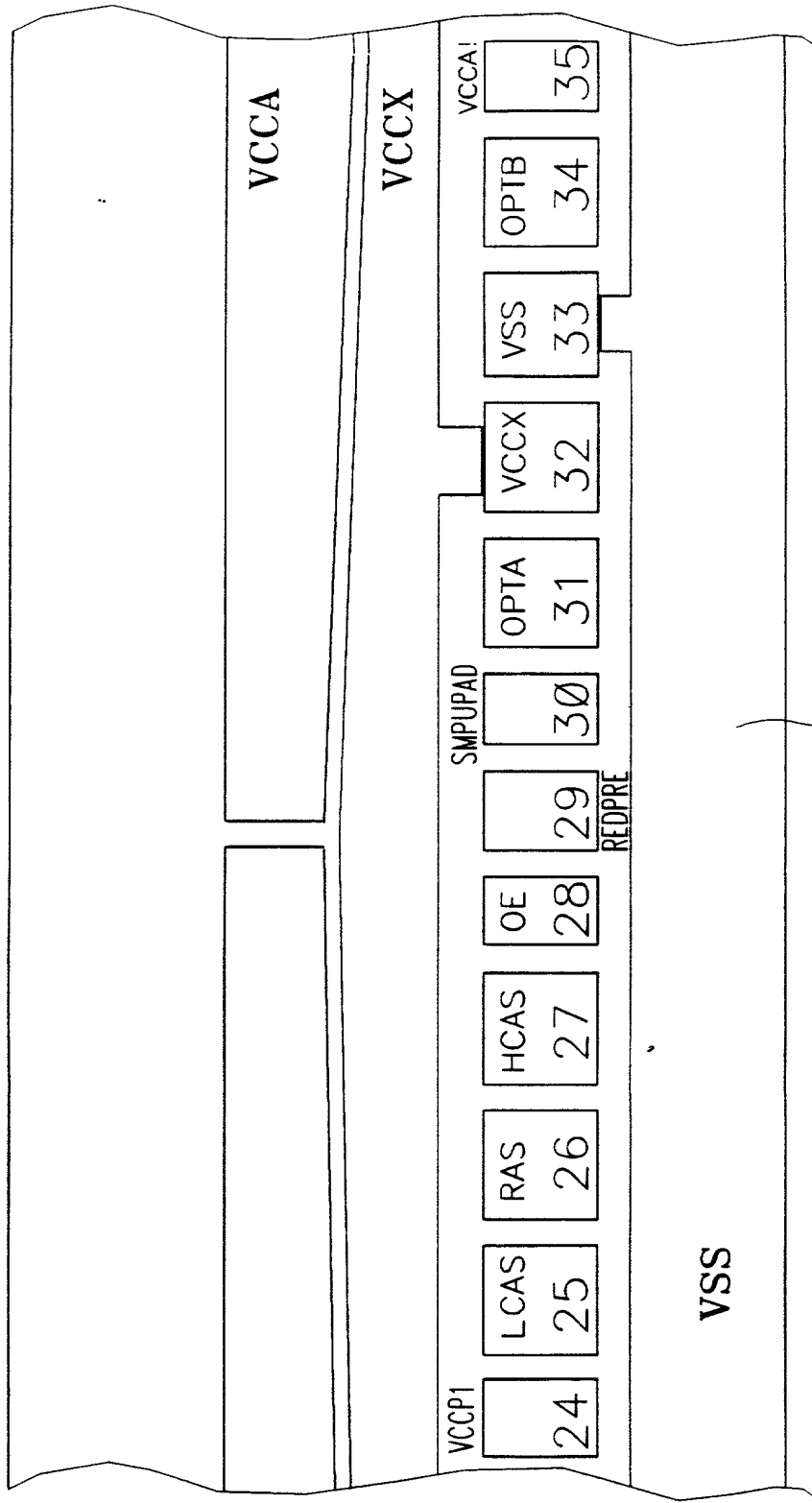
TO FIG. 34A1



TO FIG. 34B1

FIG. 34A2

TO FIG. 34A2



TO FIG. 34B2

FIG. 34B1

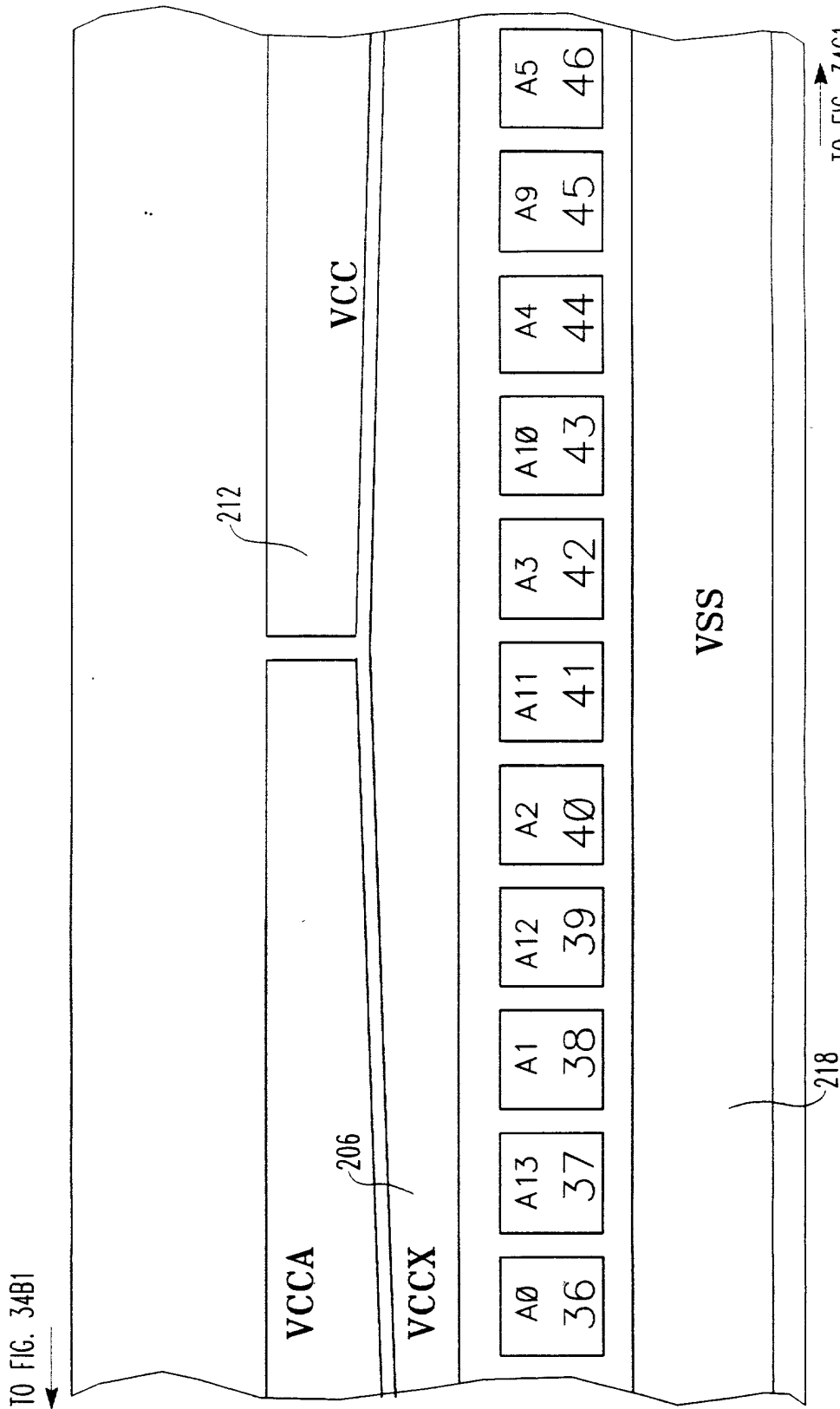
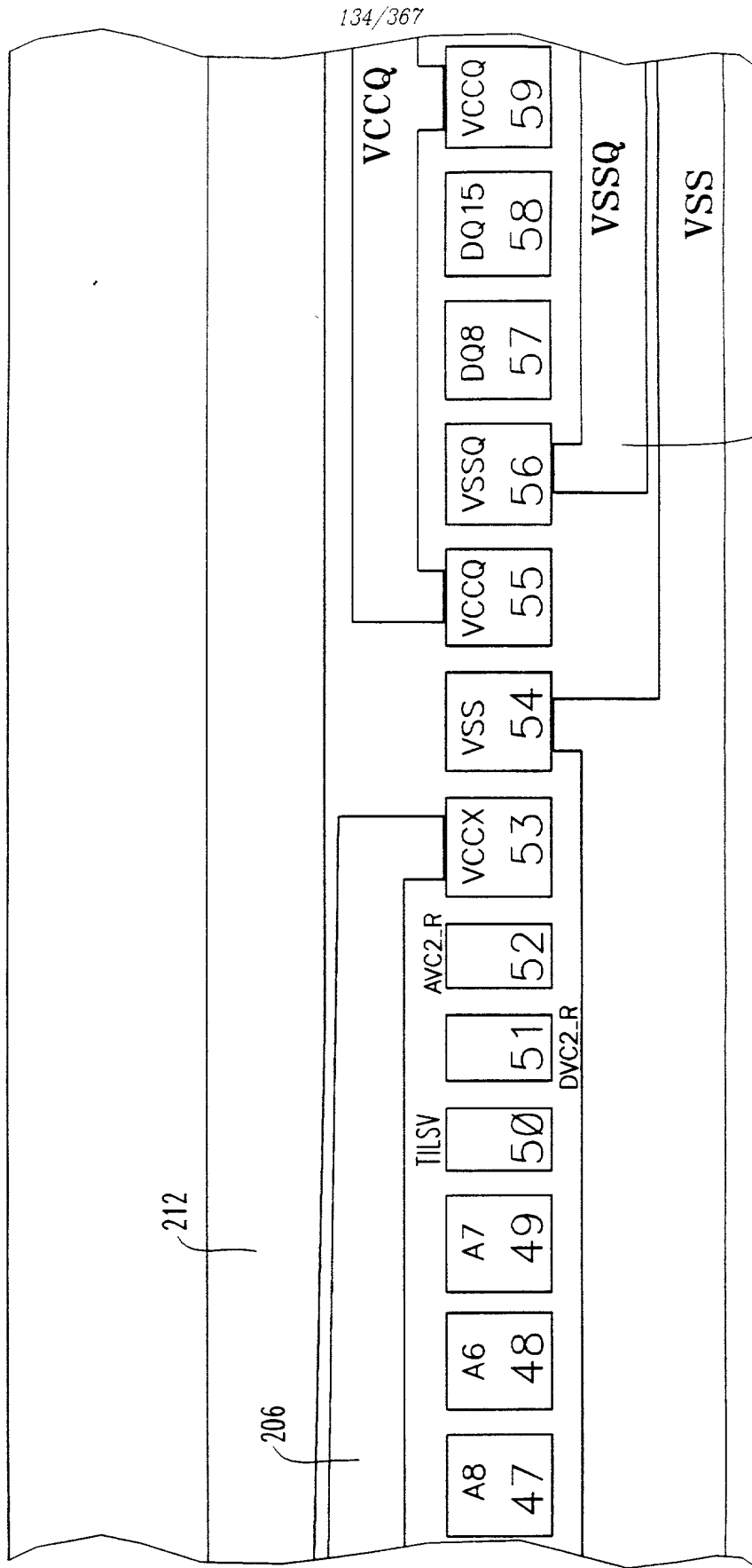


FIG. 34B2

TO FIG. 34B2



TO FIG. 34C2

FIG. 34C1

TO FIG. 34C1

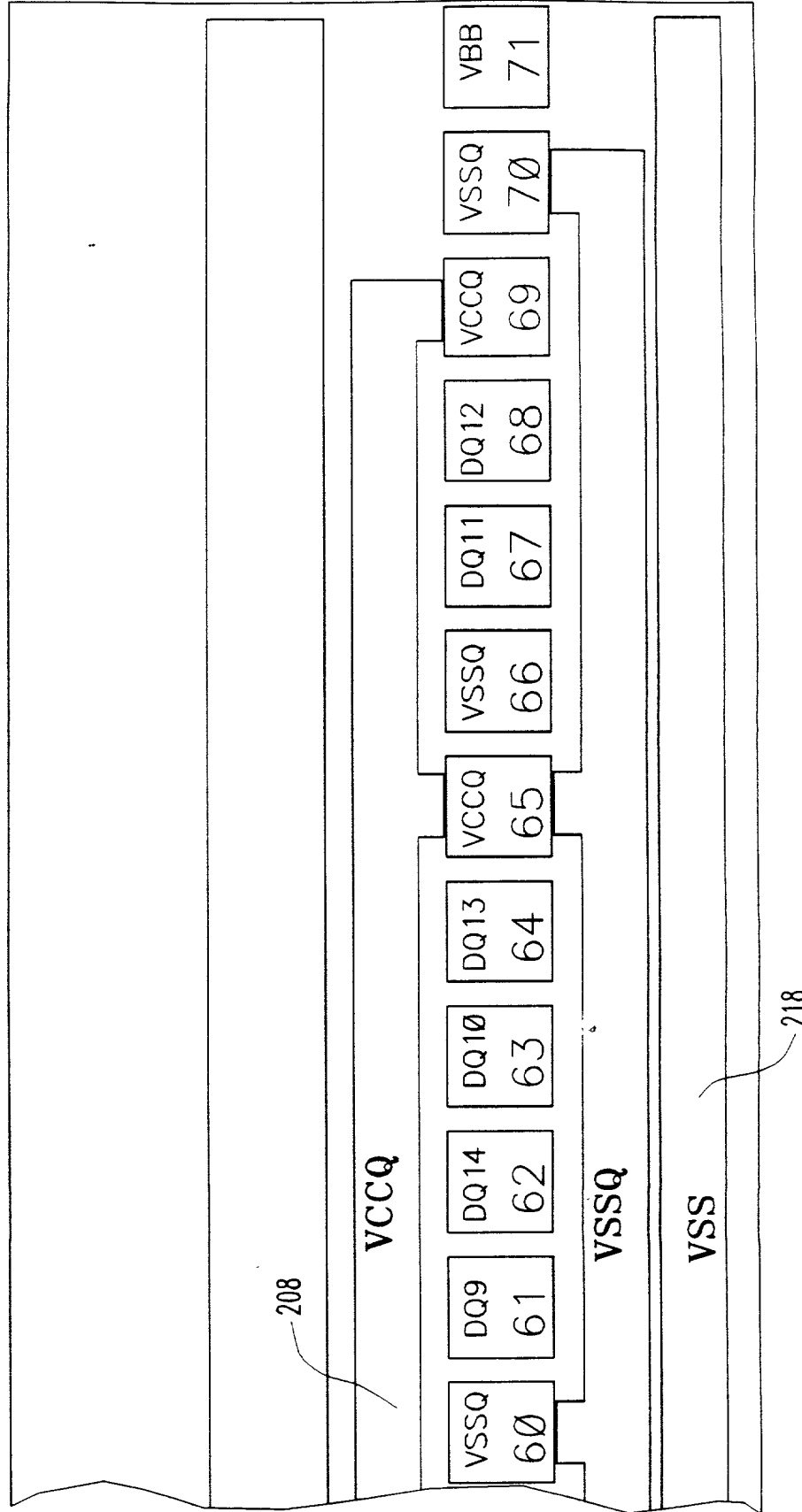


FIG. 34C2

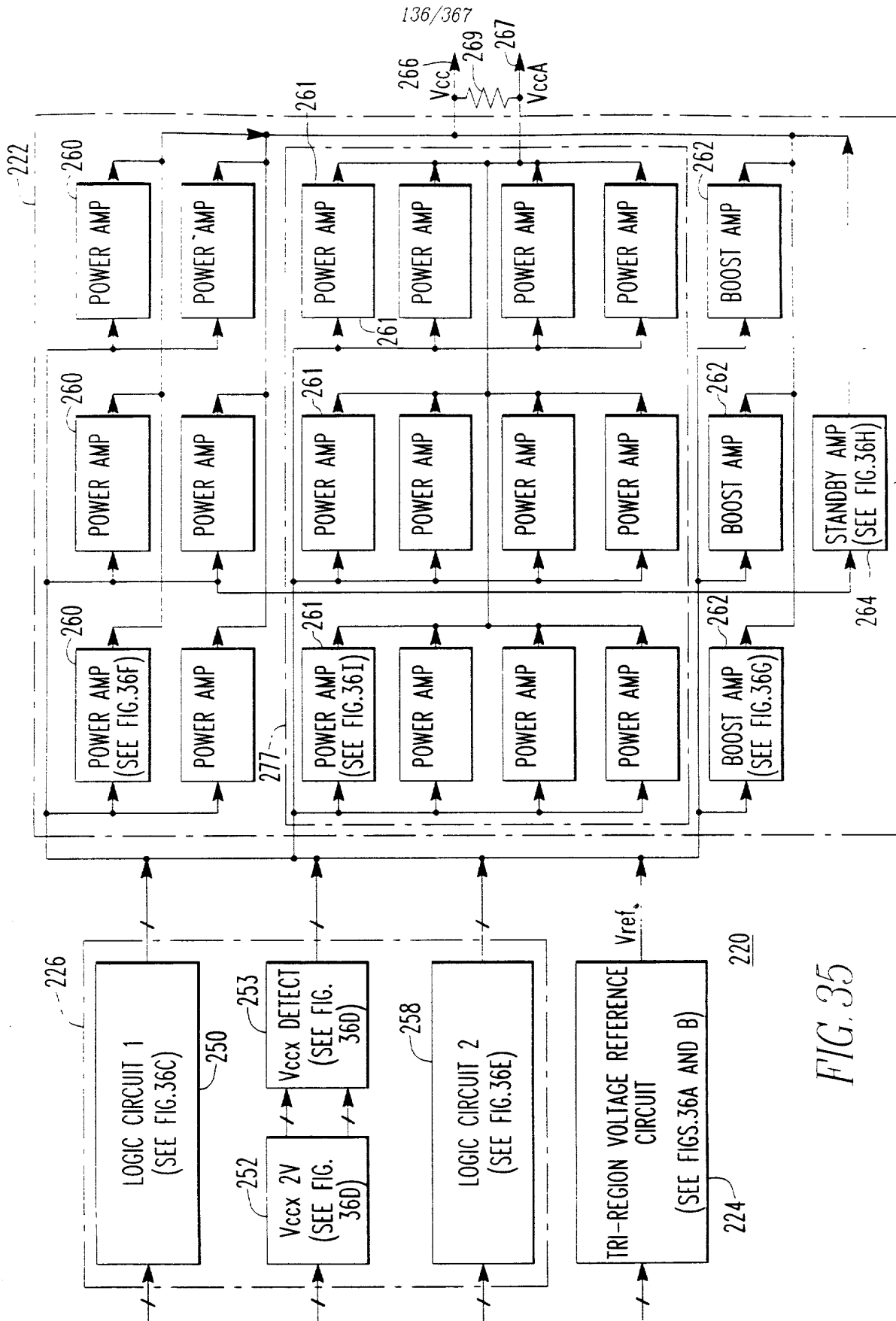


FIG. 35



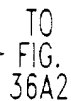


FIG. 36A1

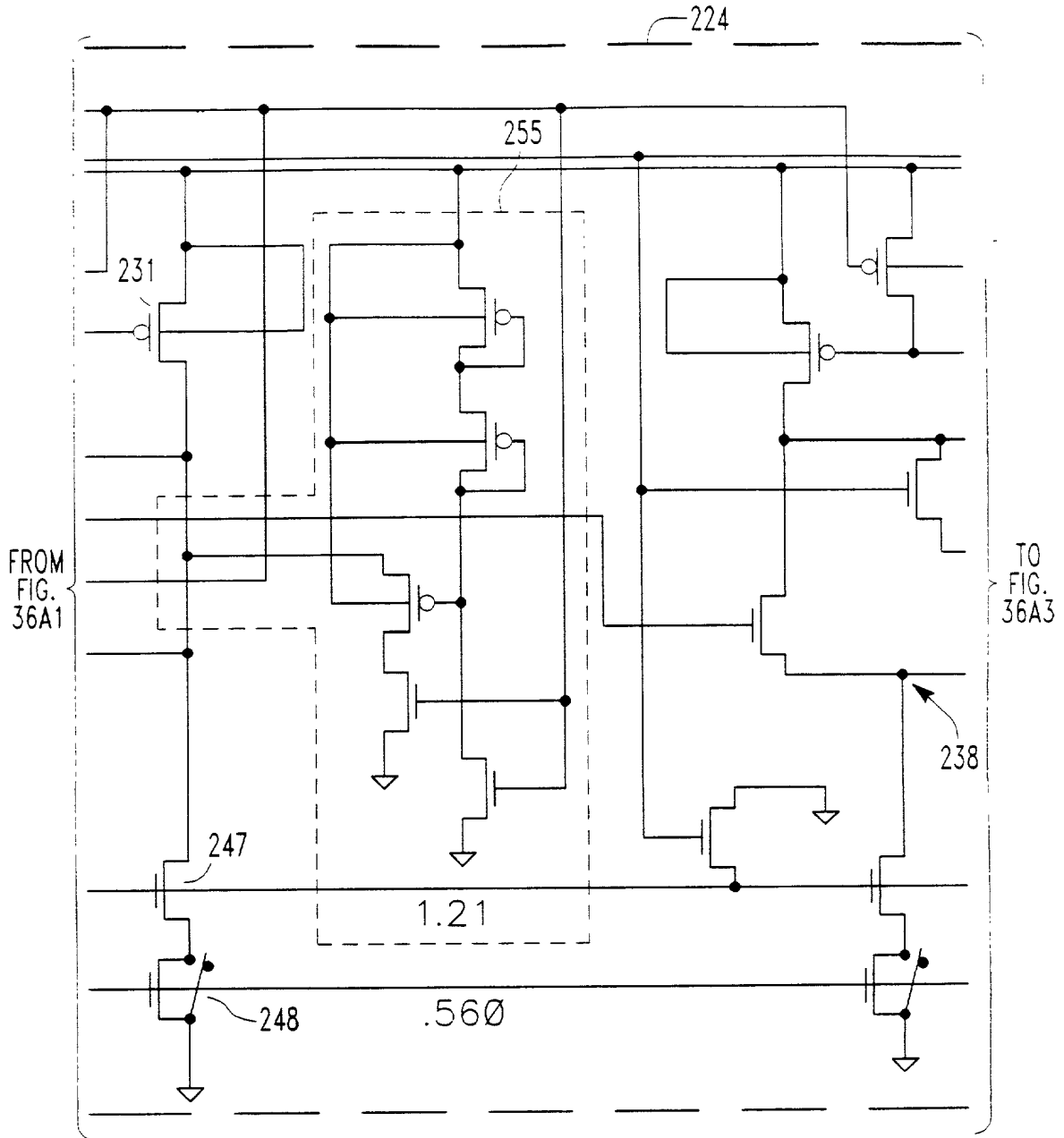


FIG. 36A2

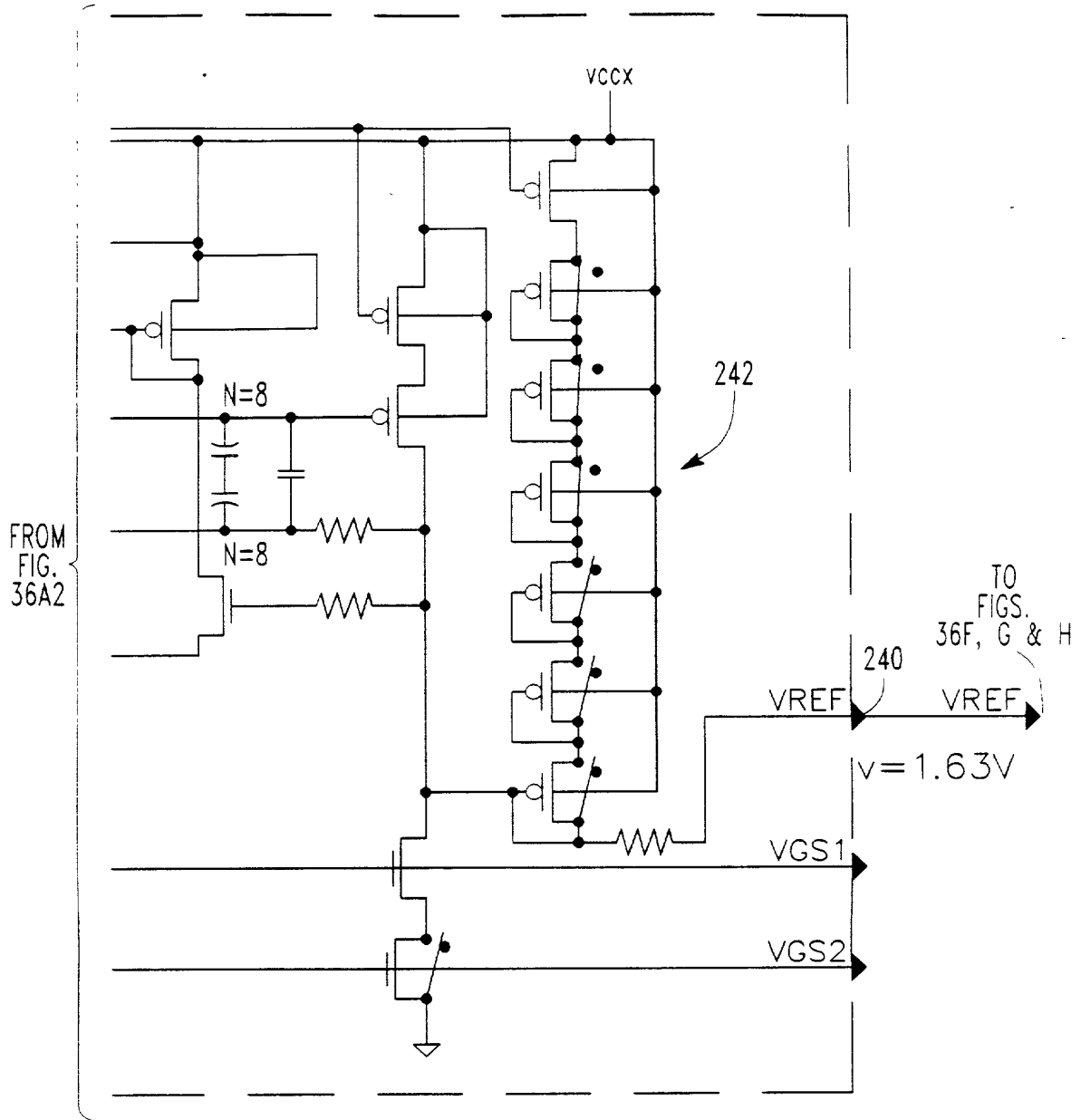


FIG. 36A3

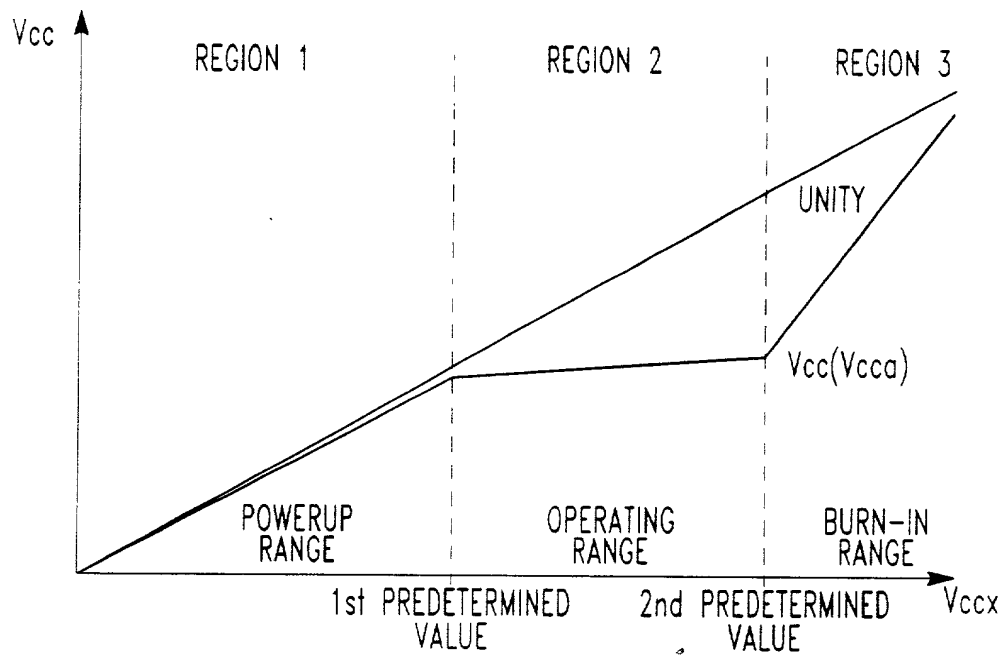


FIG. 36B



FIG. 36C1

[illegible]

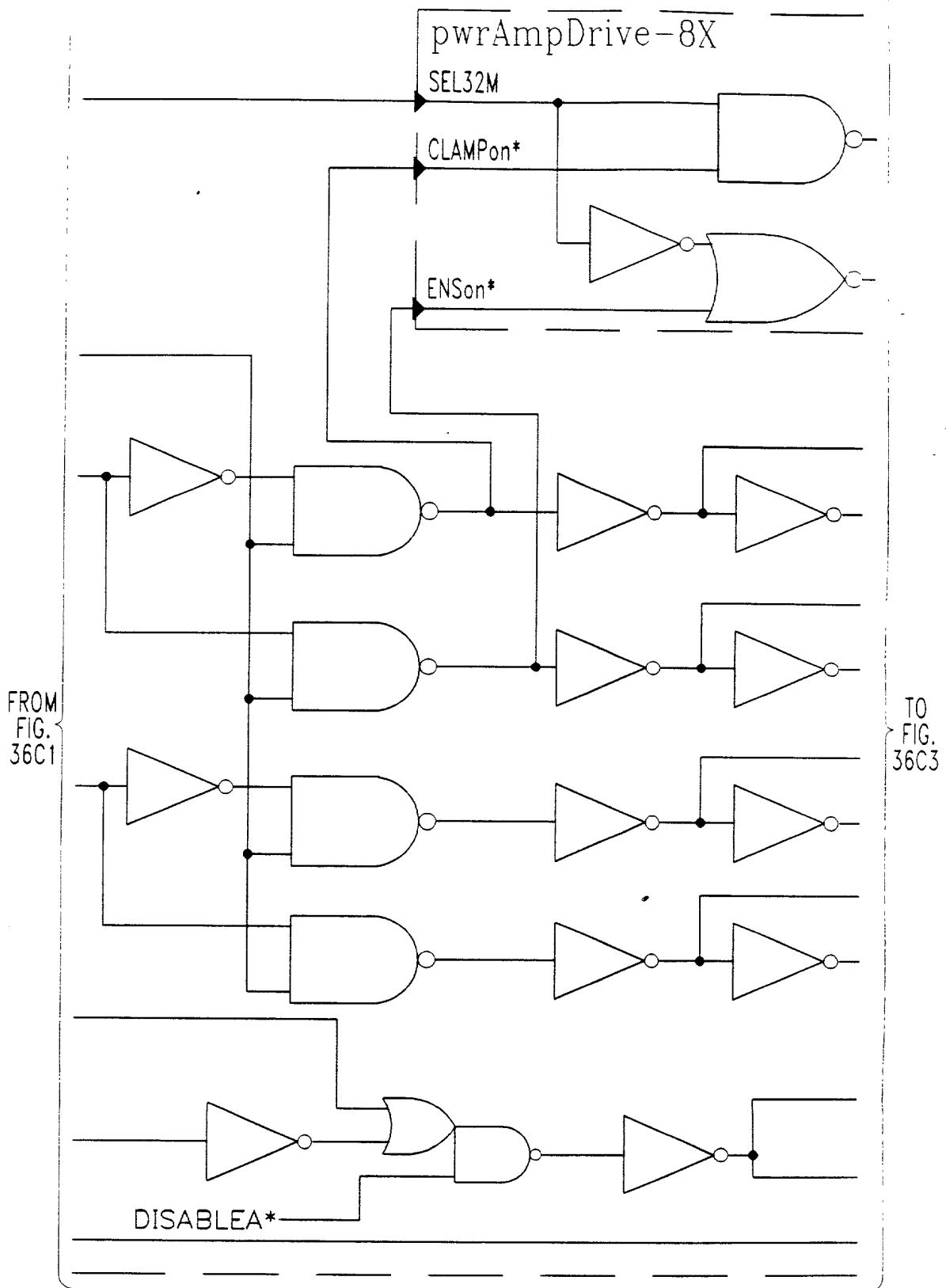


FIG. 36C2

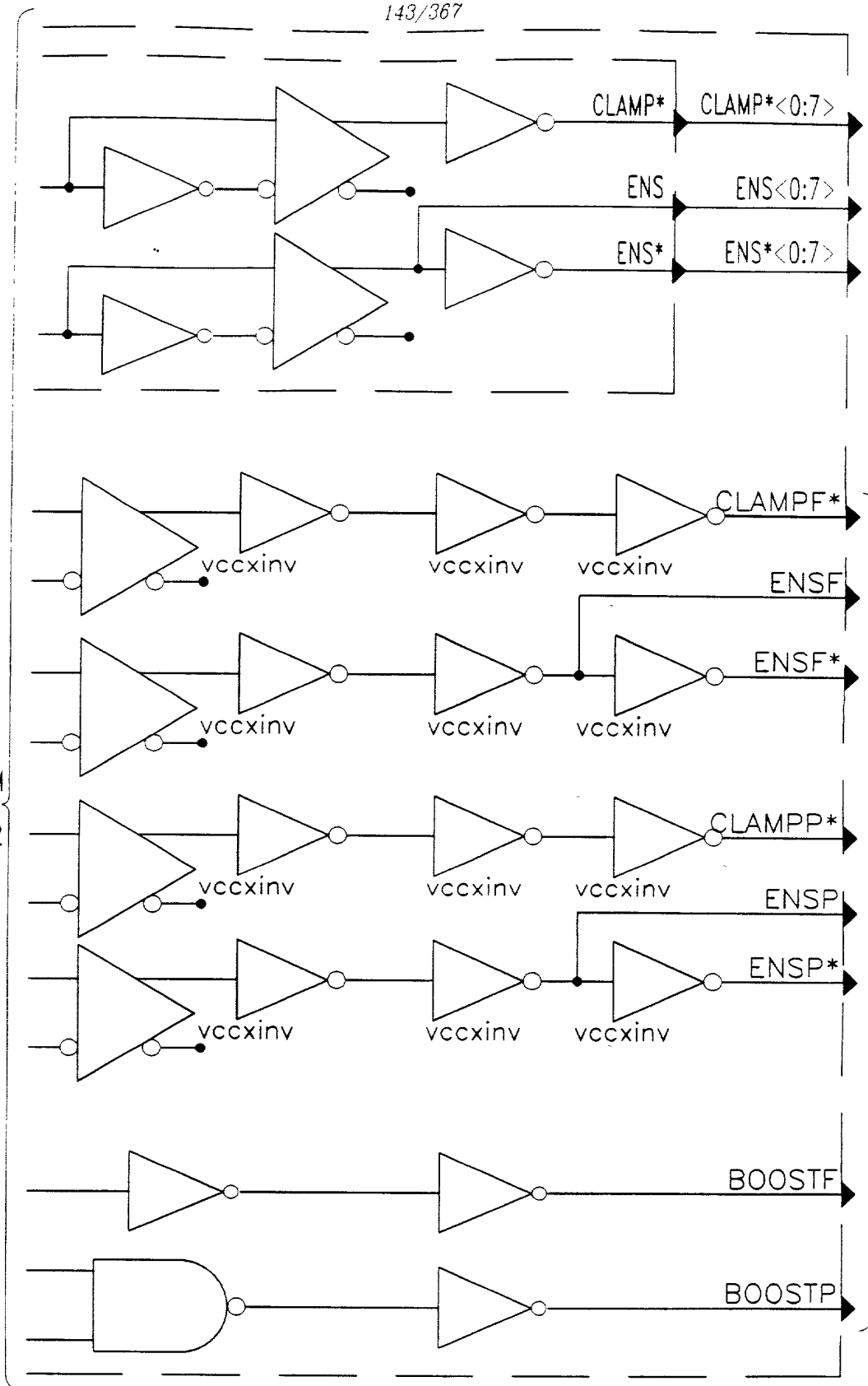
FROM  
FIG.  
36C2TO  
FIG.  
36F

FIG. 36C3

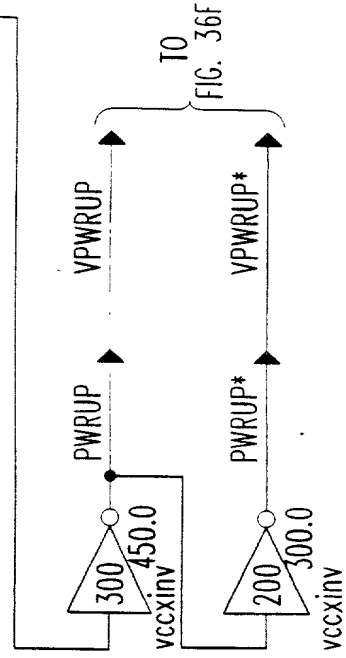
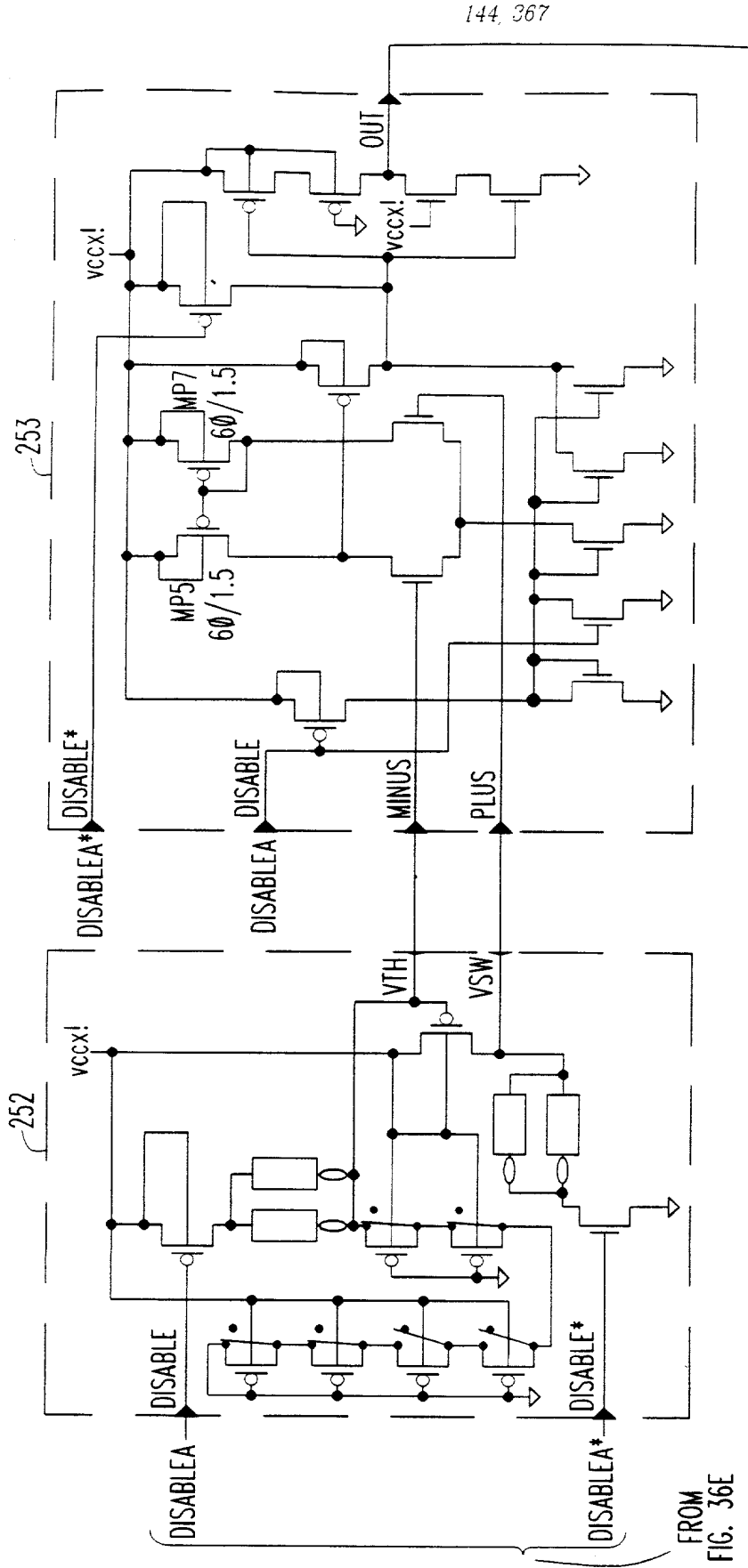


FIG. 36D





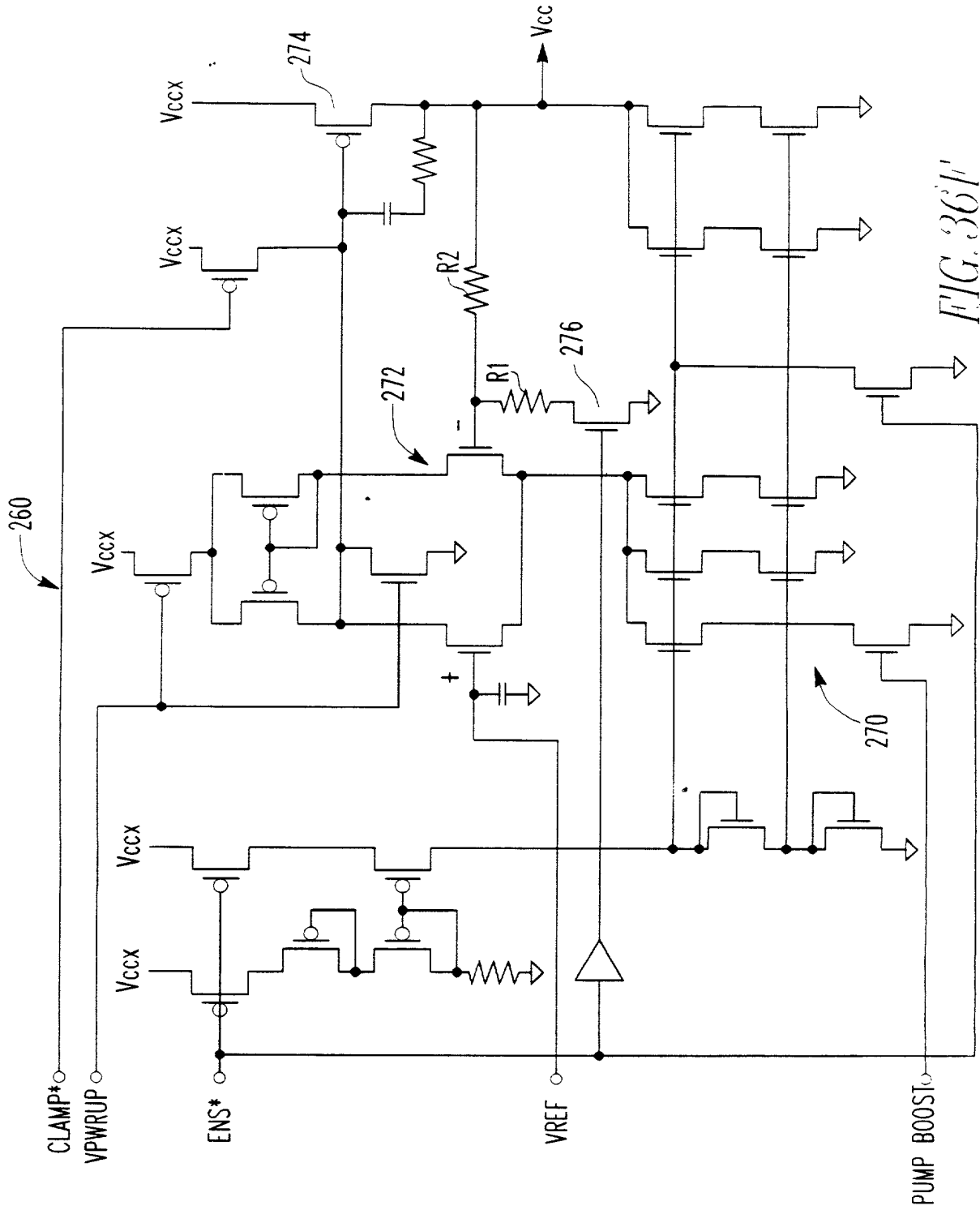


FIG. 36F

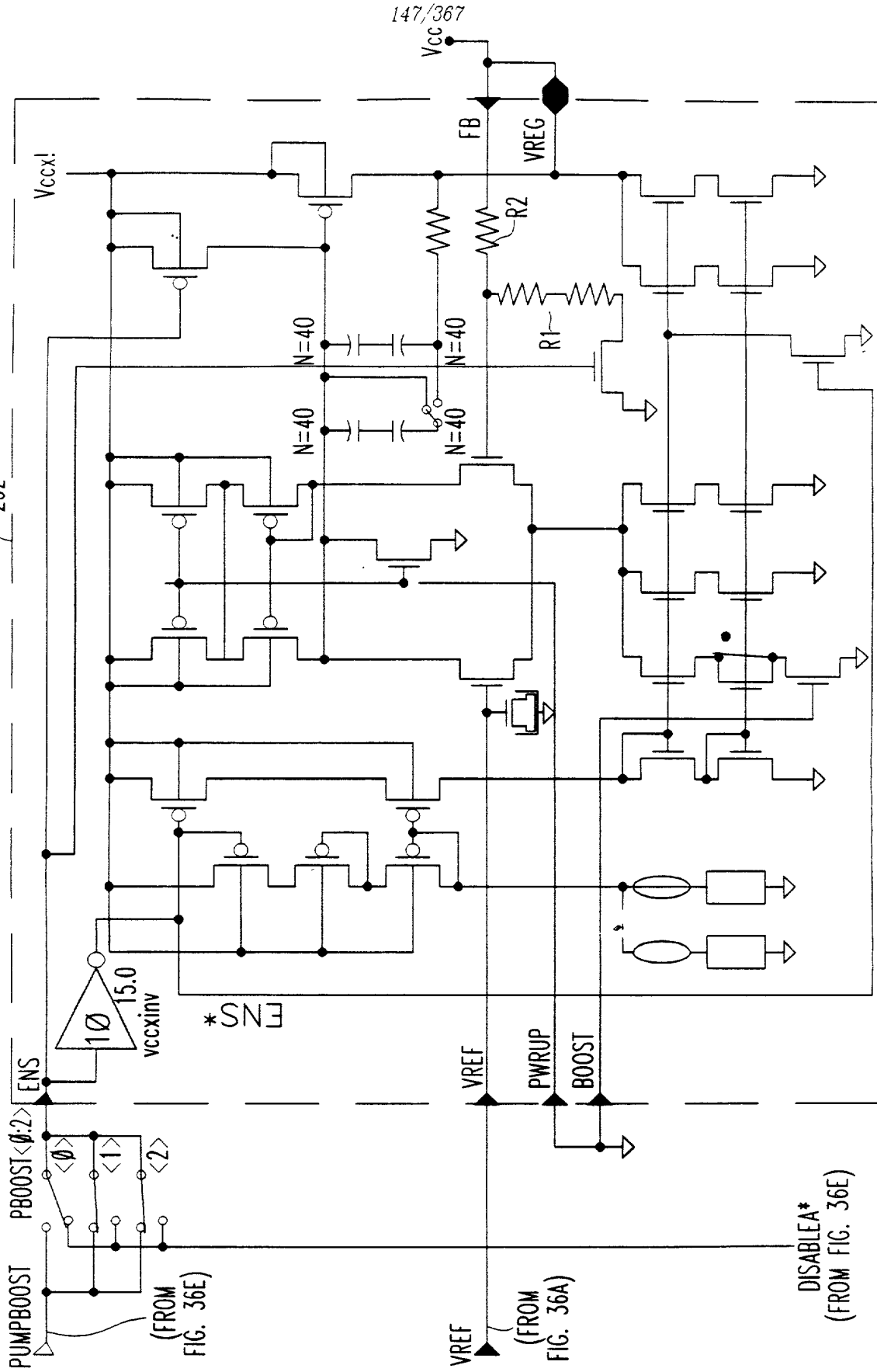


FIG. 36G

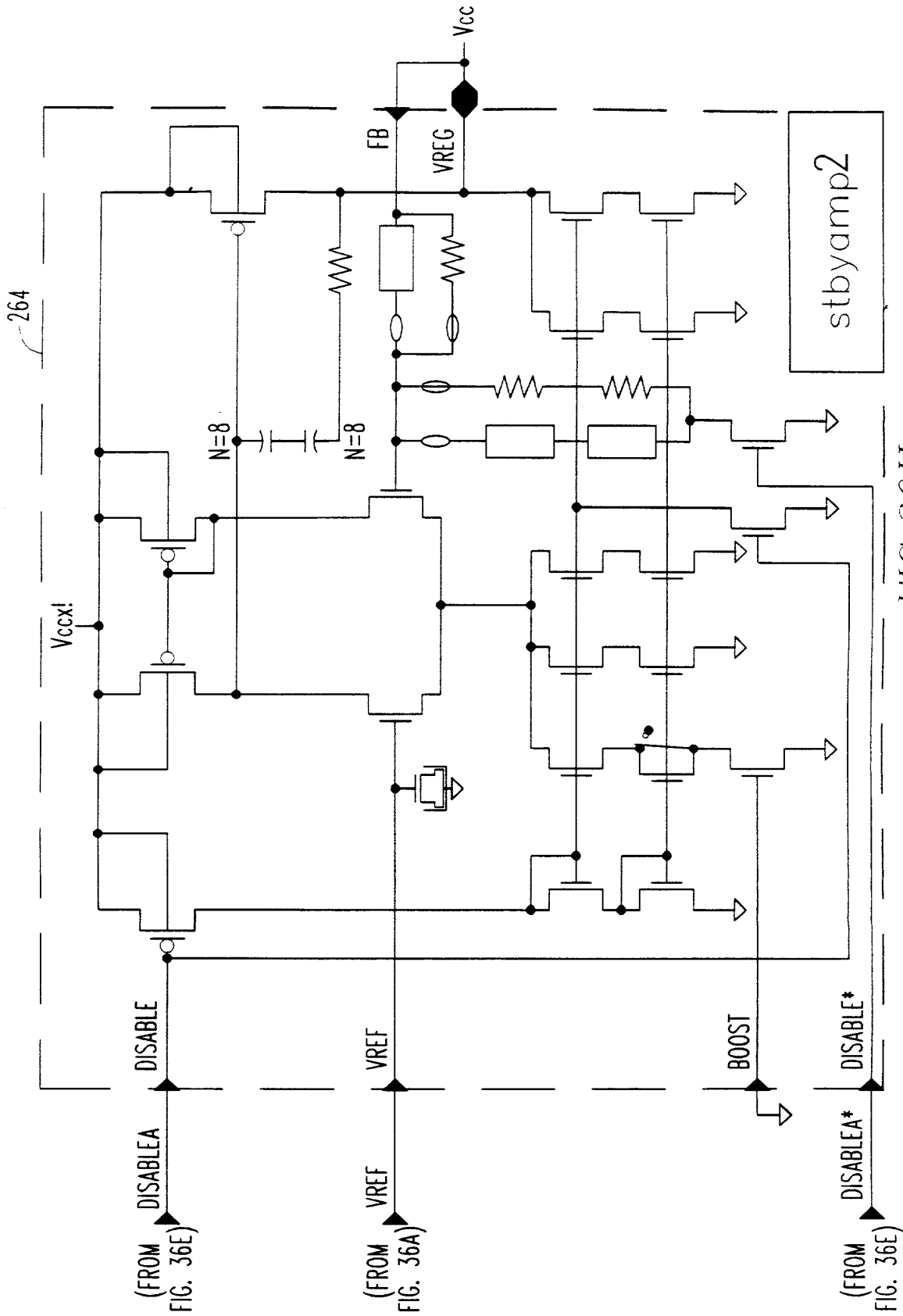


FIG. 36H

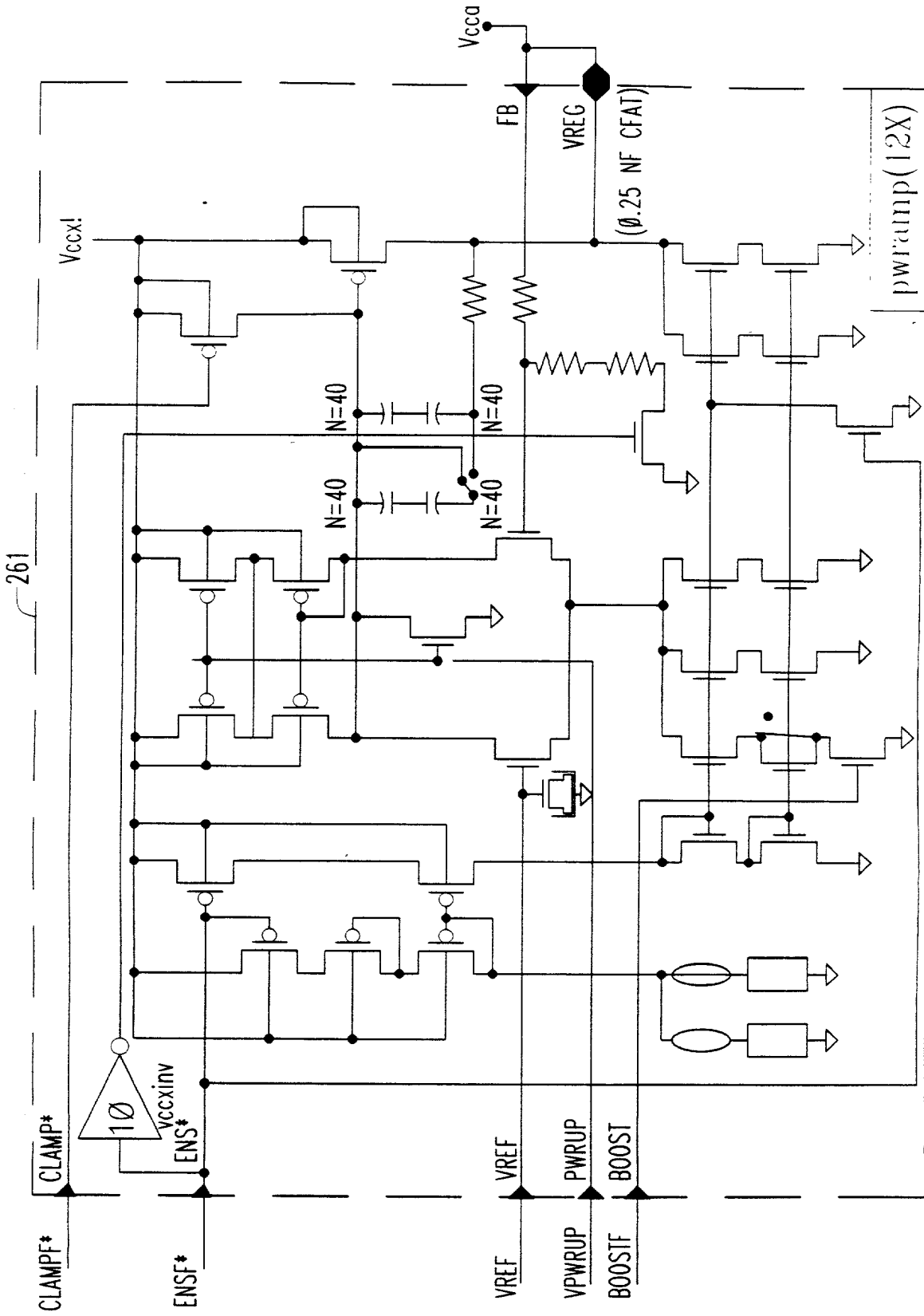


FIG. 361

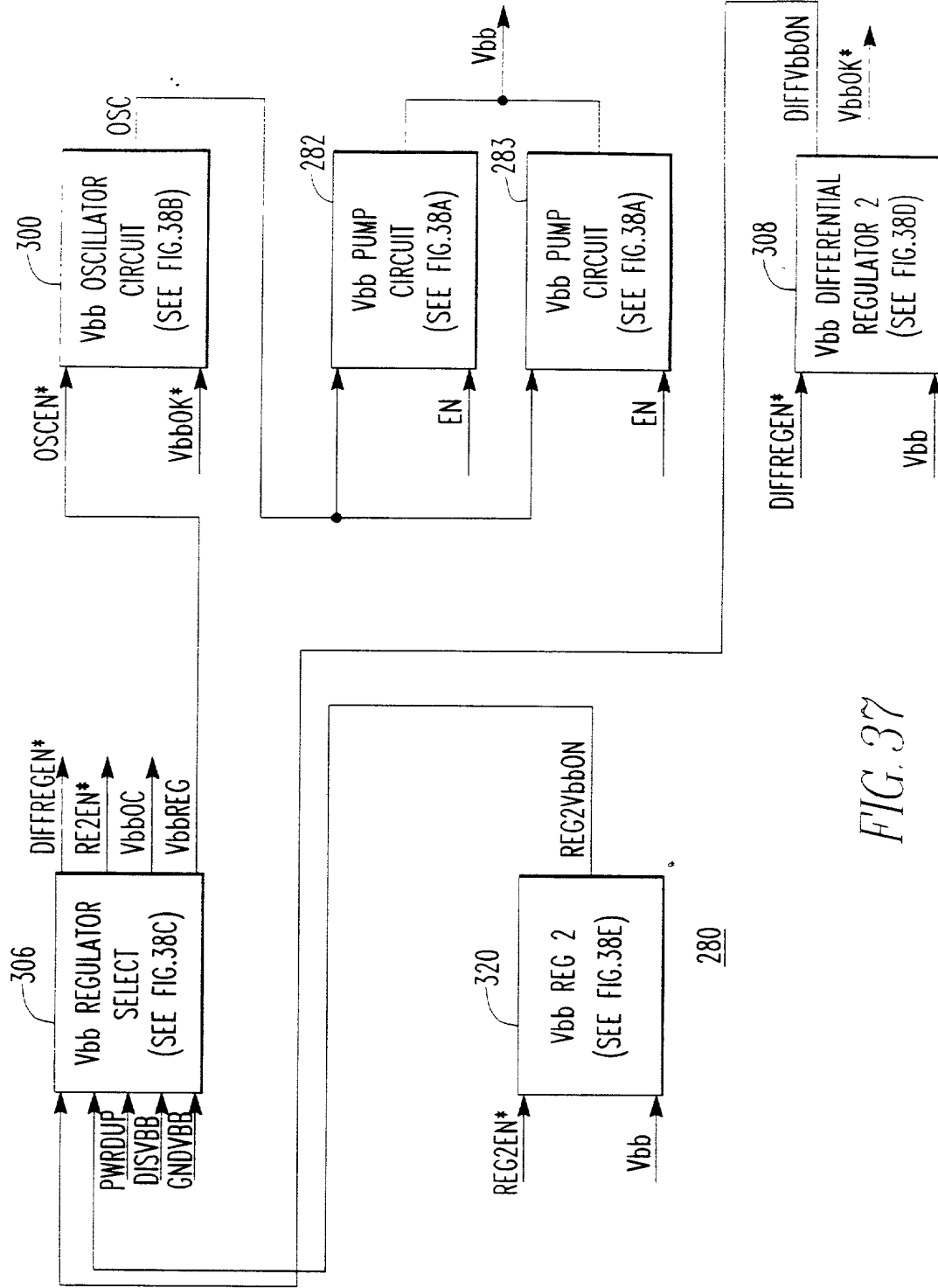


FIG. 37

FIG. 38B)

FIG. 38C) OSCEN\*

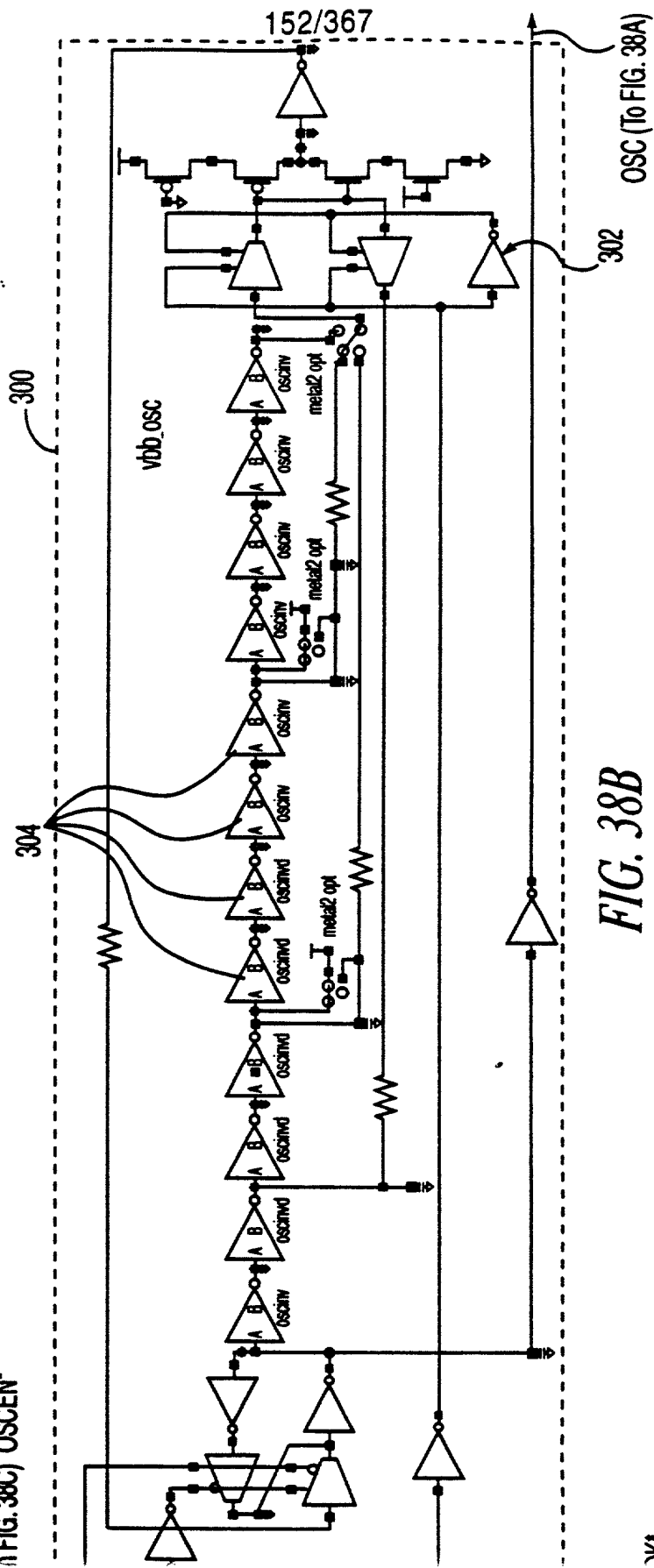


FIG. 38B



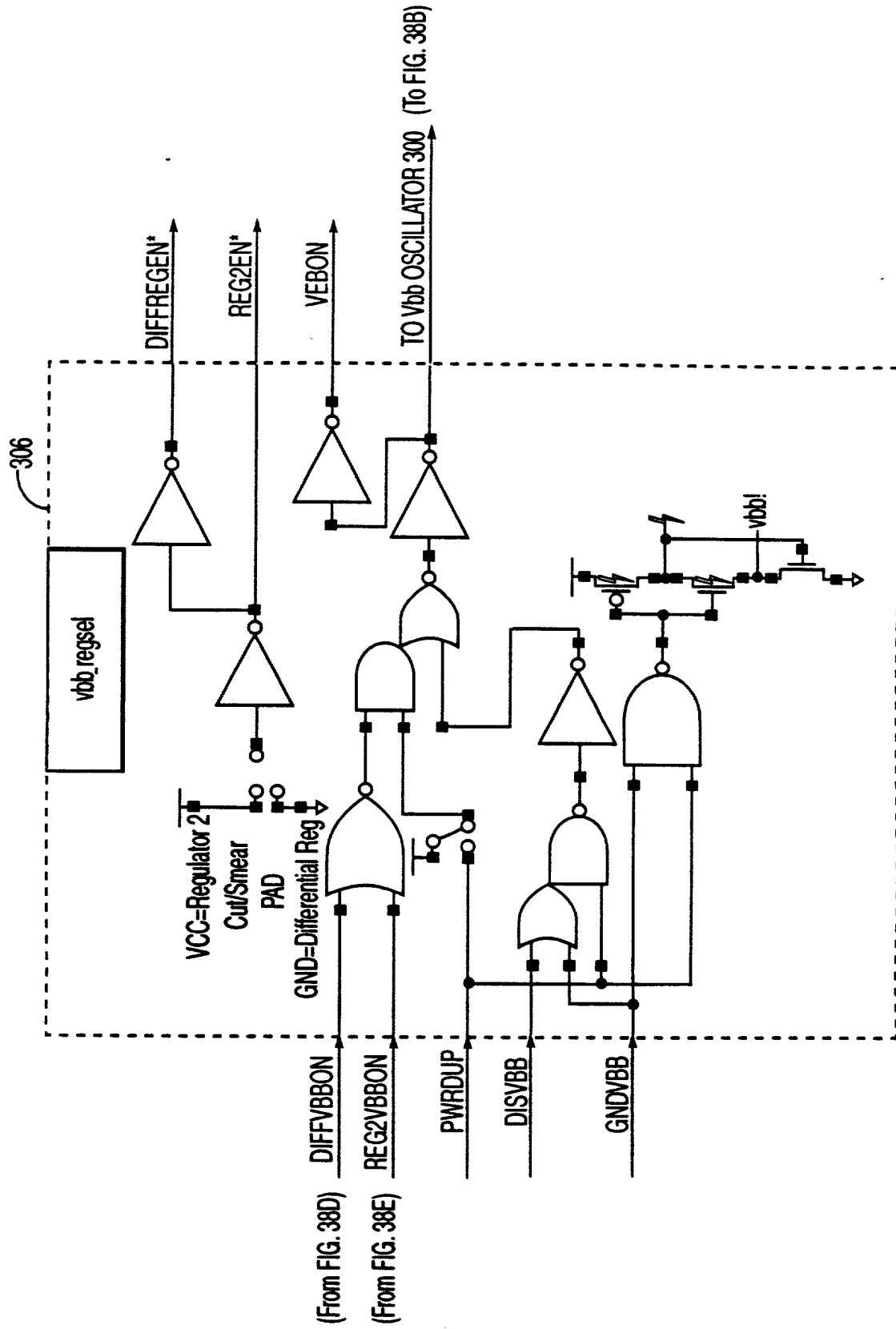


FIG. 38C

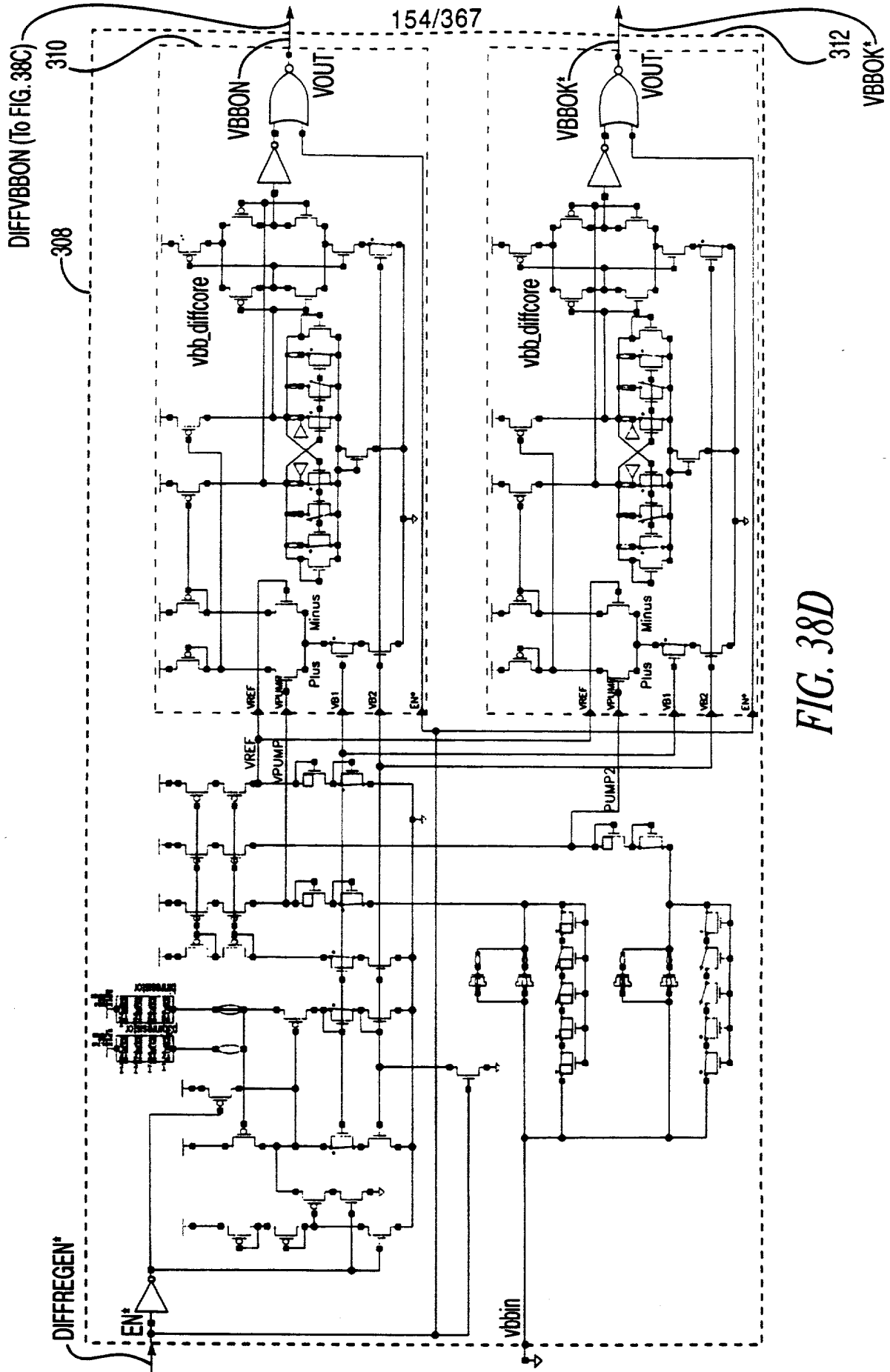


FIG. 38D

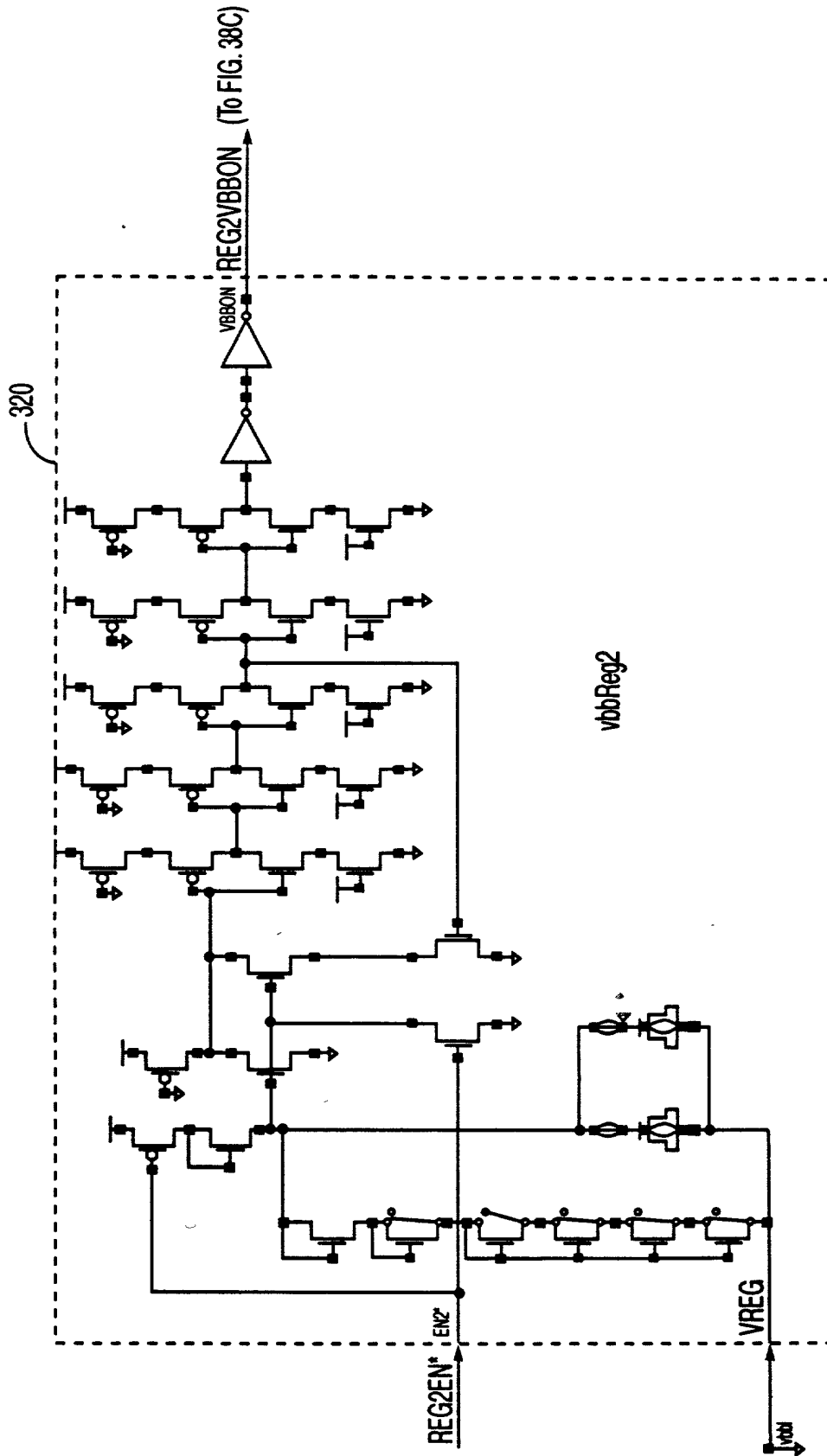


FIG. 38E

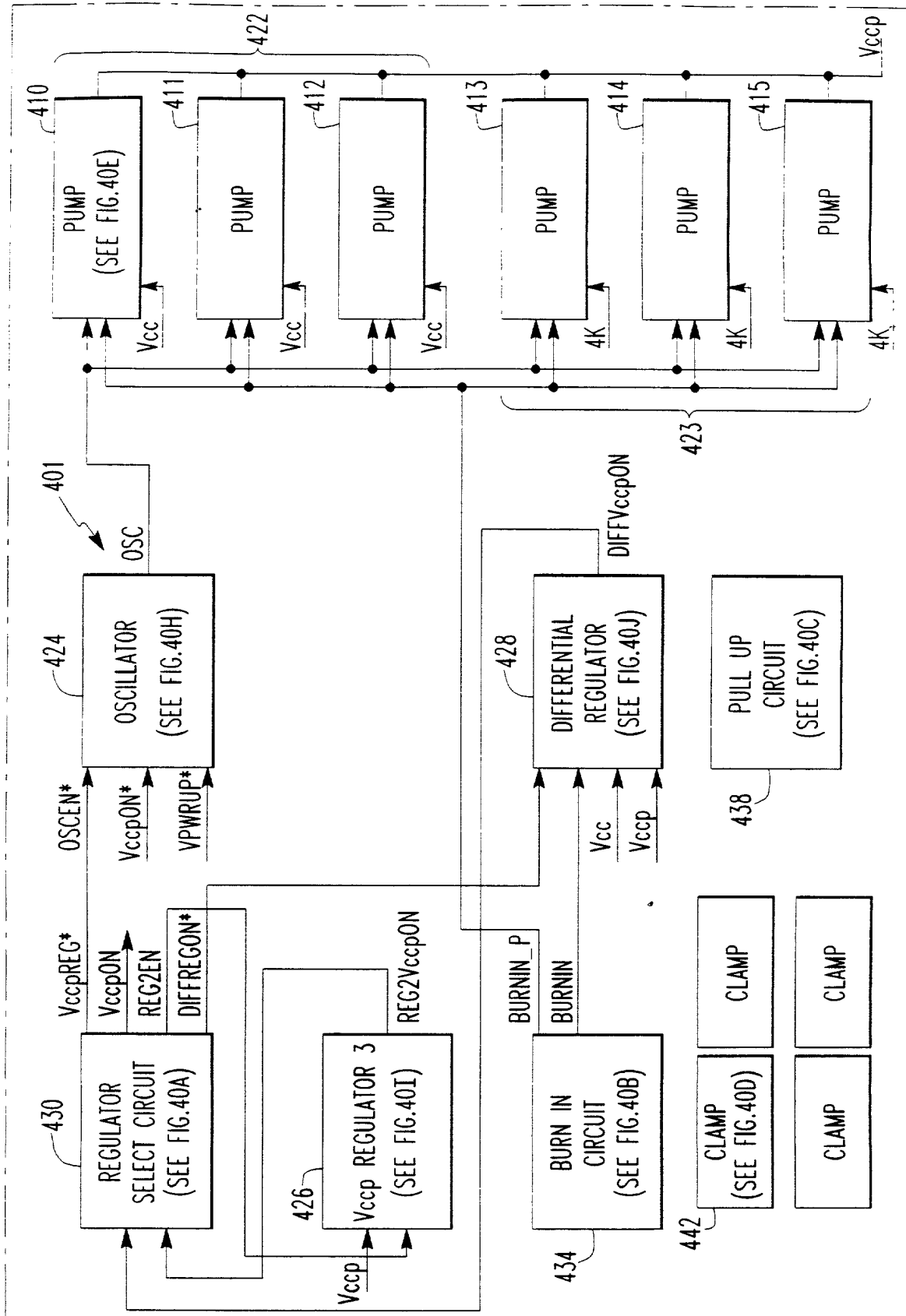


FIG. 39

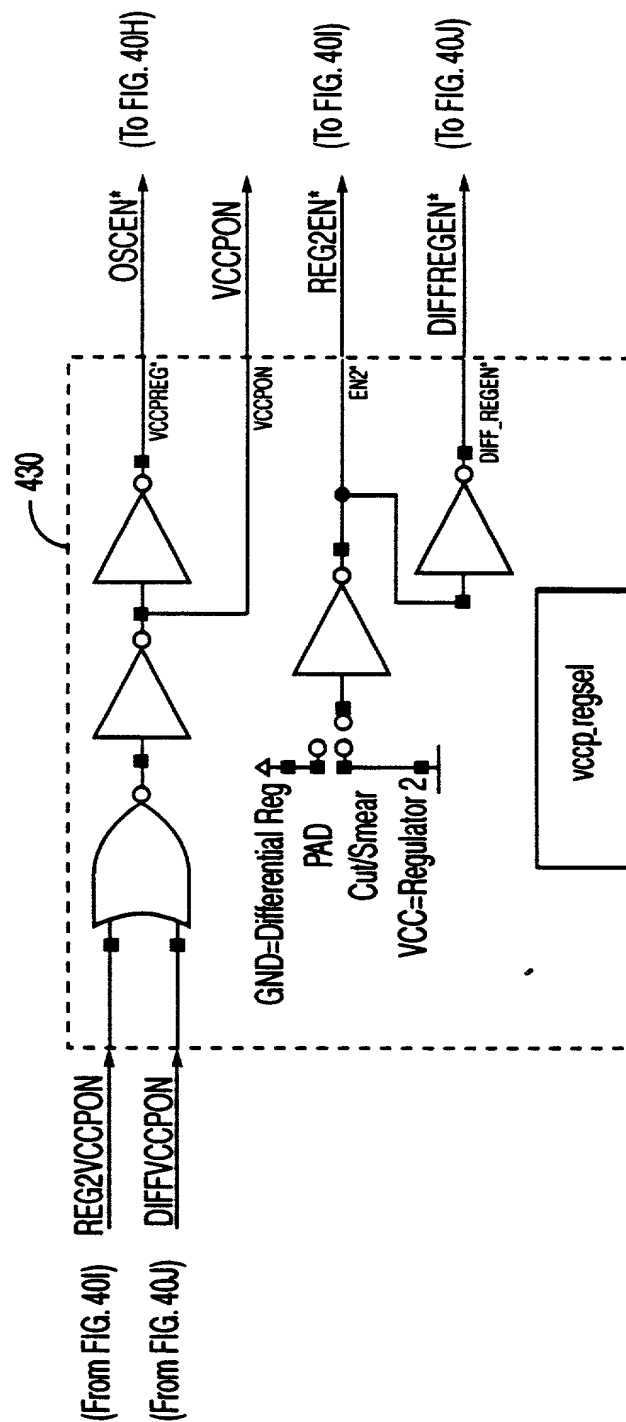


FIG. 40A

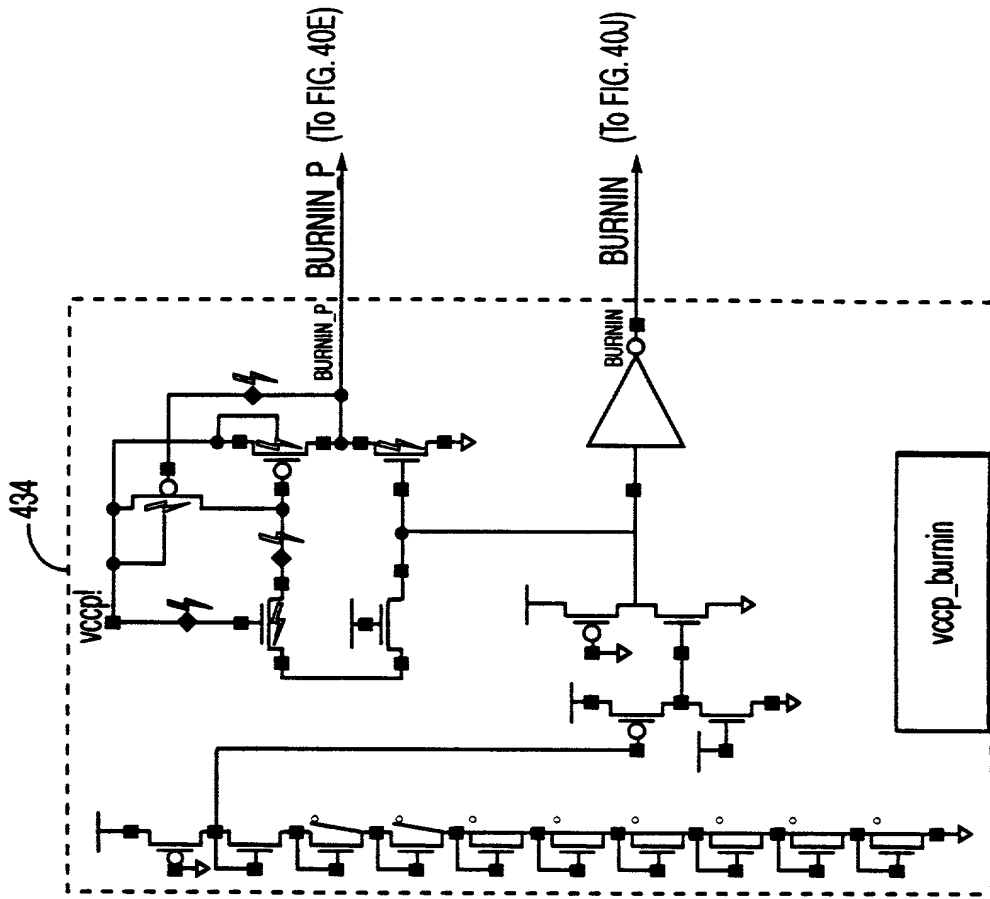


FIG. 40B

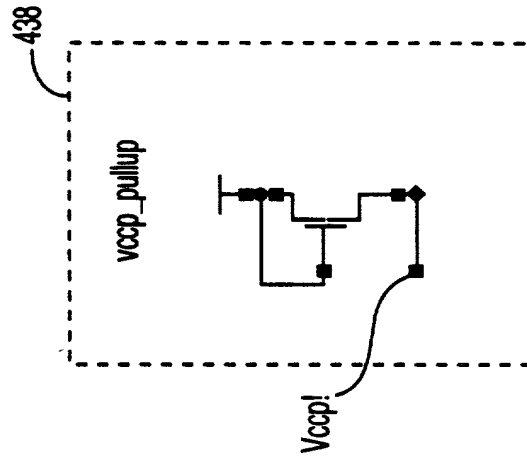


FIG. 40C

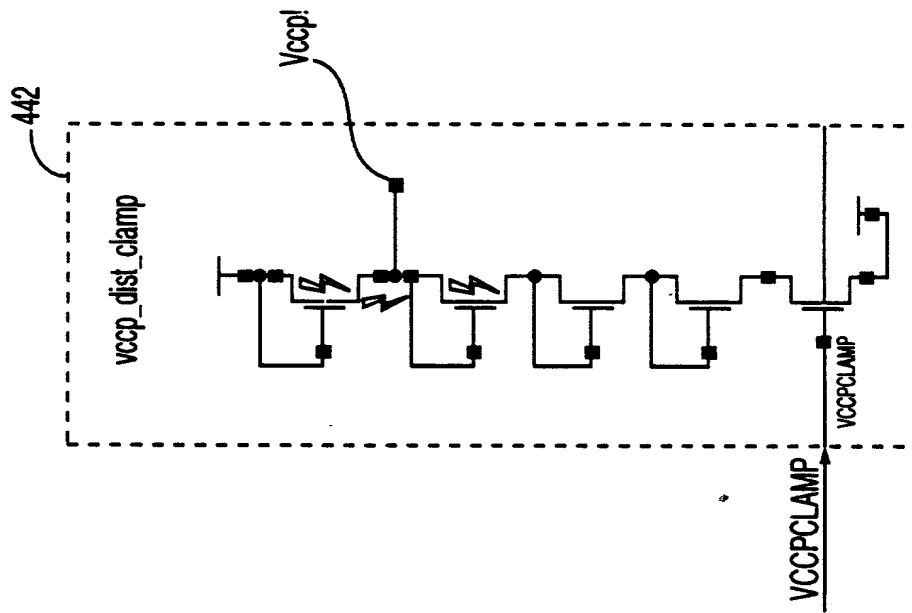
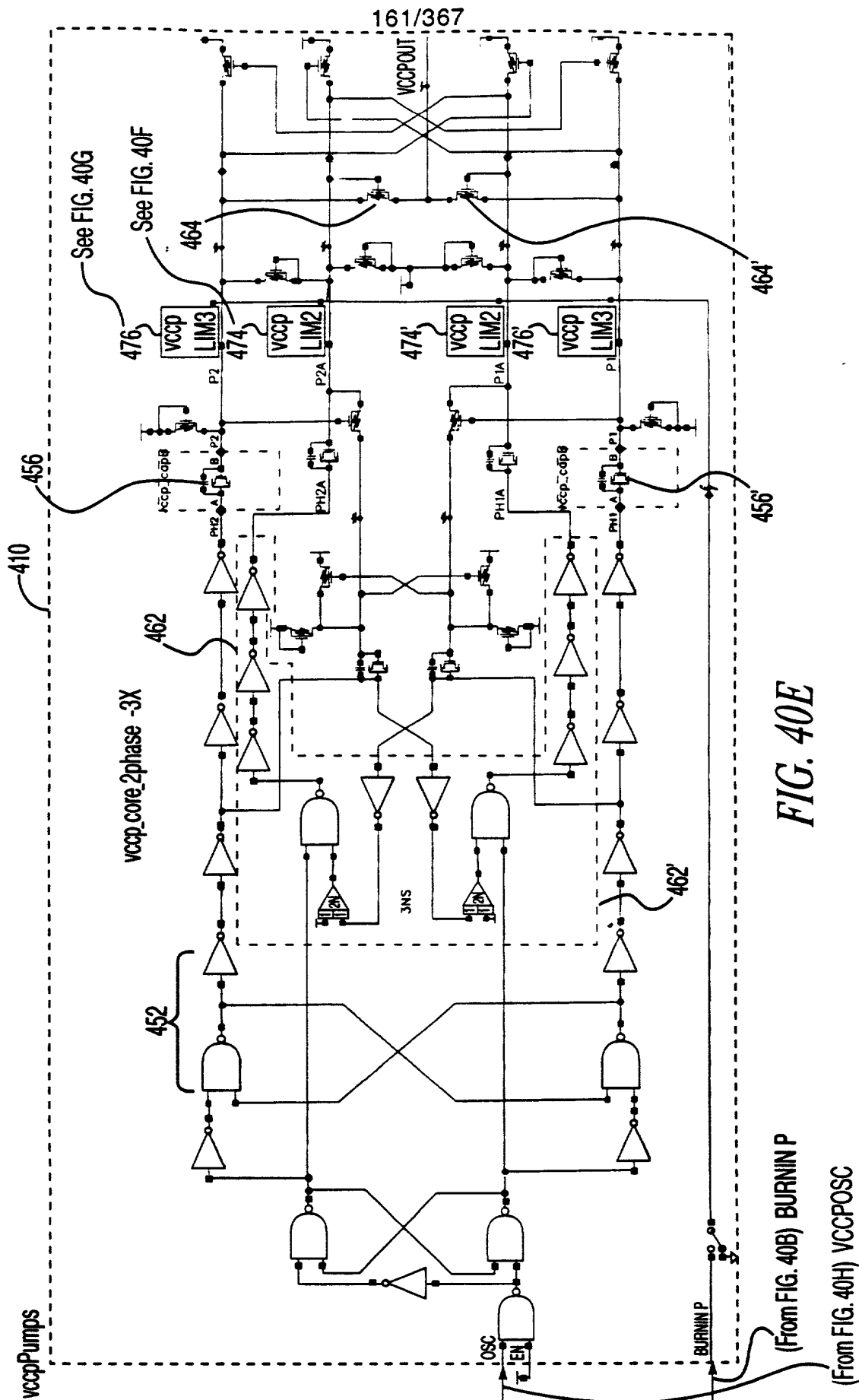


FIG. 40D





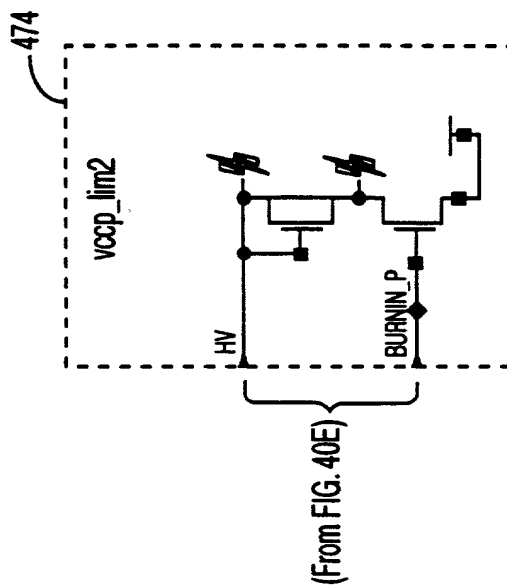


FIG. 40F

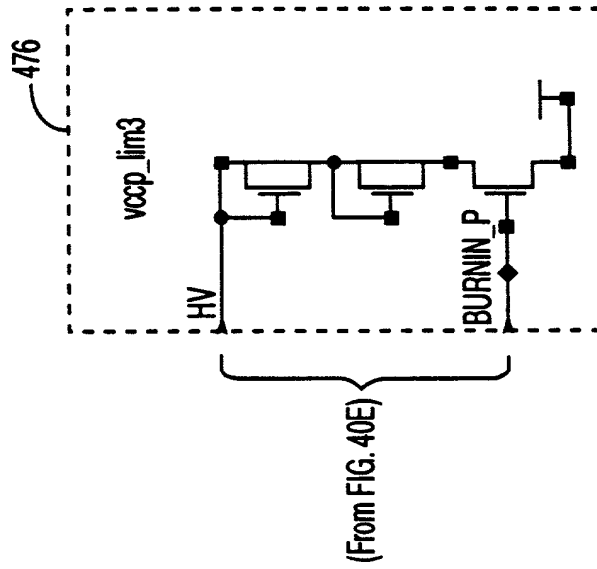
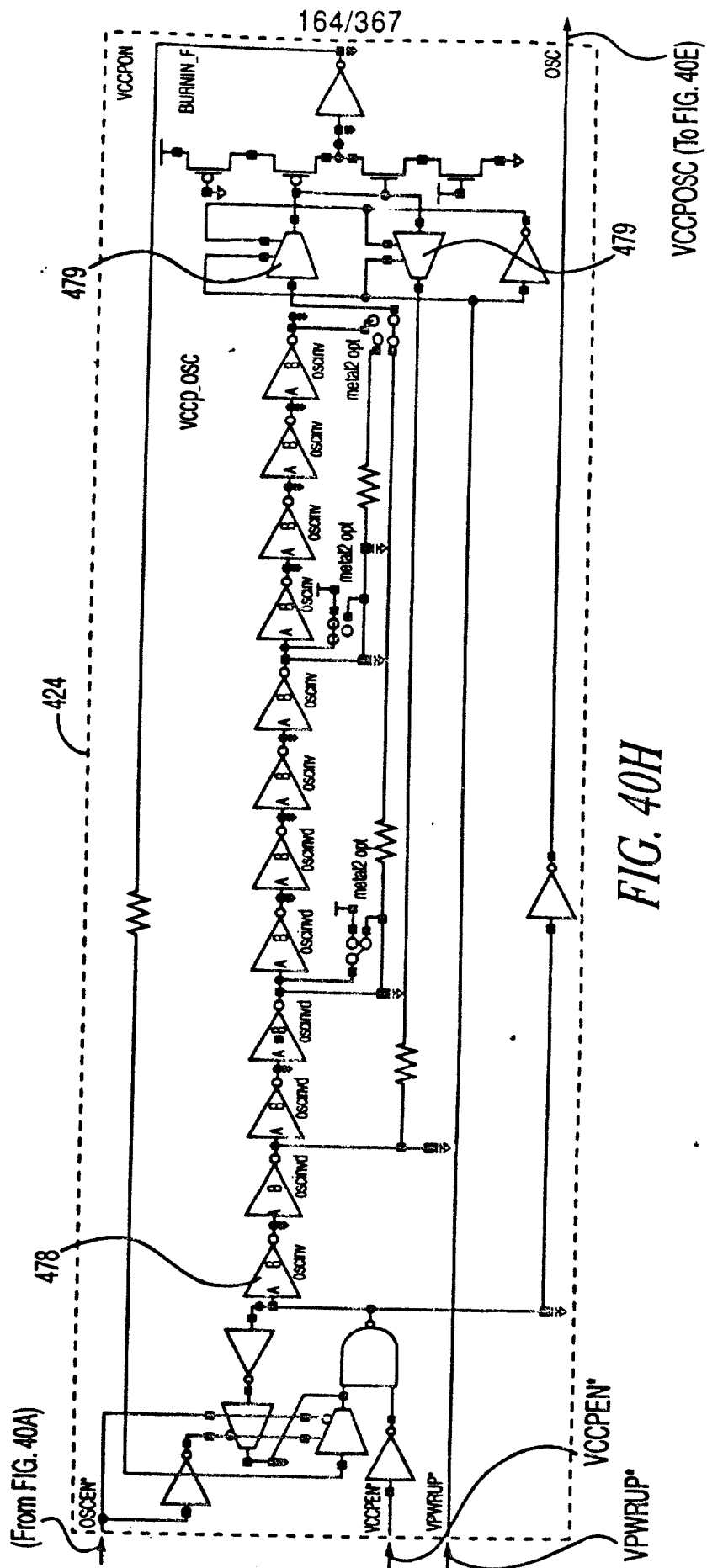


FIG. 40G



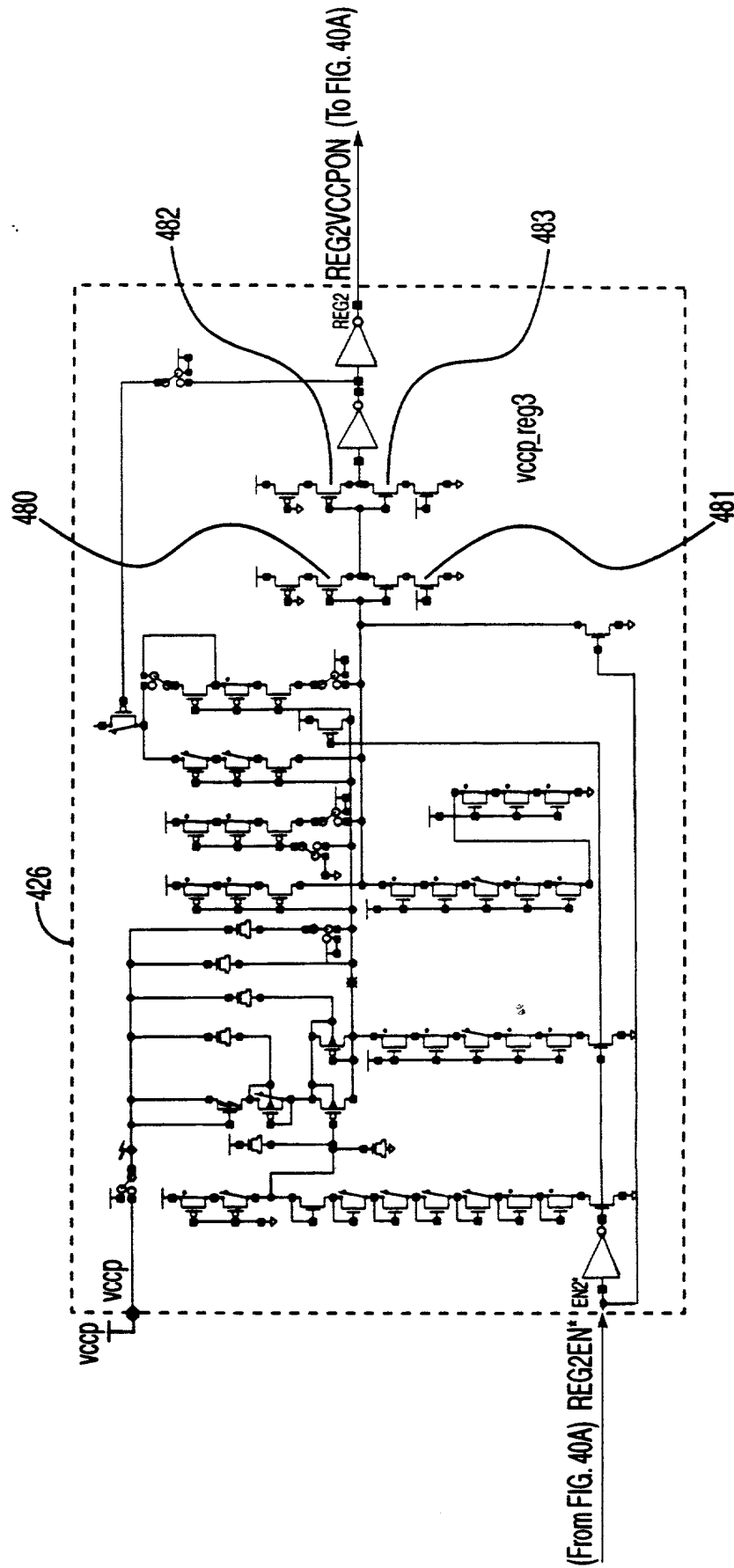
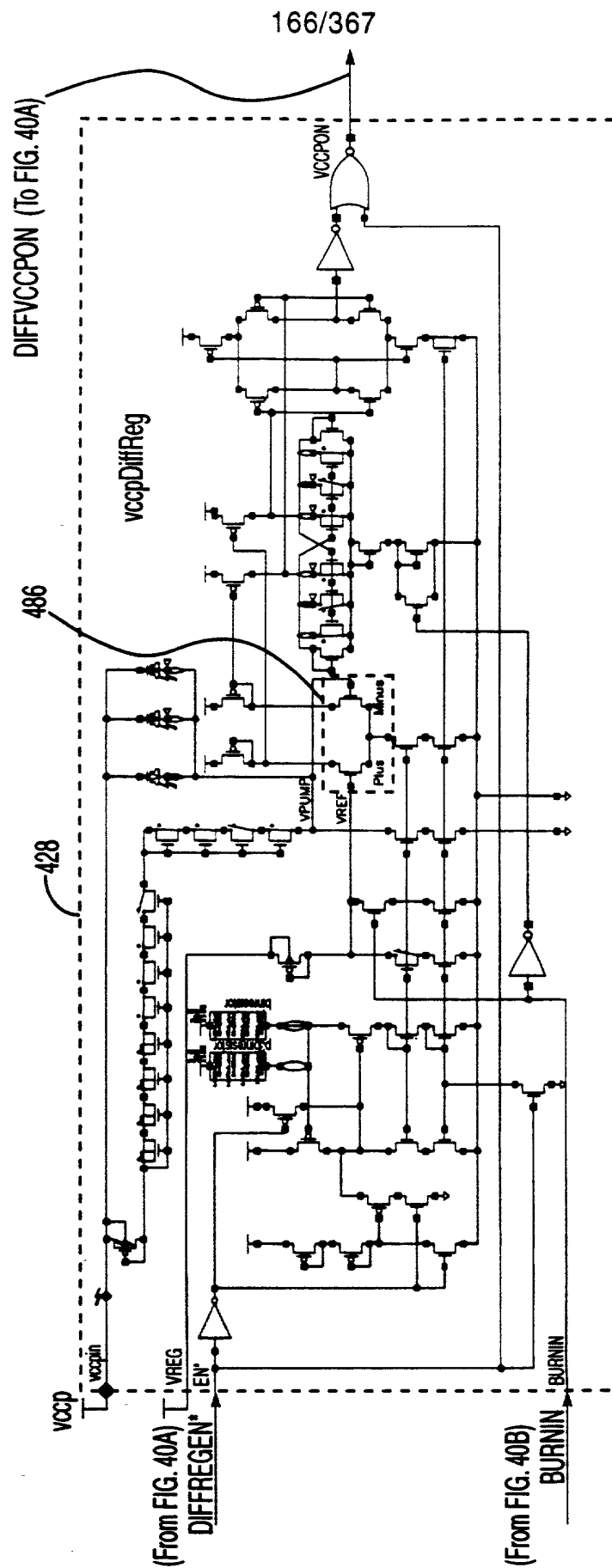
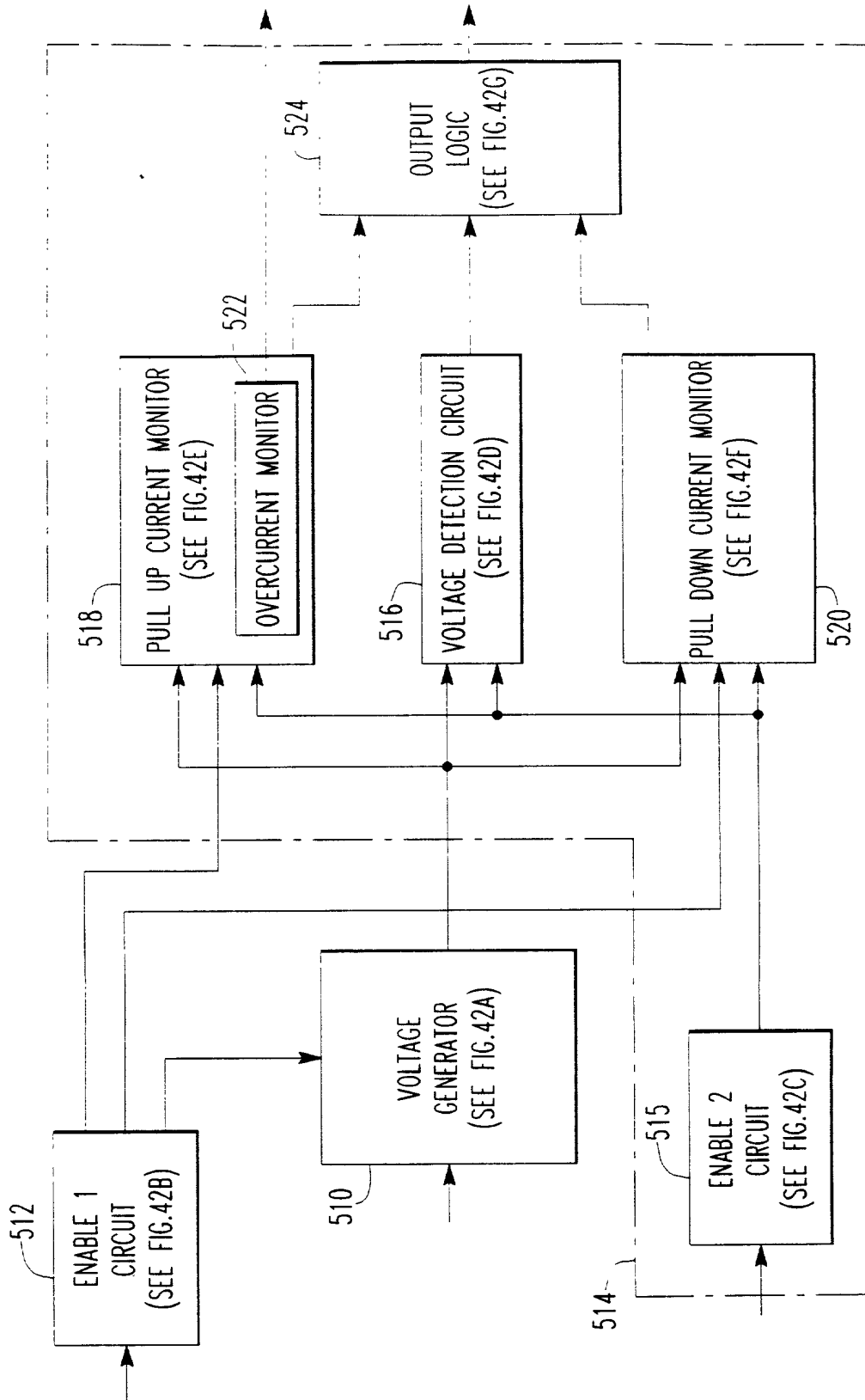


FIG. 40I





500

FIG. 41

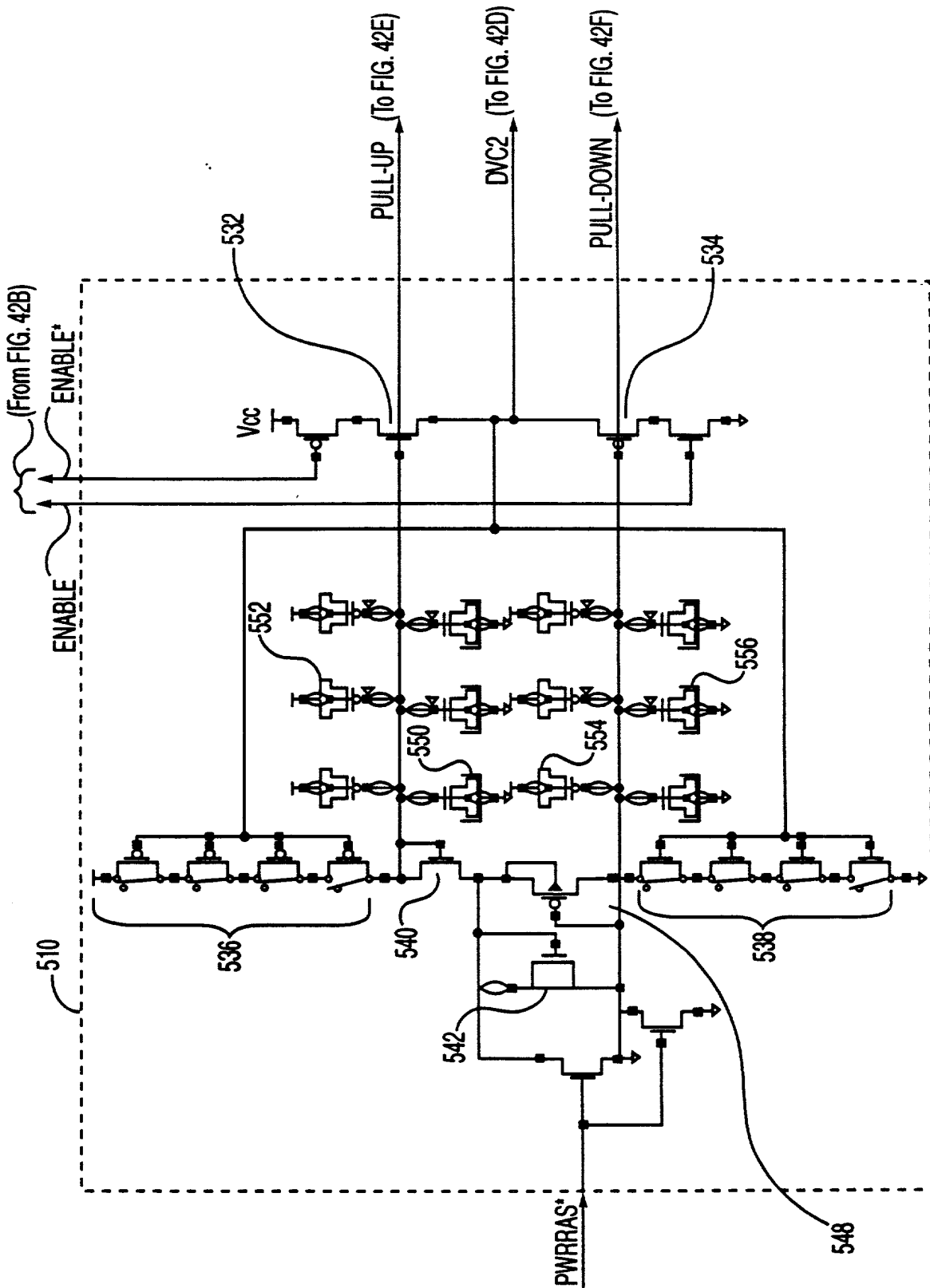


FIG. 42A



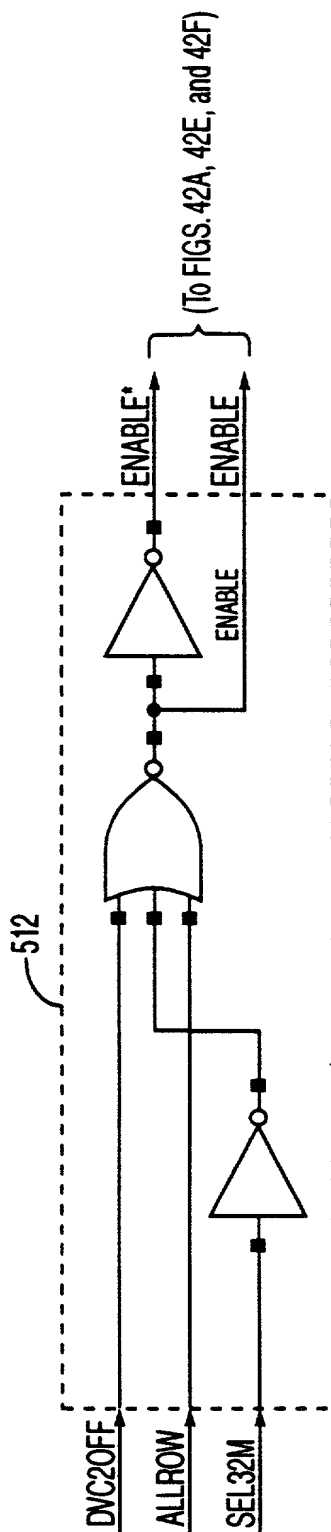


FIG. 42B

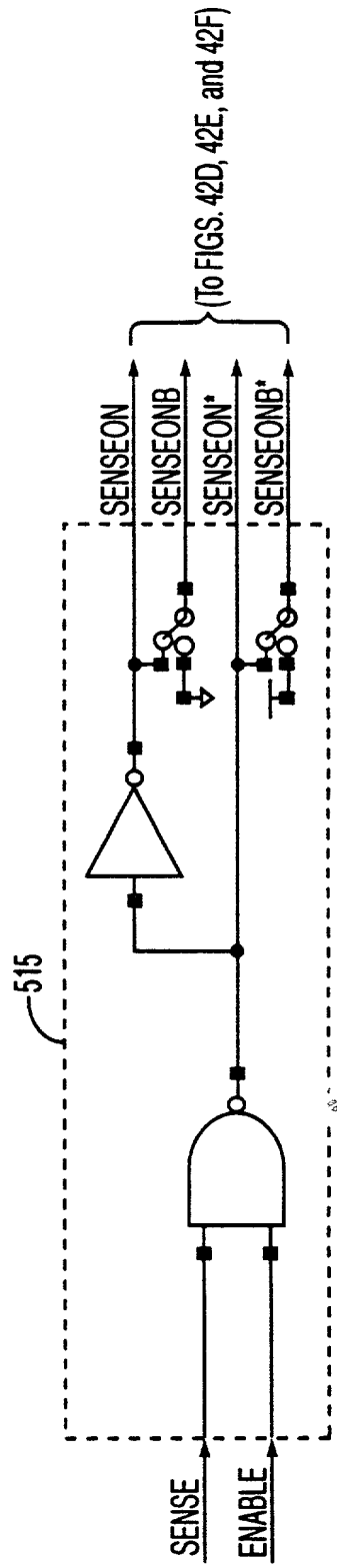


FIG. 42C



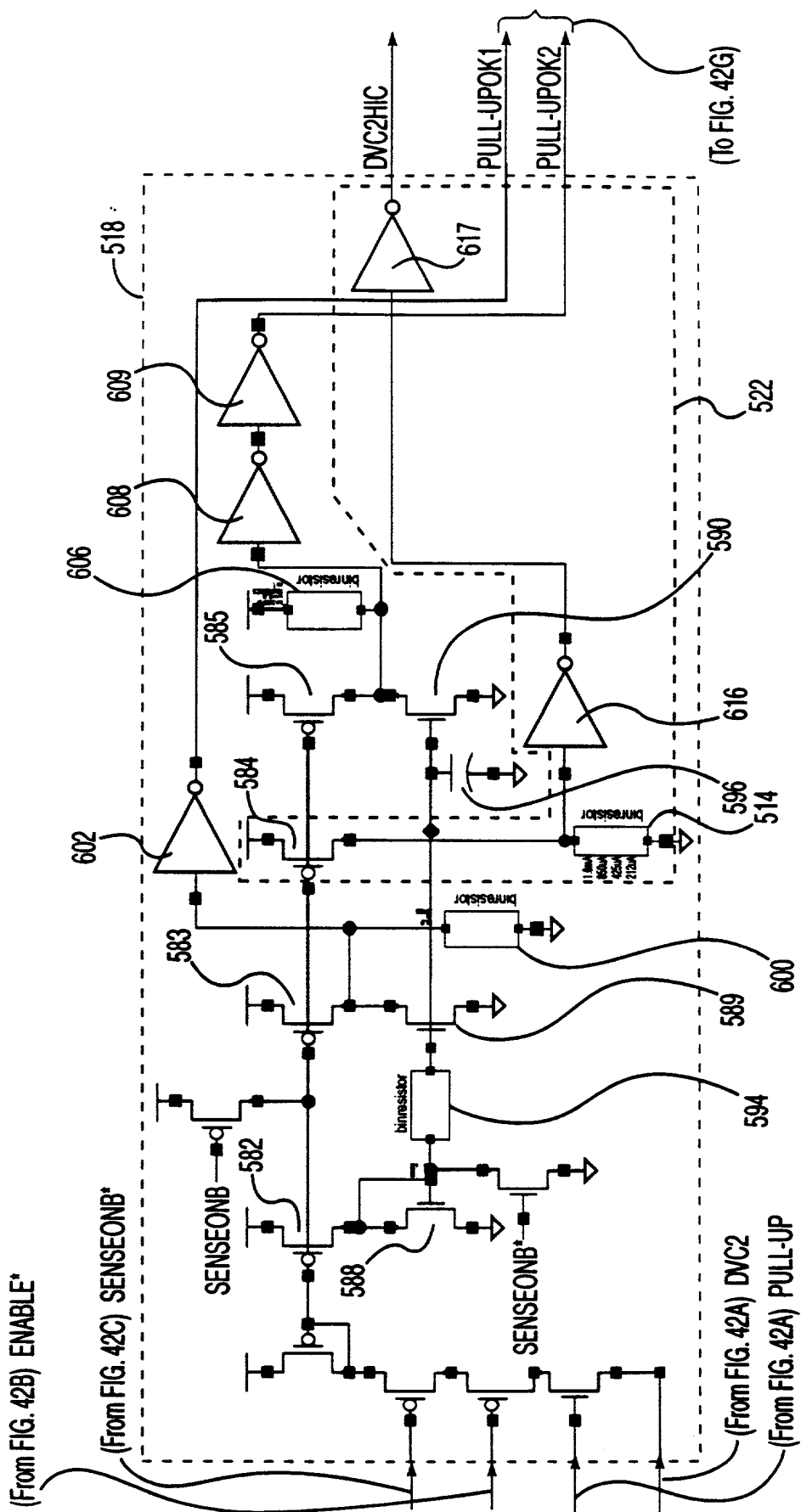


FIG. 42E



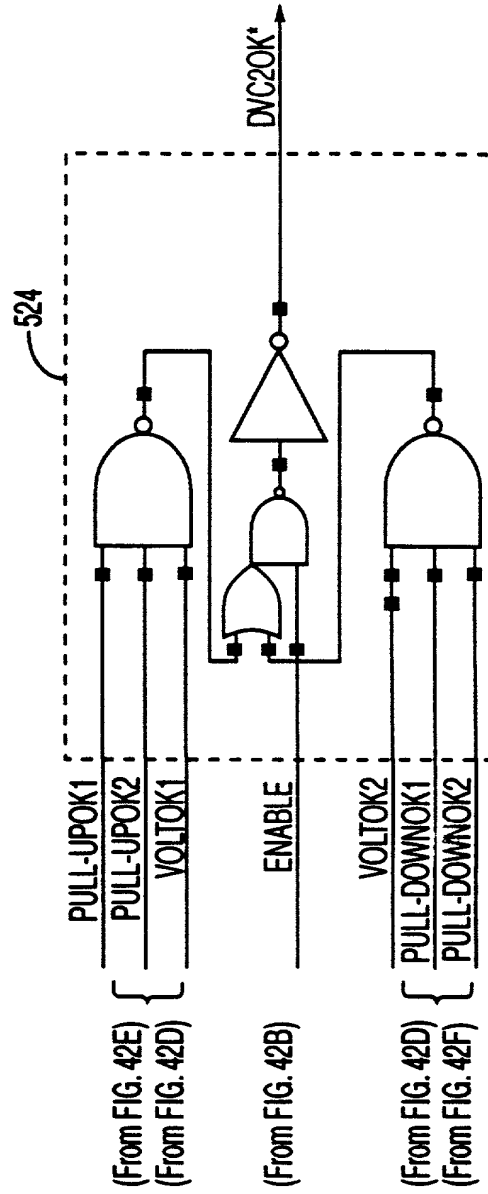


FIG. 42G

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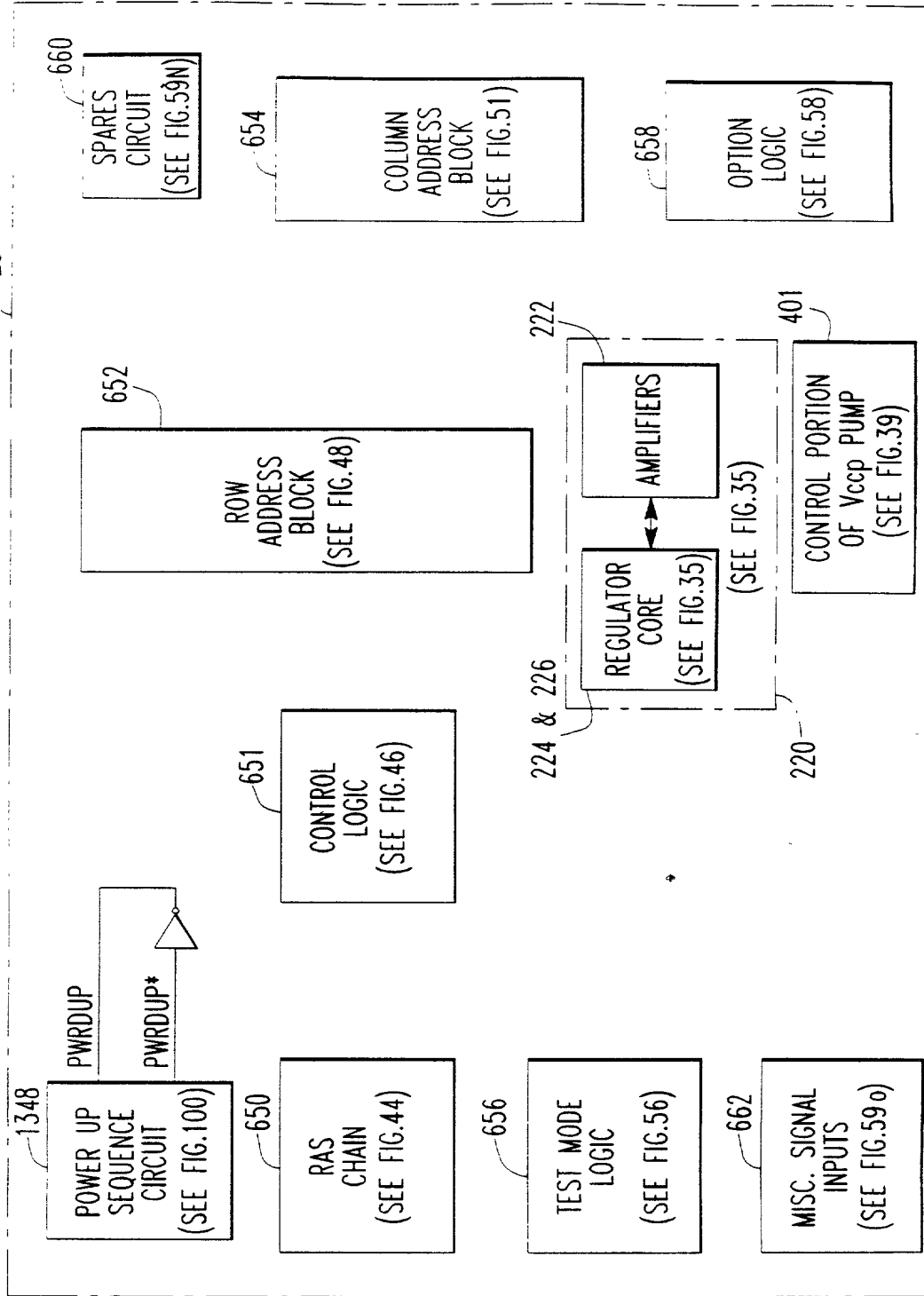


FIG. 43

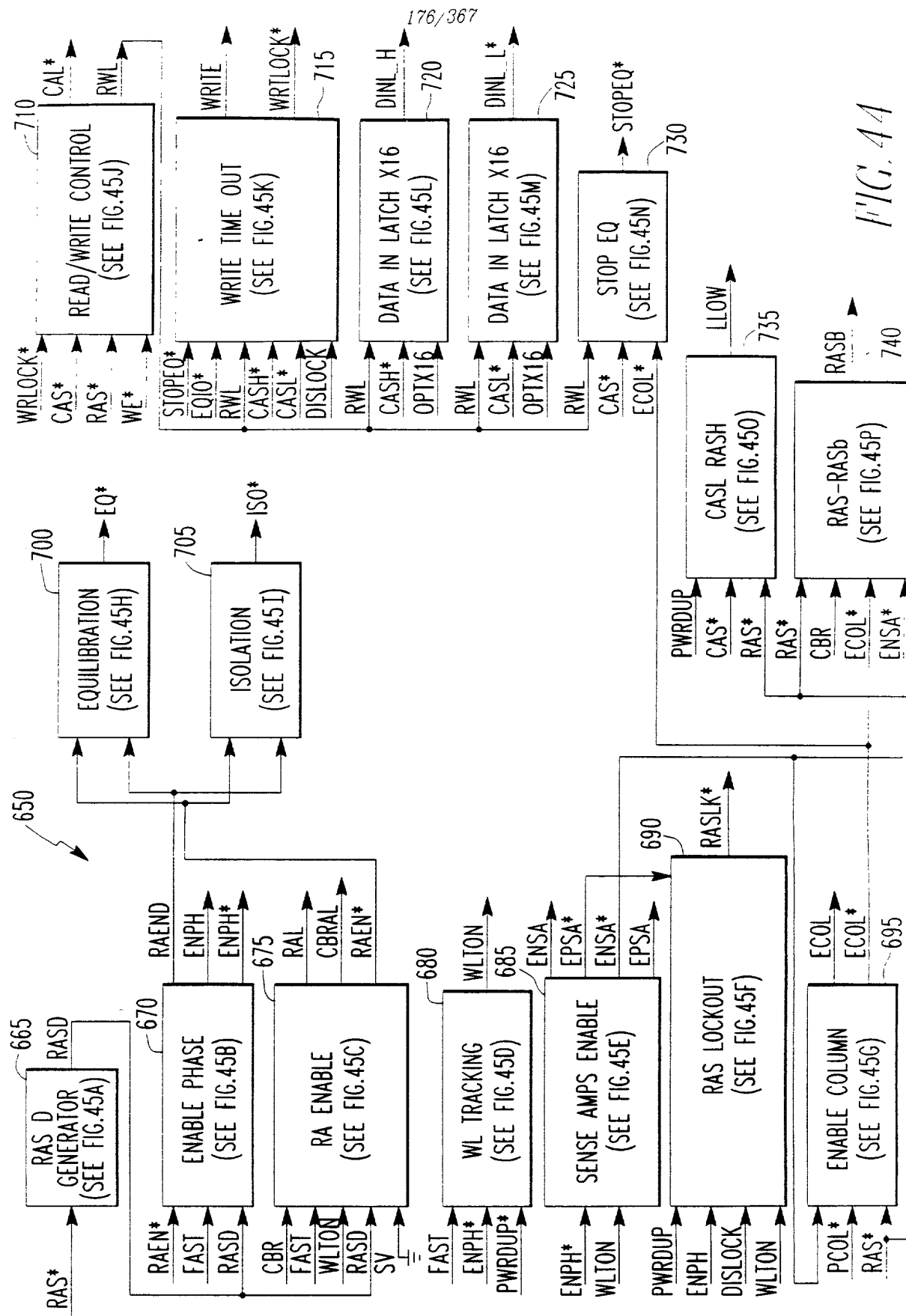


FIG. 44



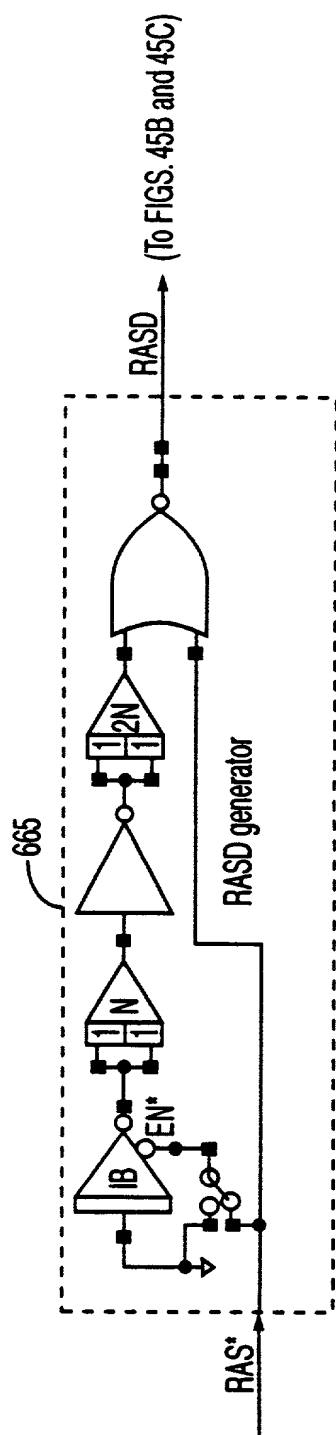
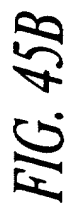


FIG. 45A





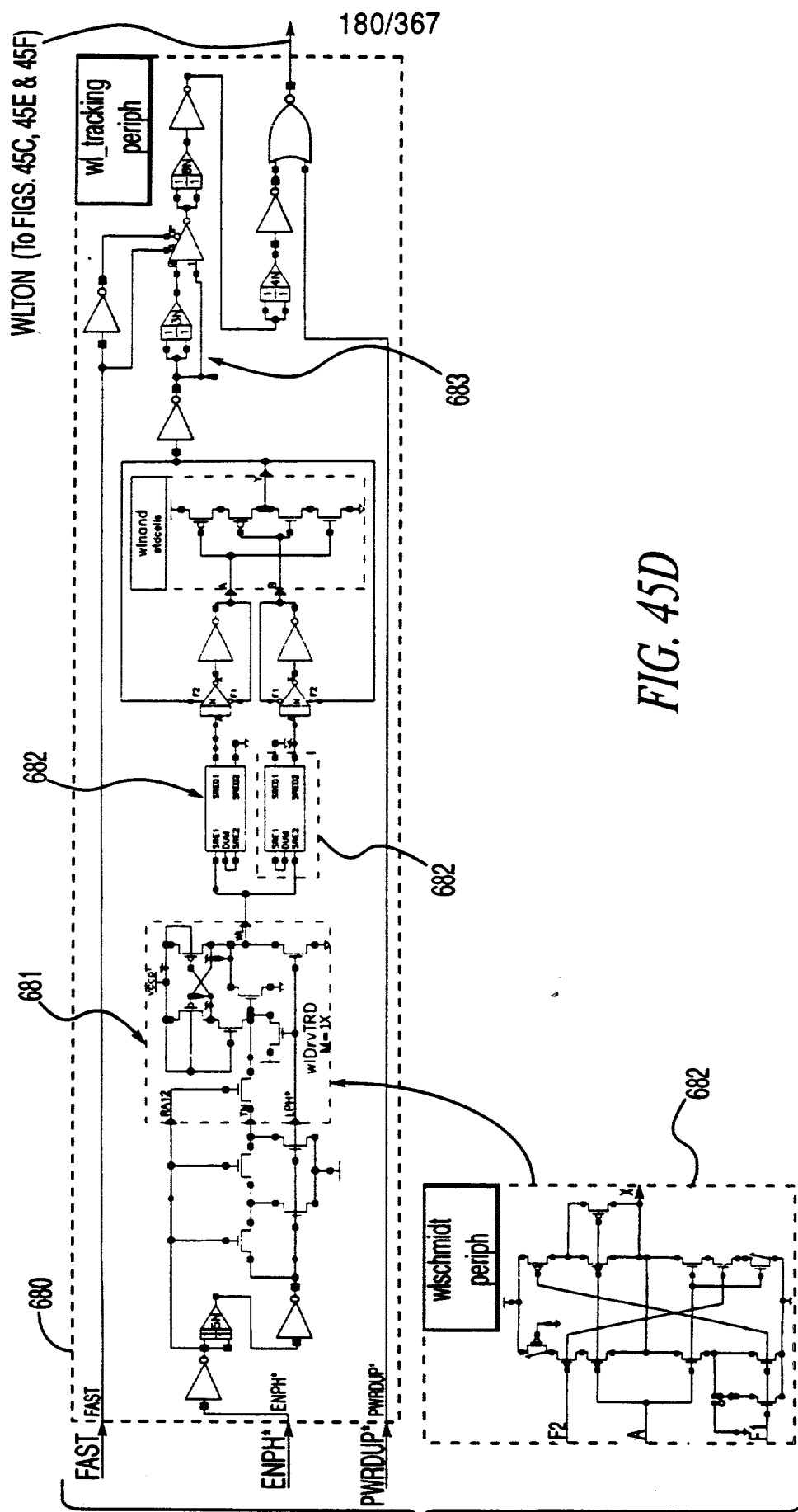


FIG. 45D

(From FIG. 45B)

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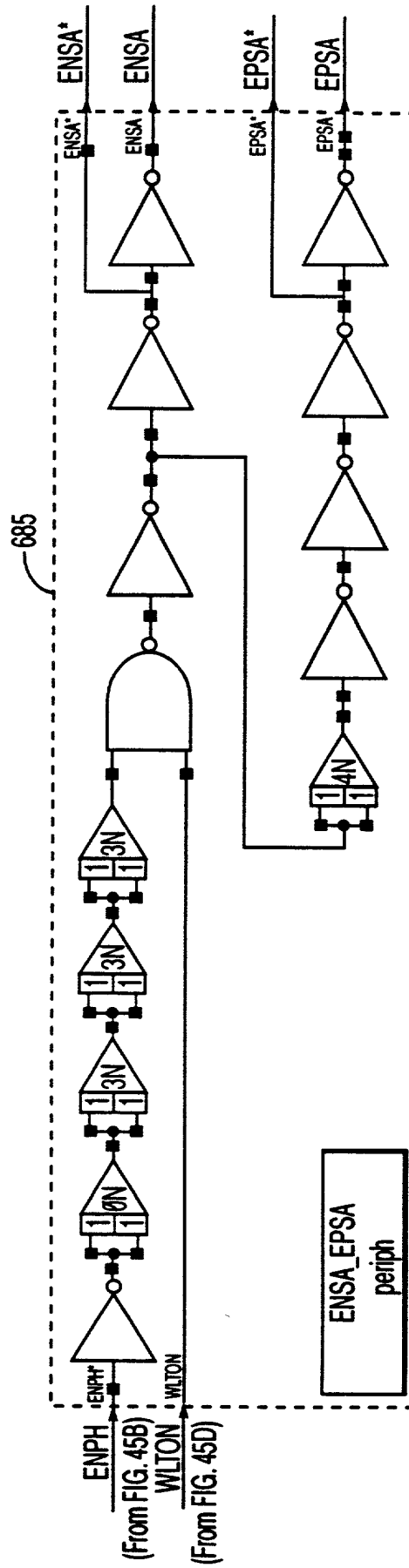
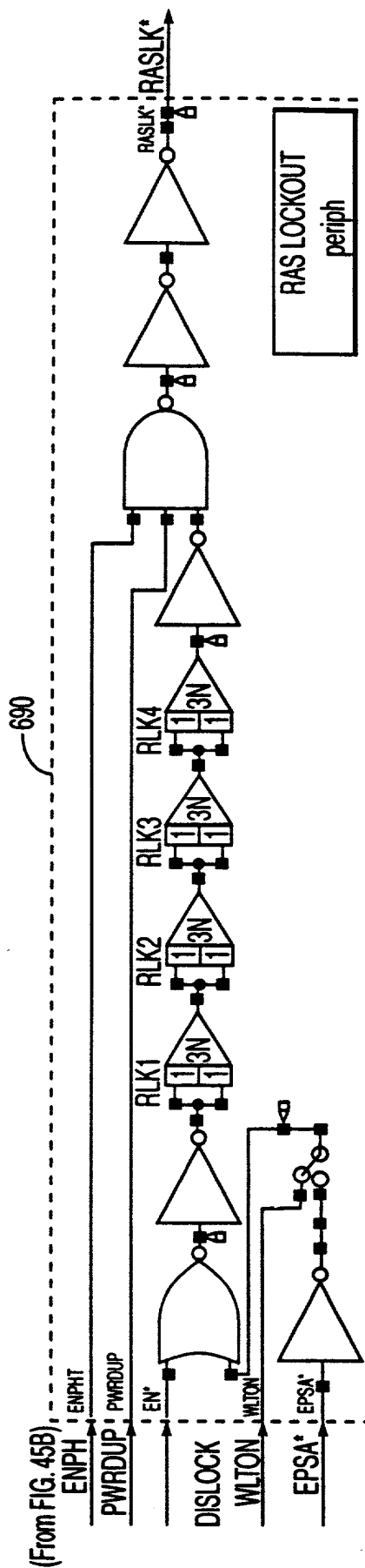


FIG. 45E



**FIG. 45F**

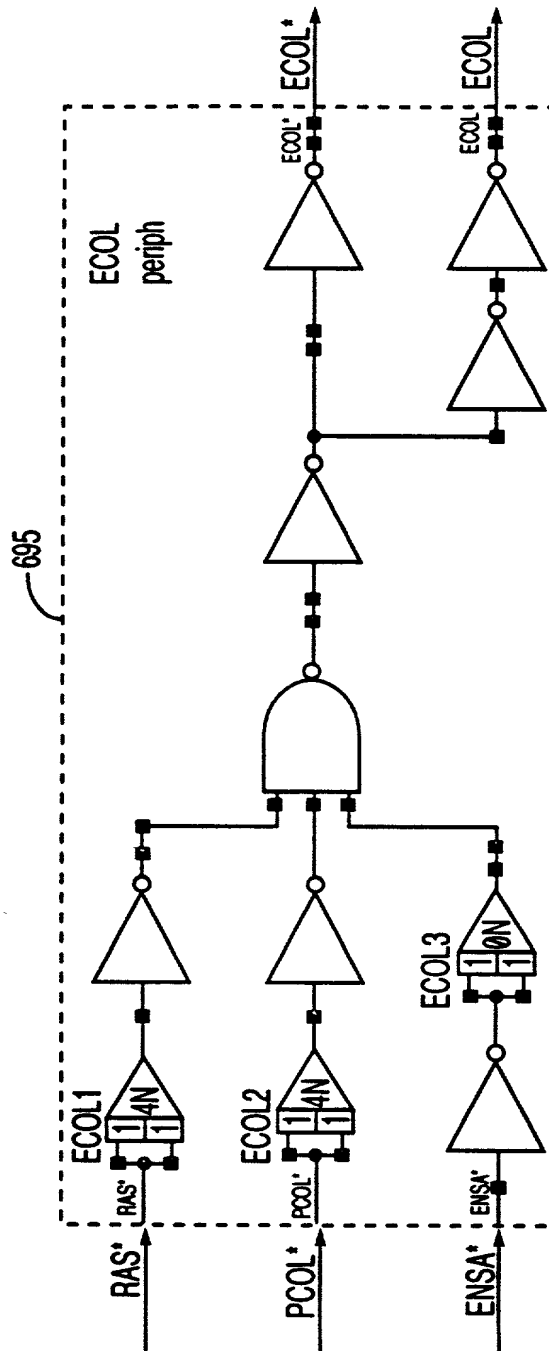


FIG. 45G





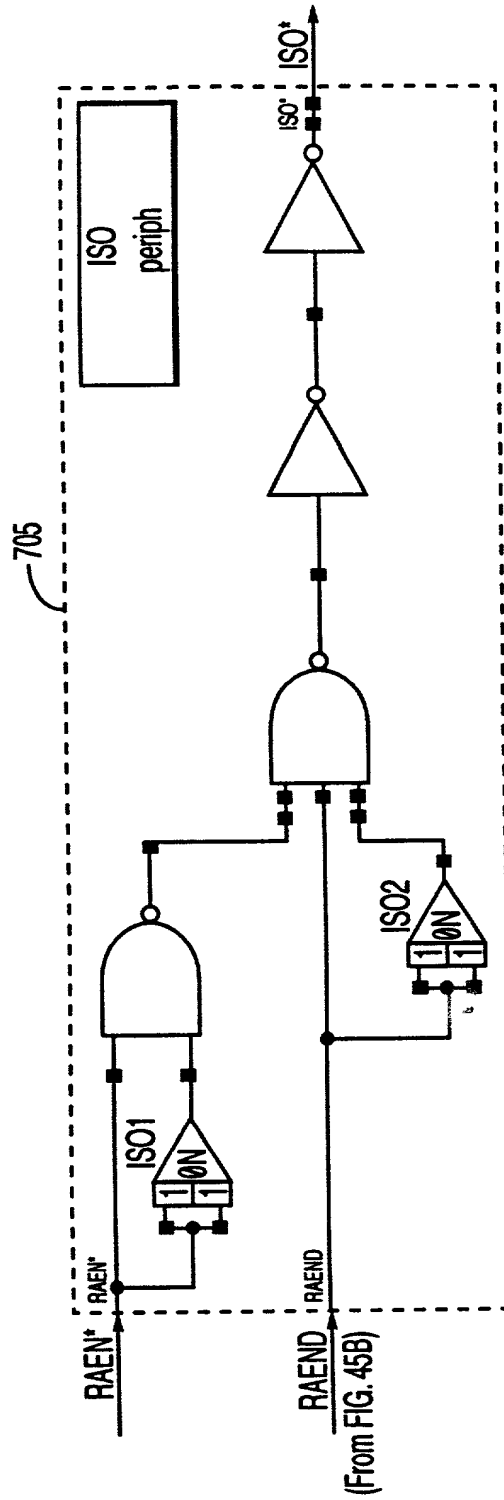
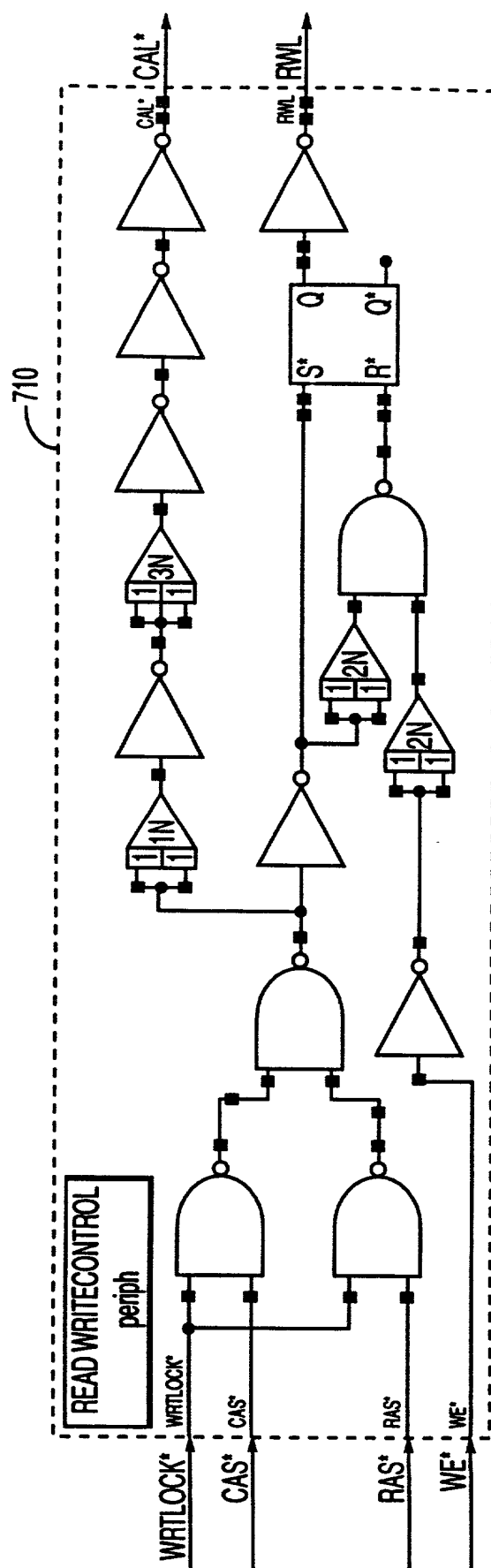


FIG. 45I



**FIG. 45J**

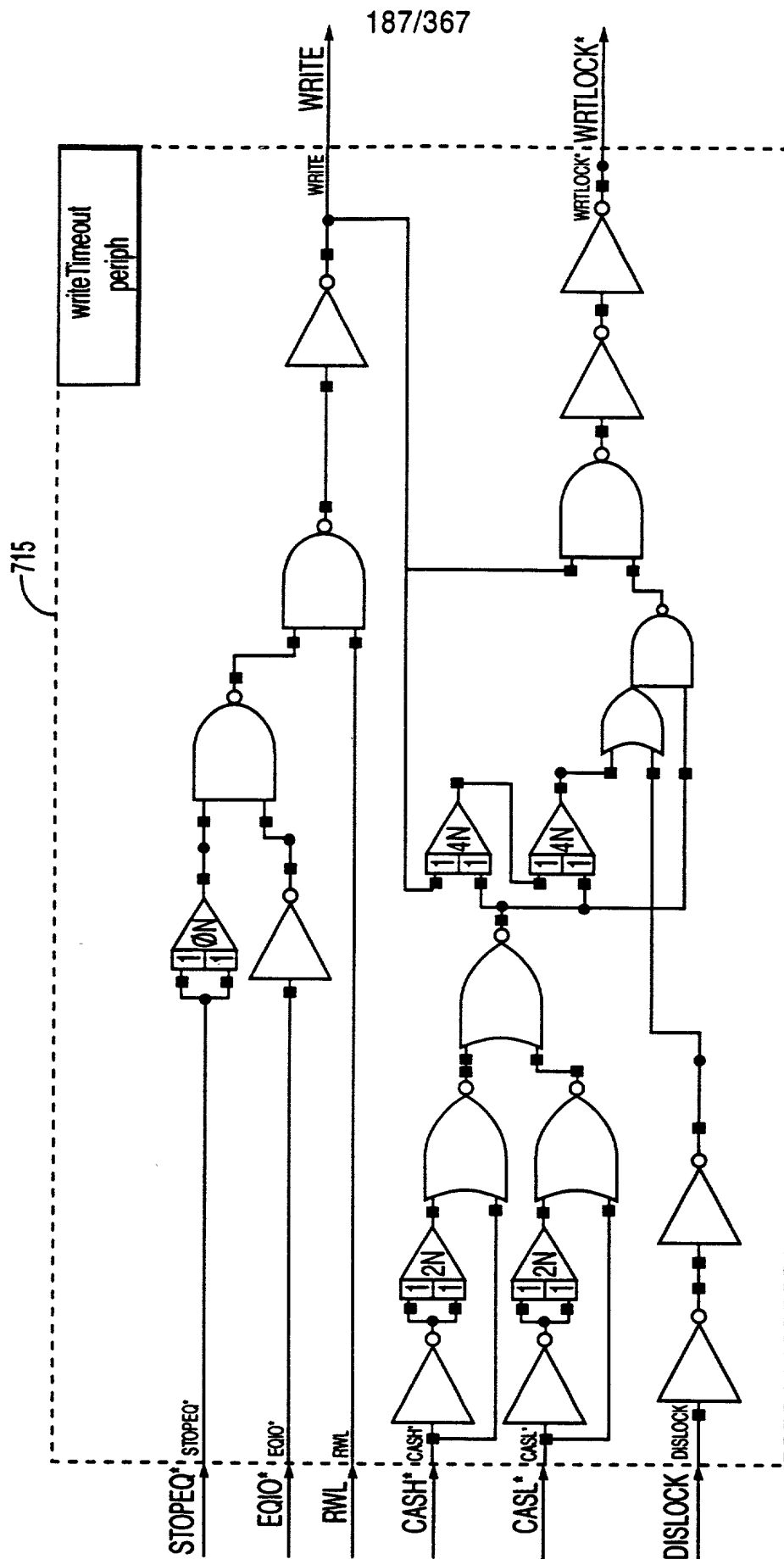


FIG. 45K

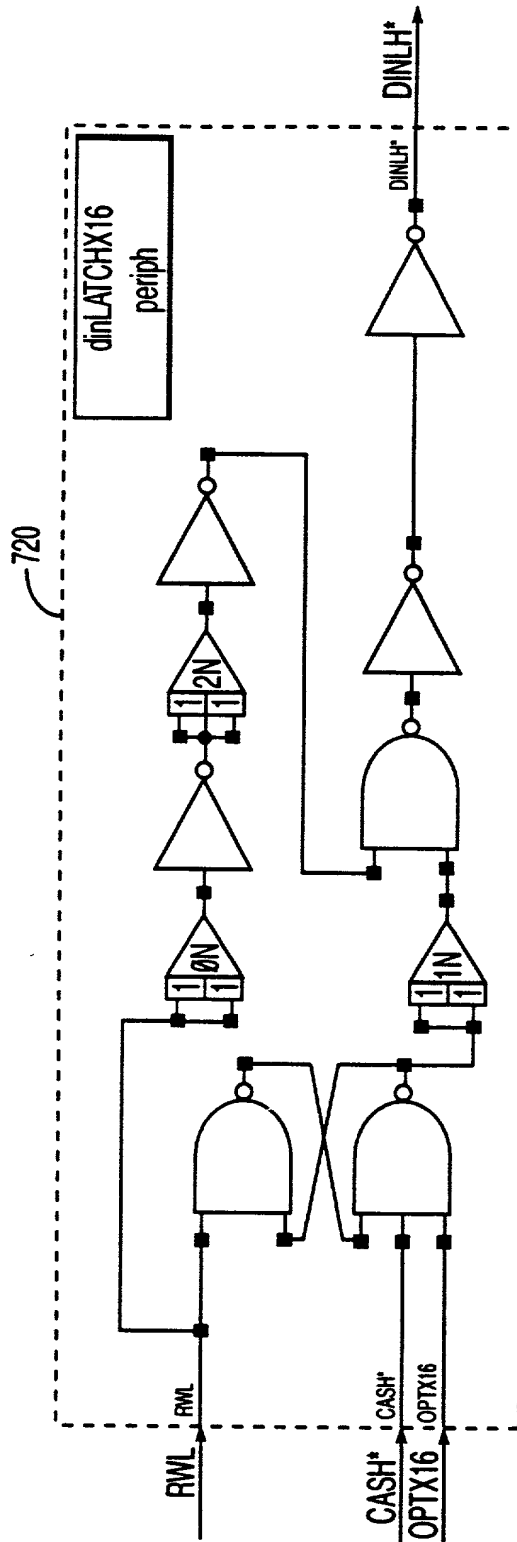


FIG. 45L

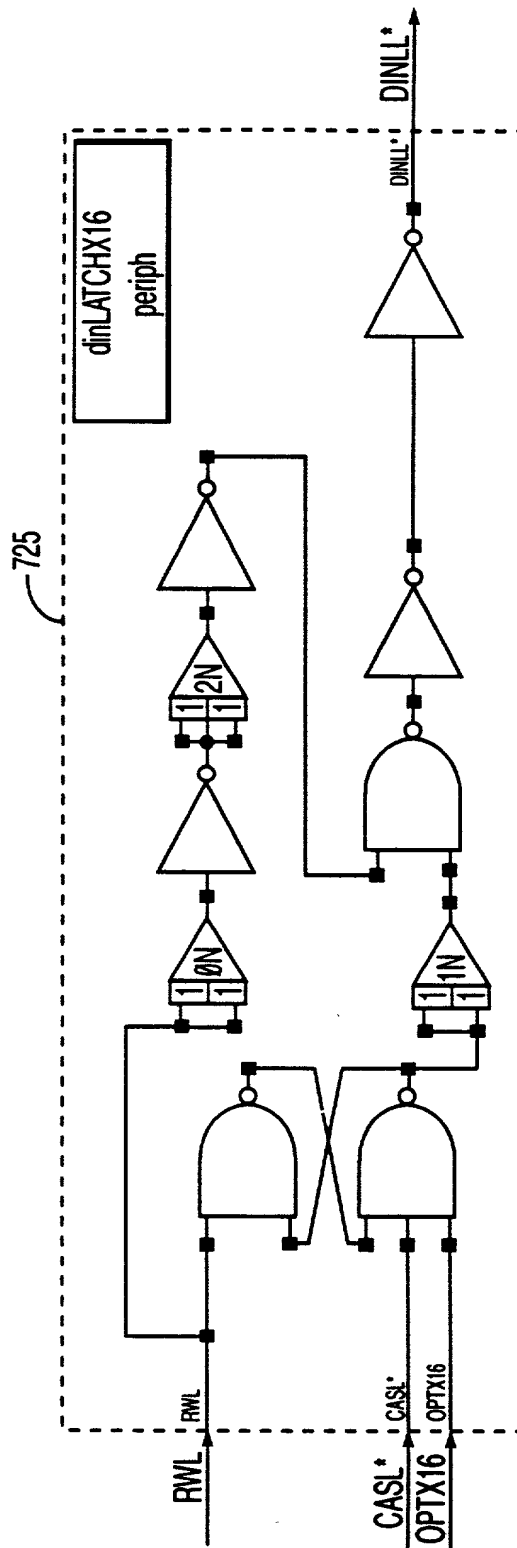
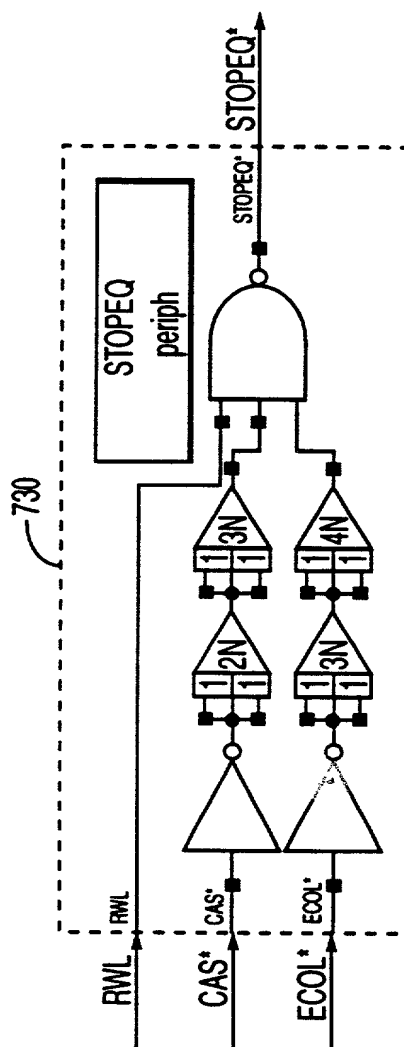


FIG. 45M



**FIG. 45N**

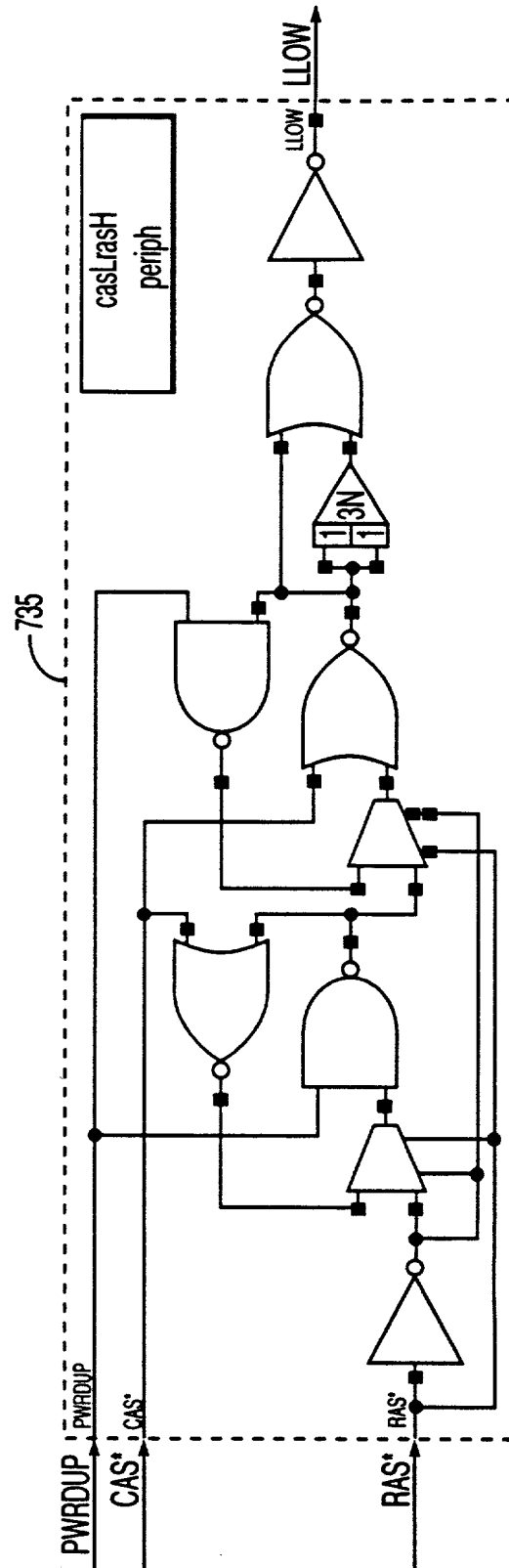


FIG. 450

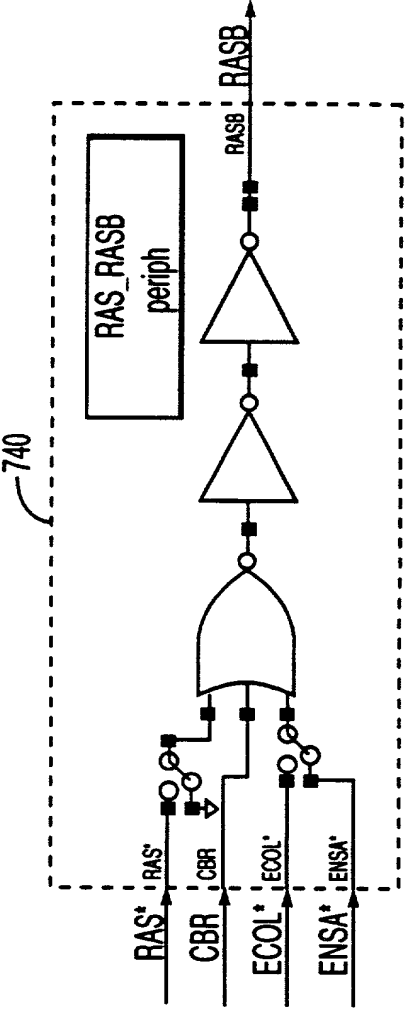


FIG. 45P



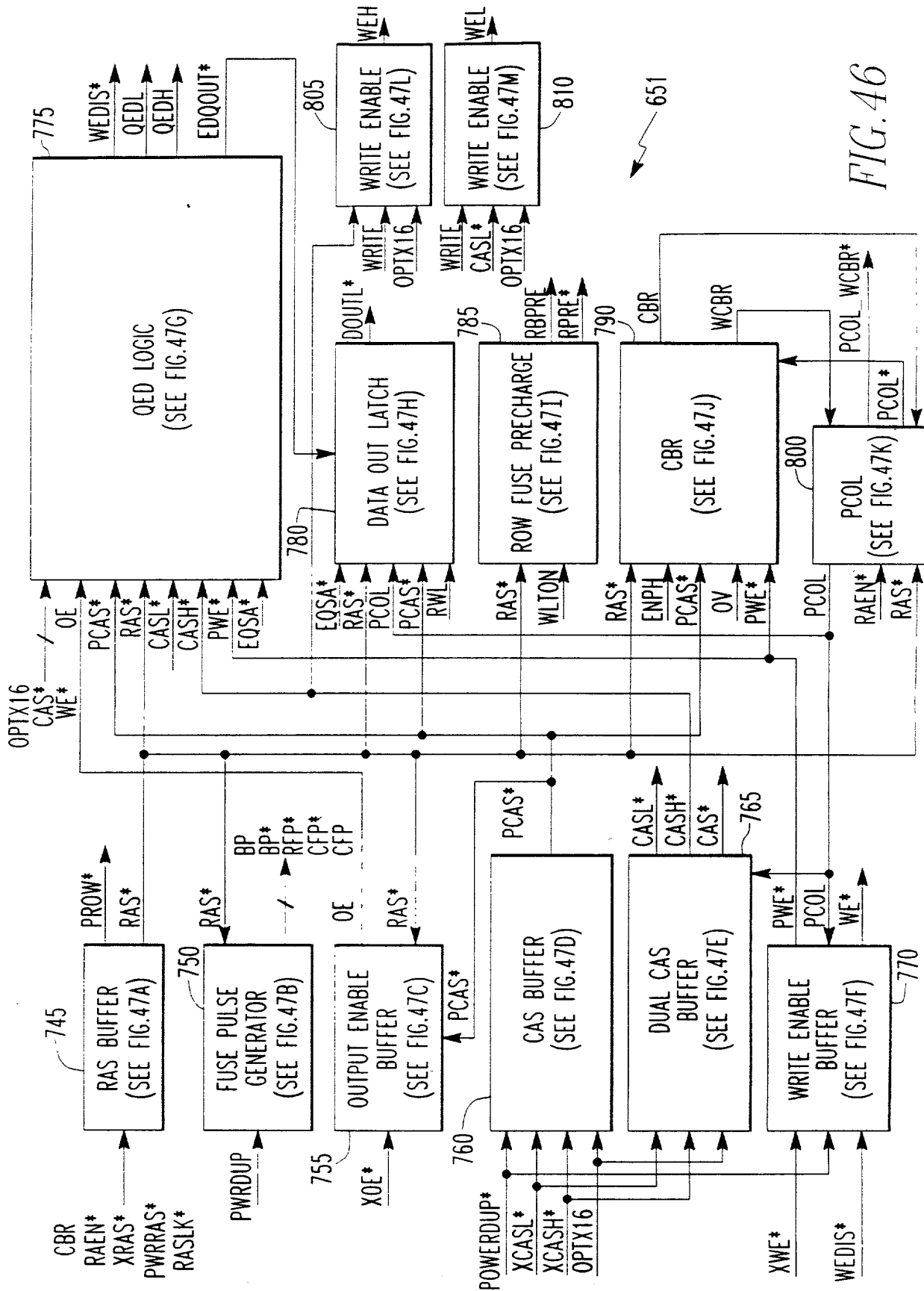


FIG. 46

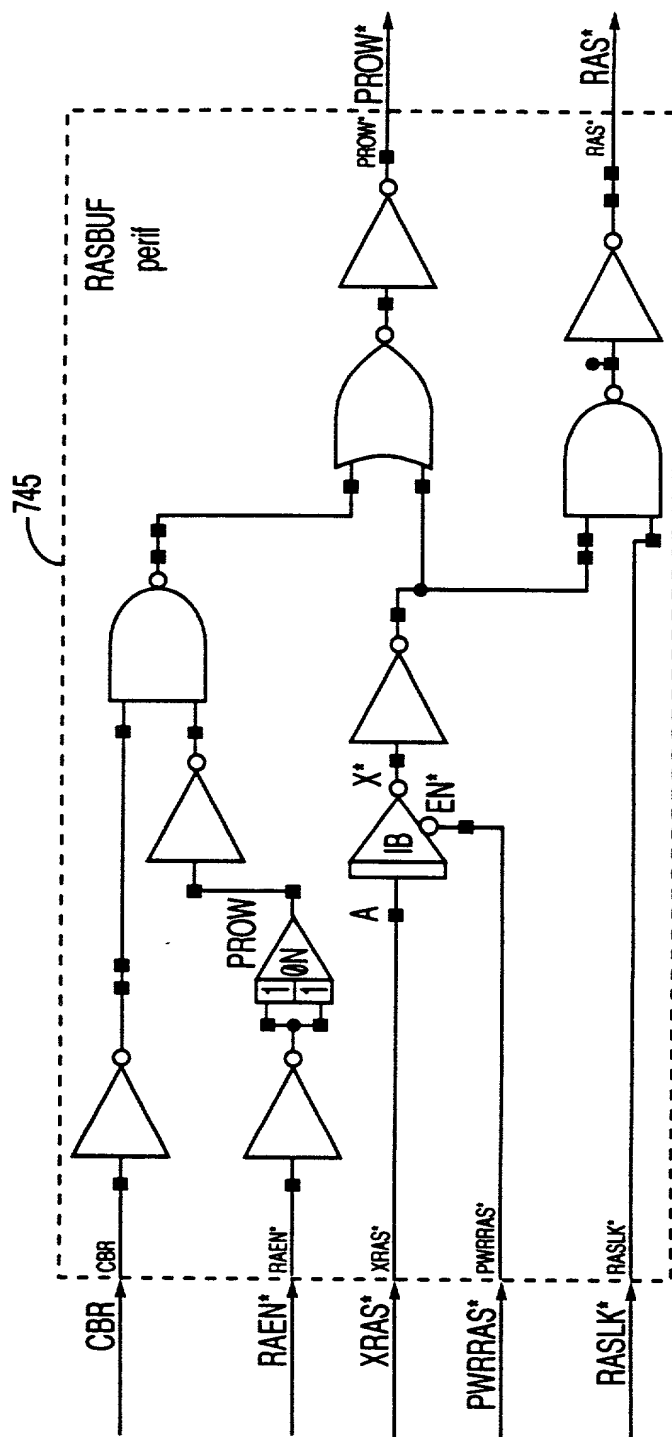


FIG. 47A



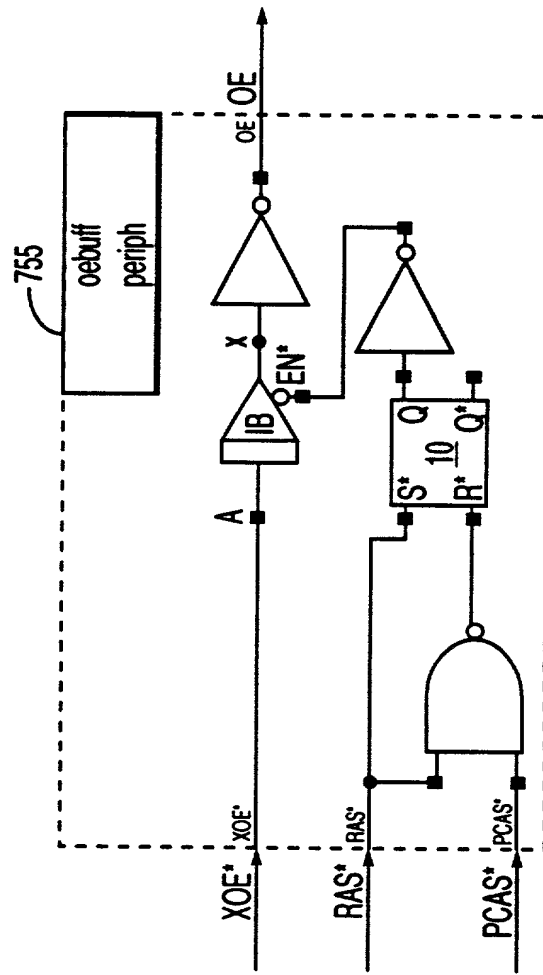


FIG. 47C

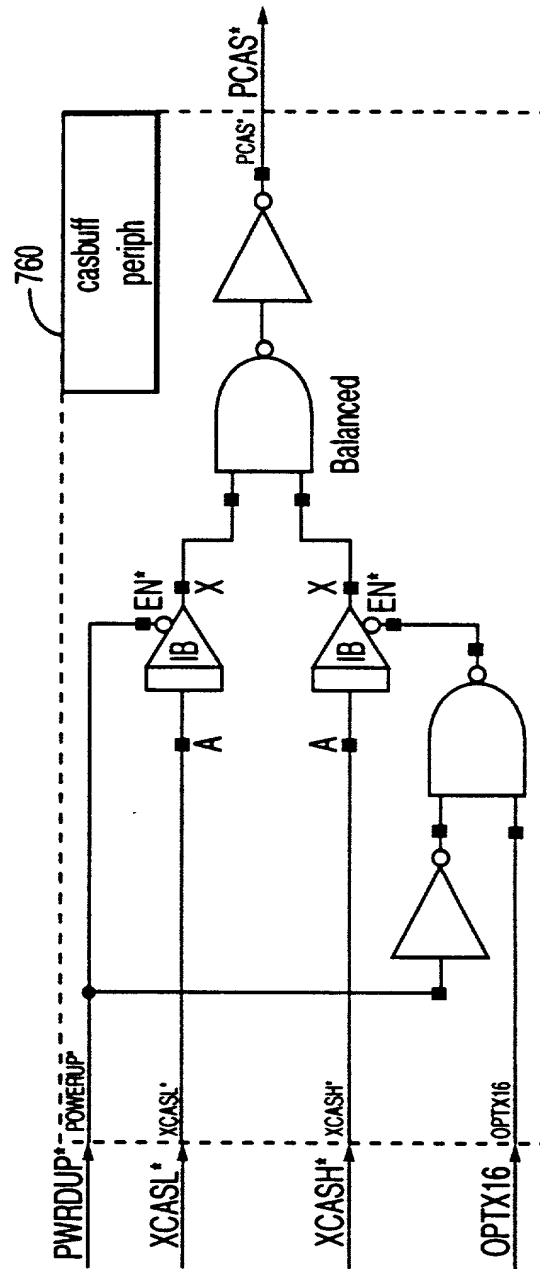


FIG. 47D

FOOTNOTES

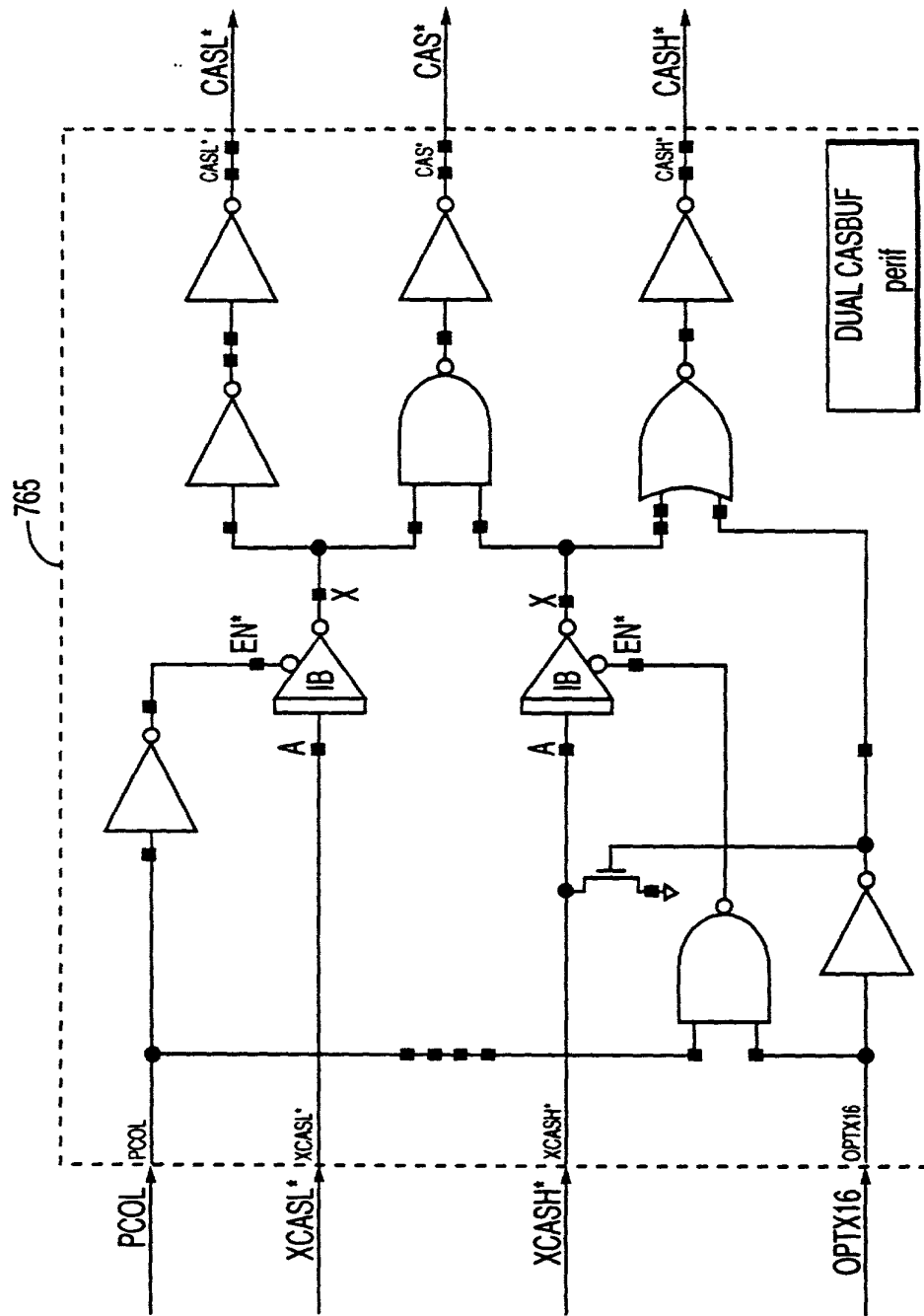


FIG. 47E

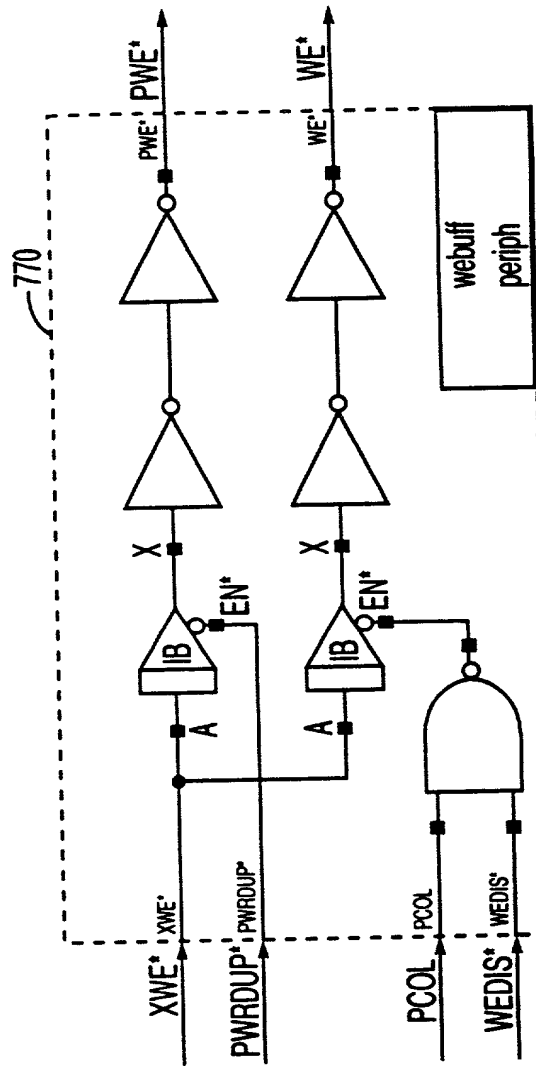


FIG. 47F

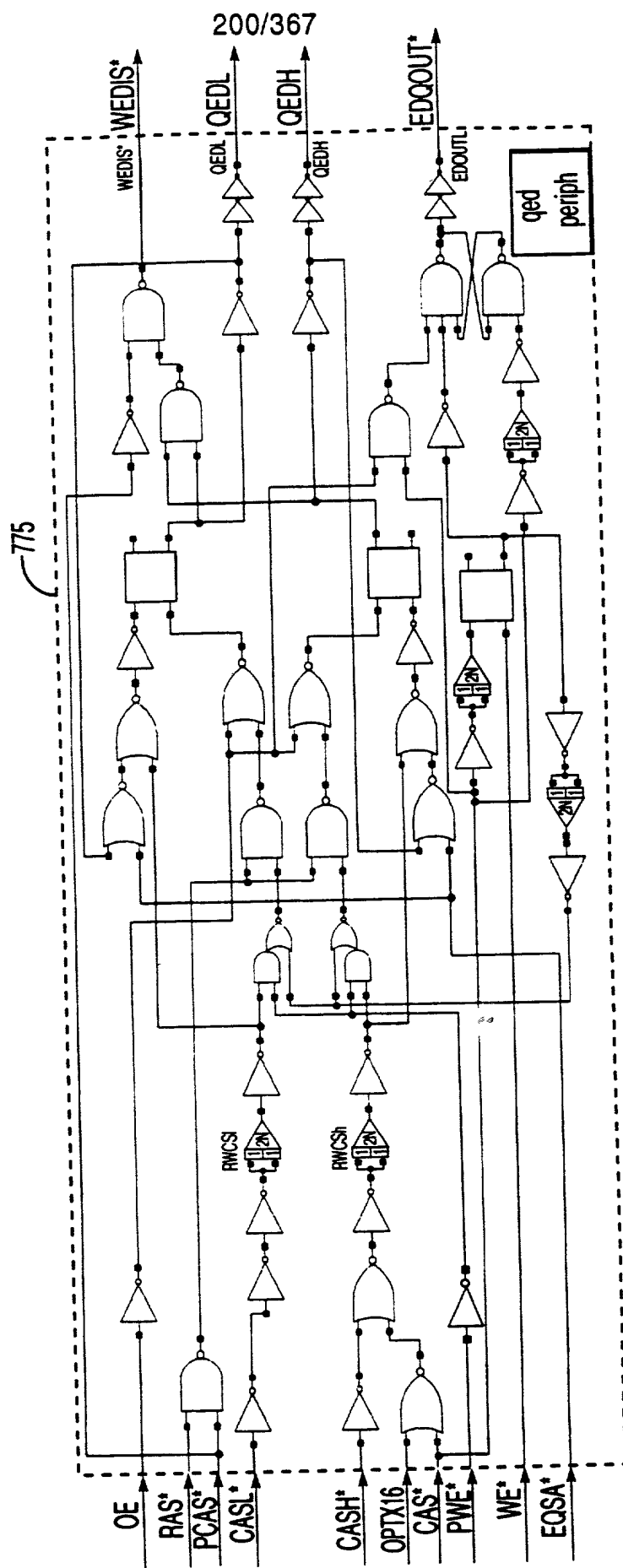


FIG. 47G



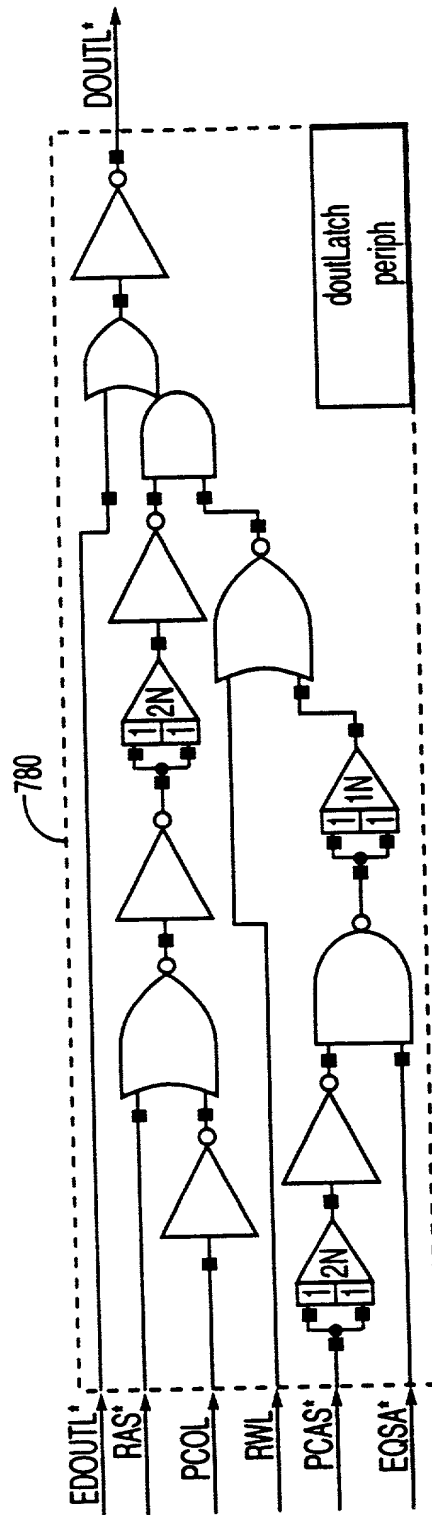


FIG. 47H

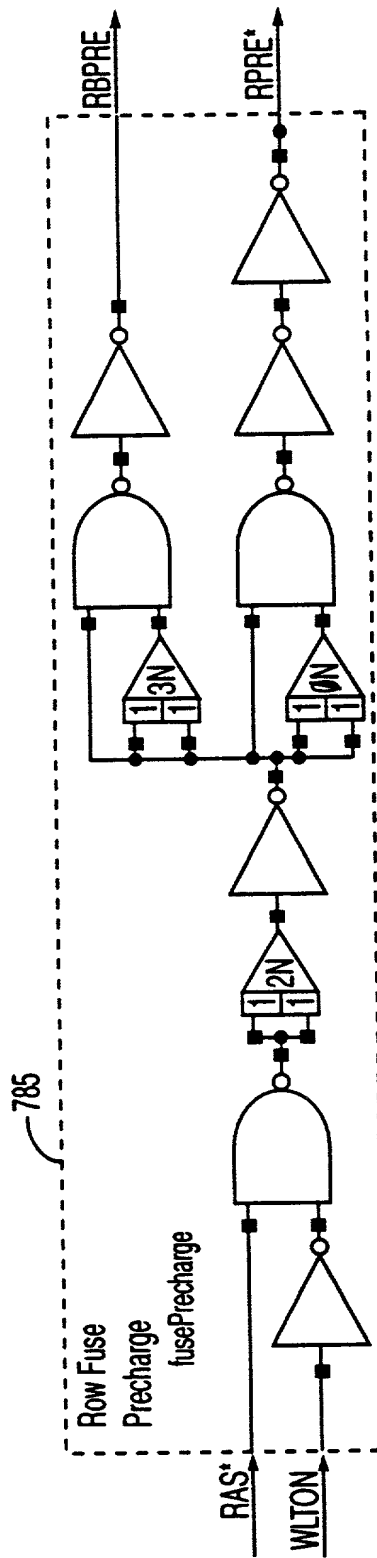


FIG. 471

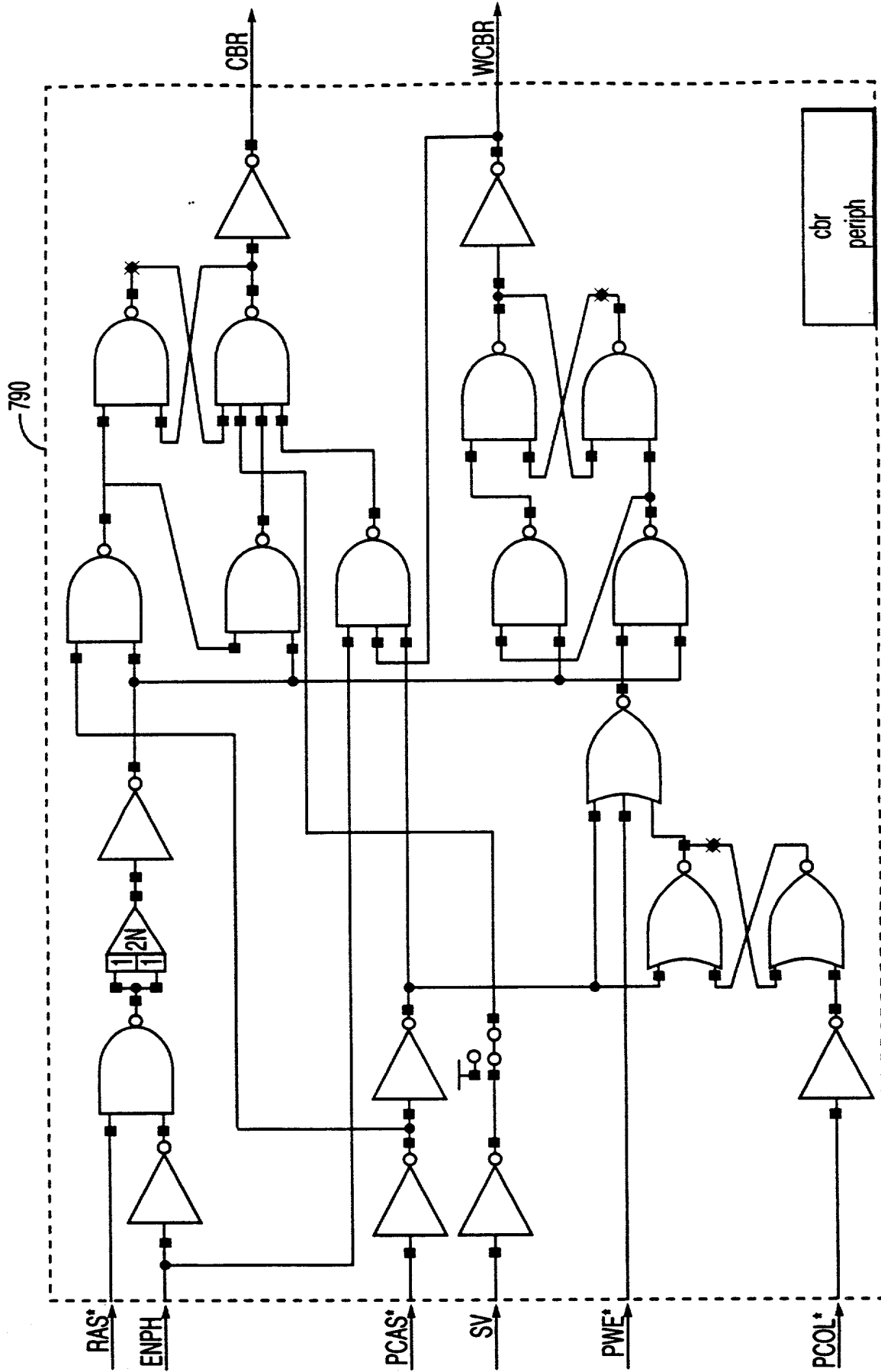


FIG. 47J

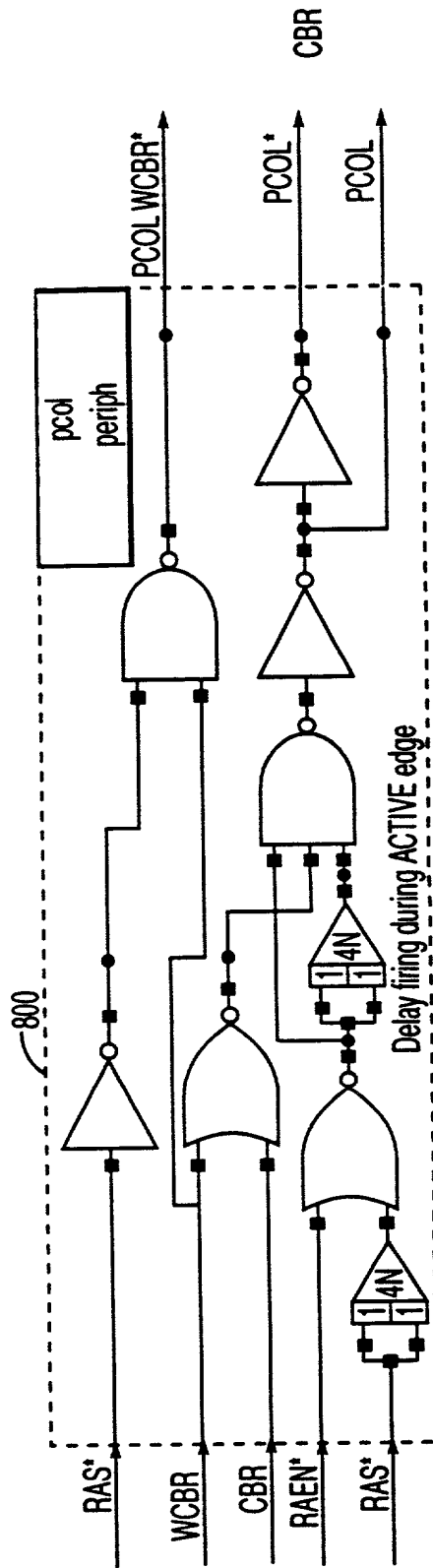


FIG. 47K

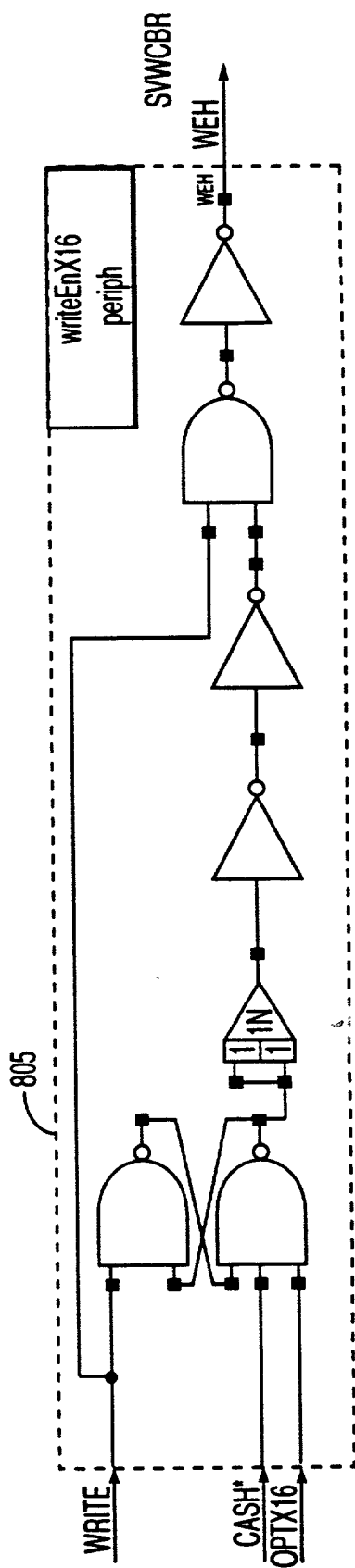


FIG. 47L

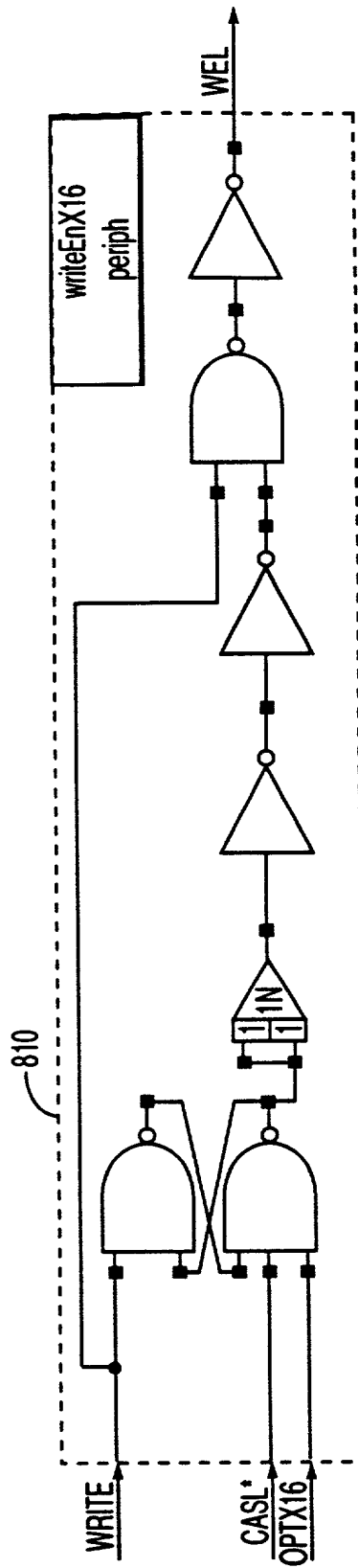


FIG. 47M

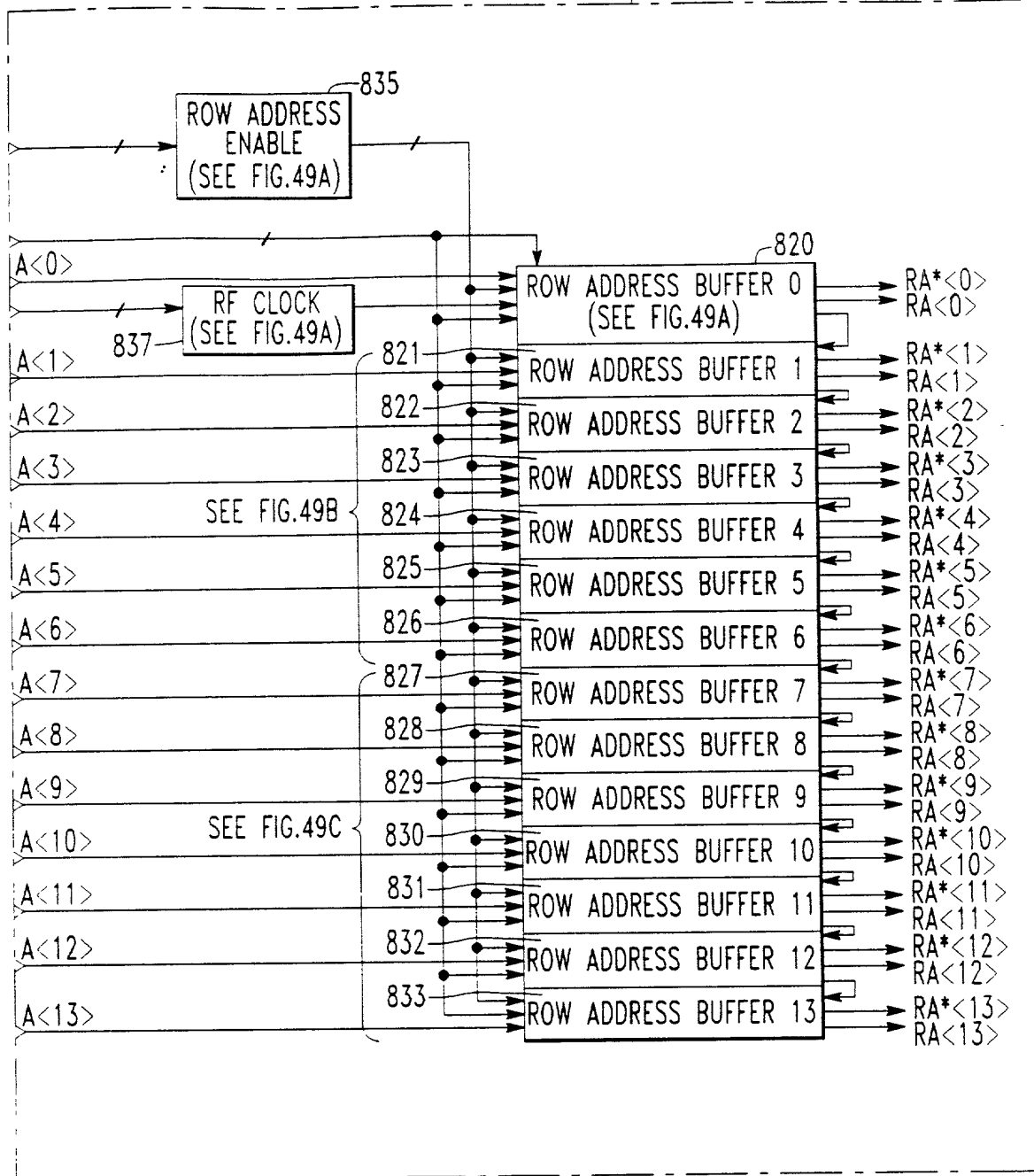


FIG. 48A

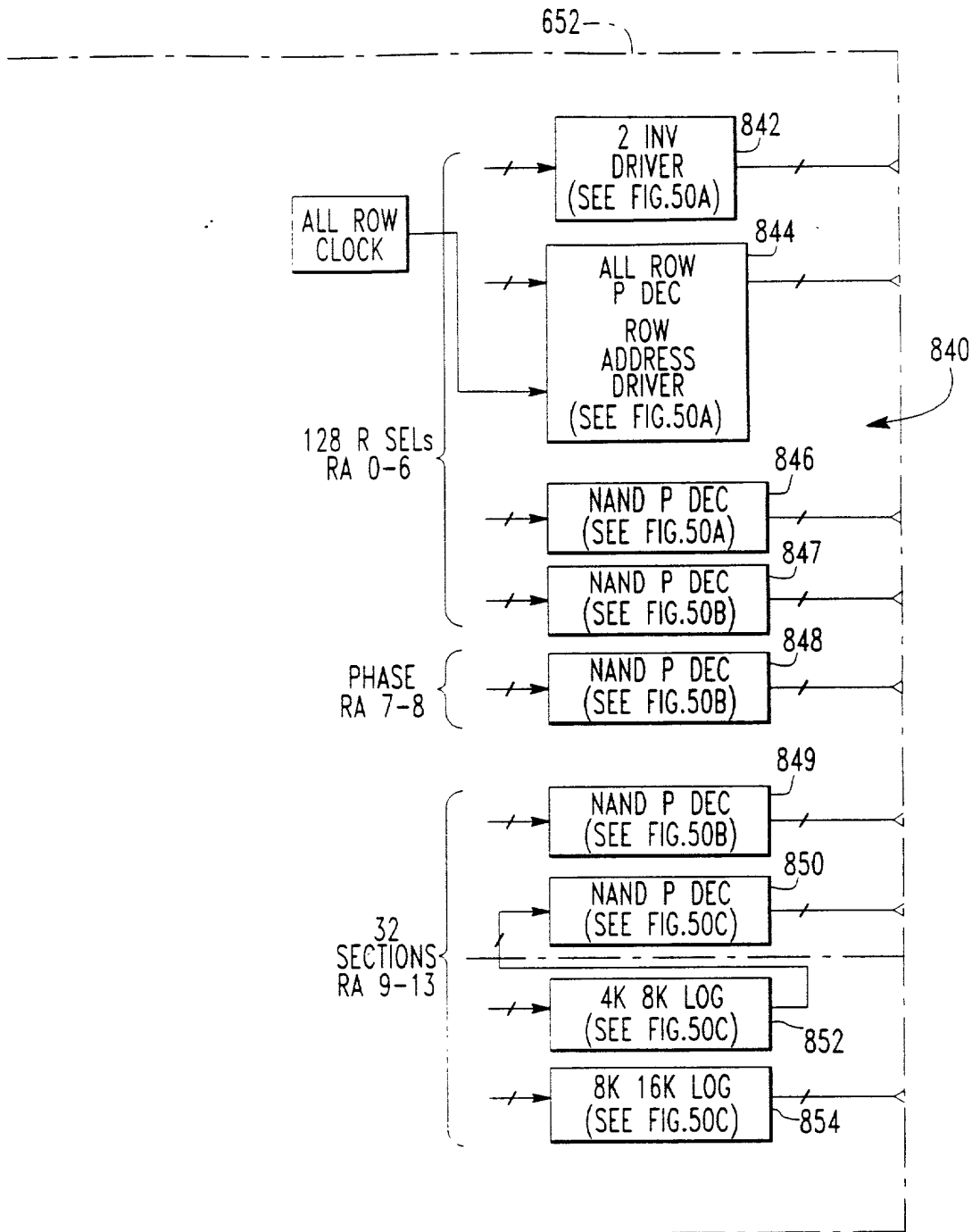


FIG. 48B



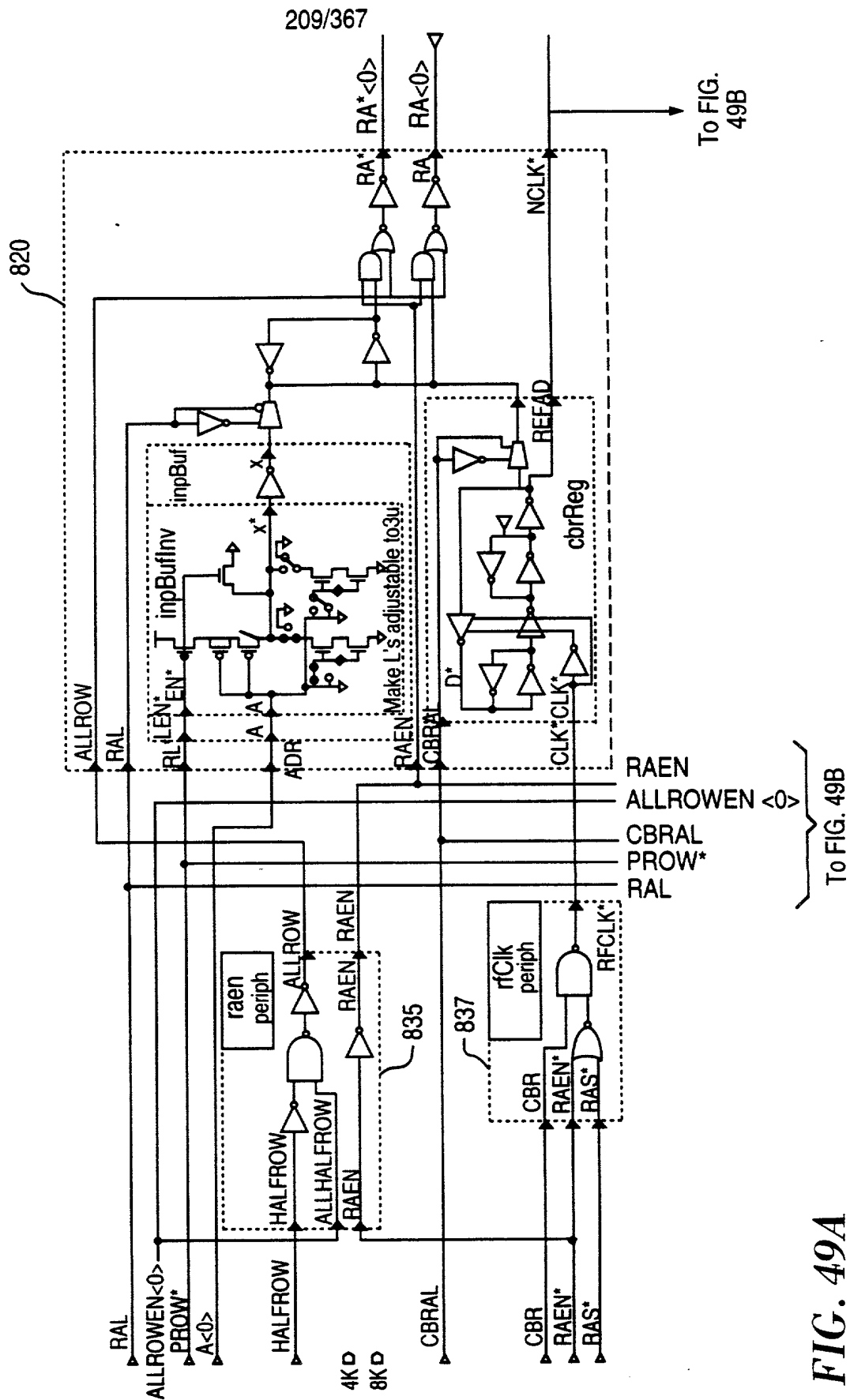
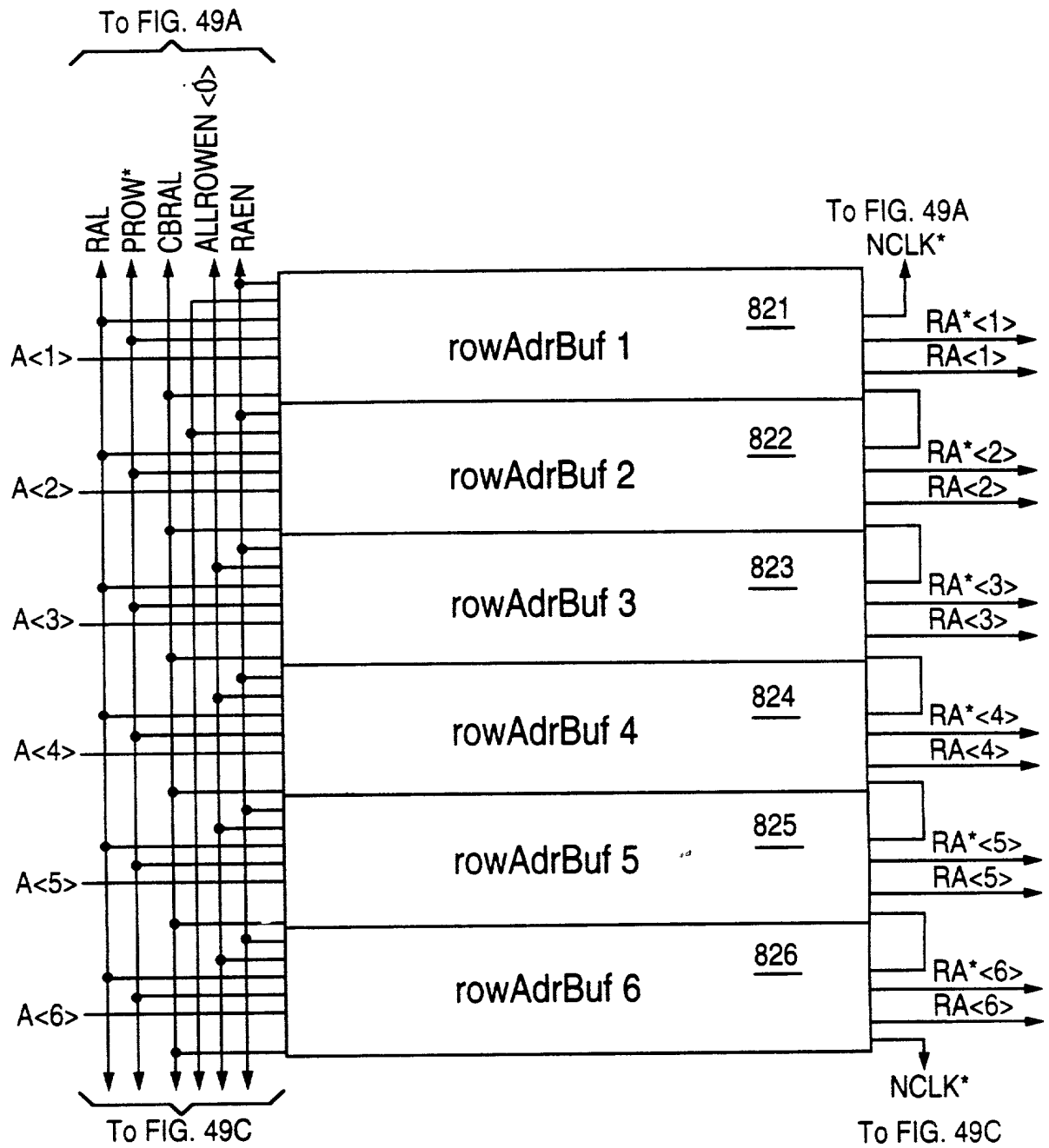
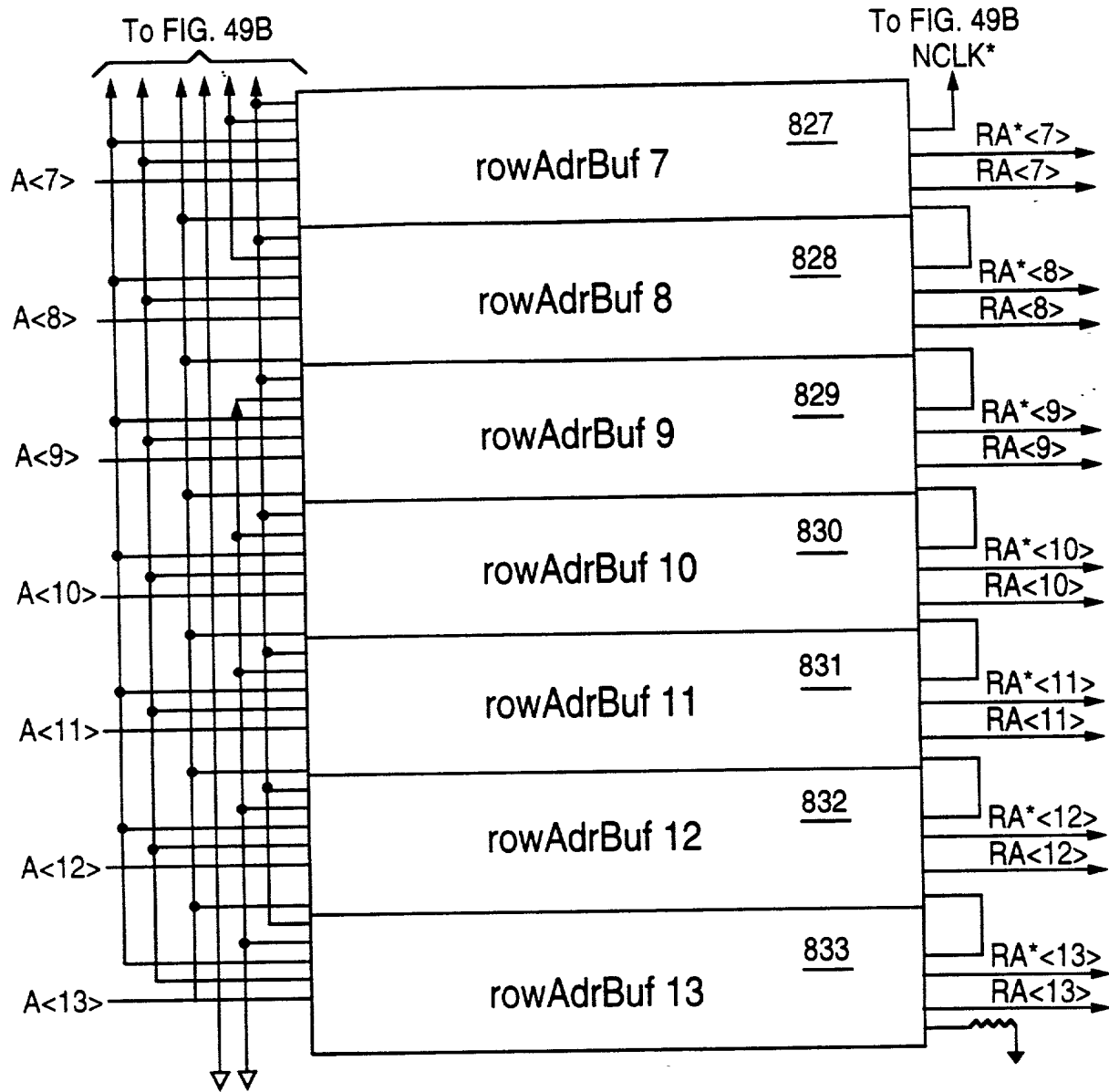


FIG. 49A



**FIG. 49B**

**FIG. 49C**

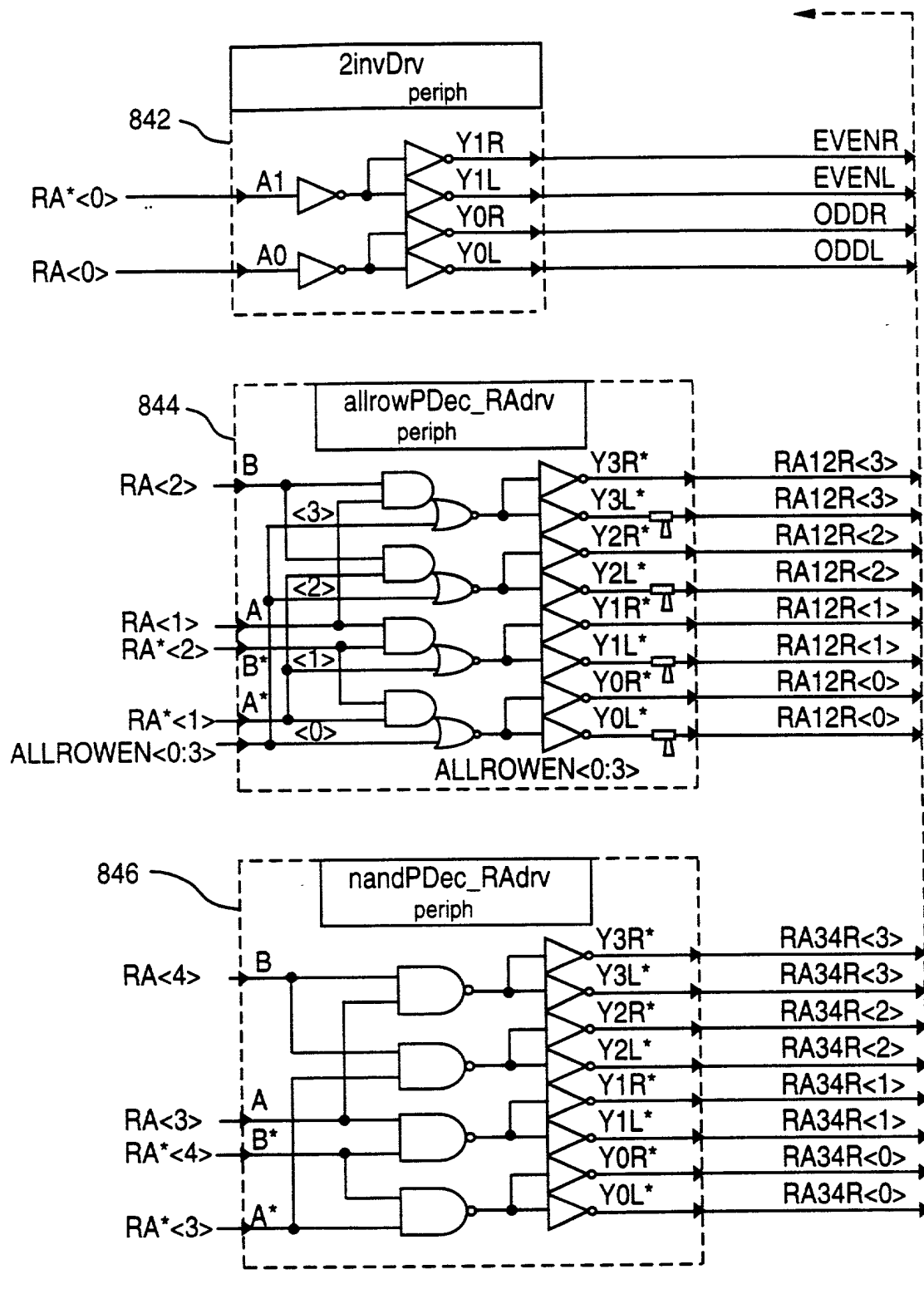
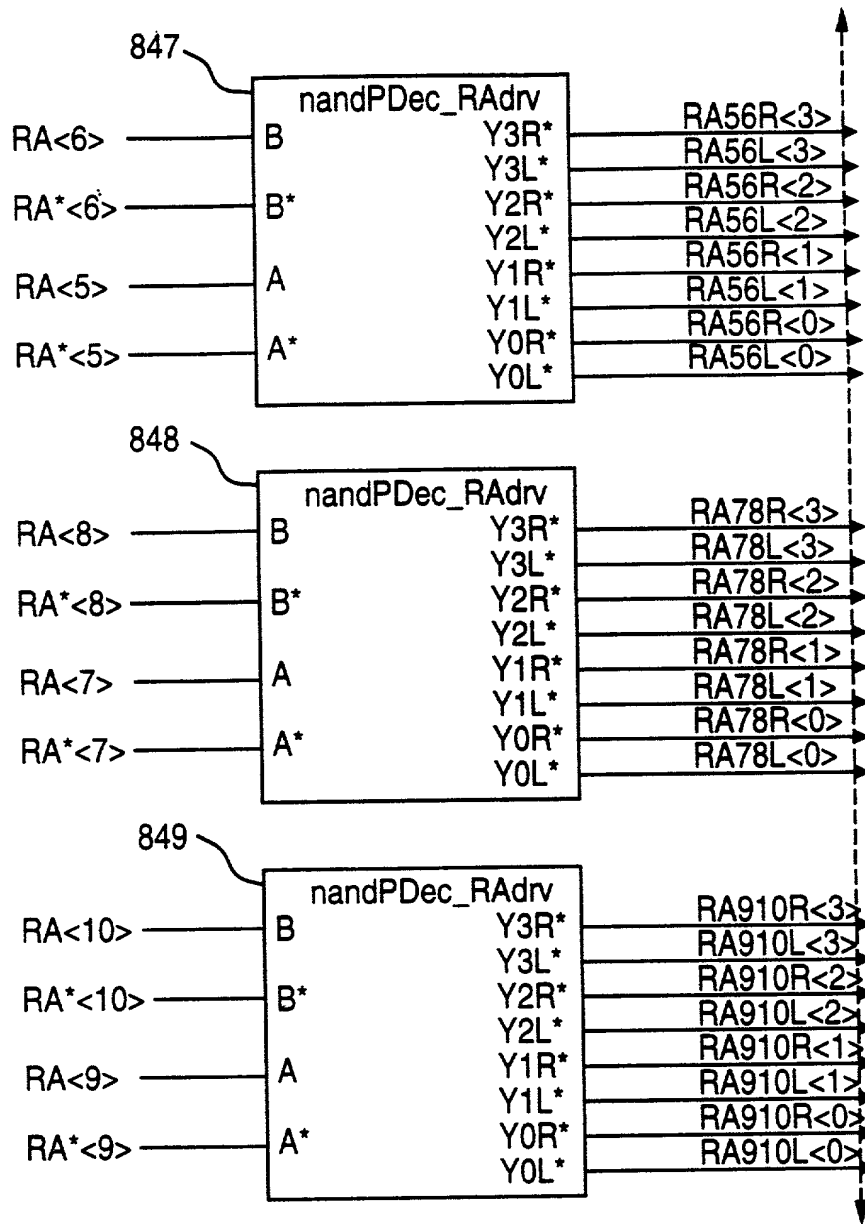


FIG. 50A

**FIG. 50B**

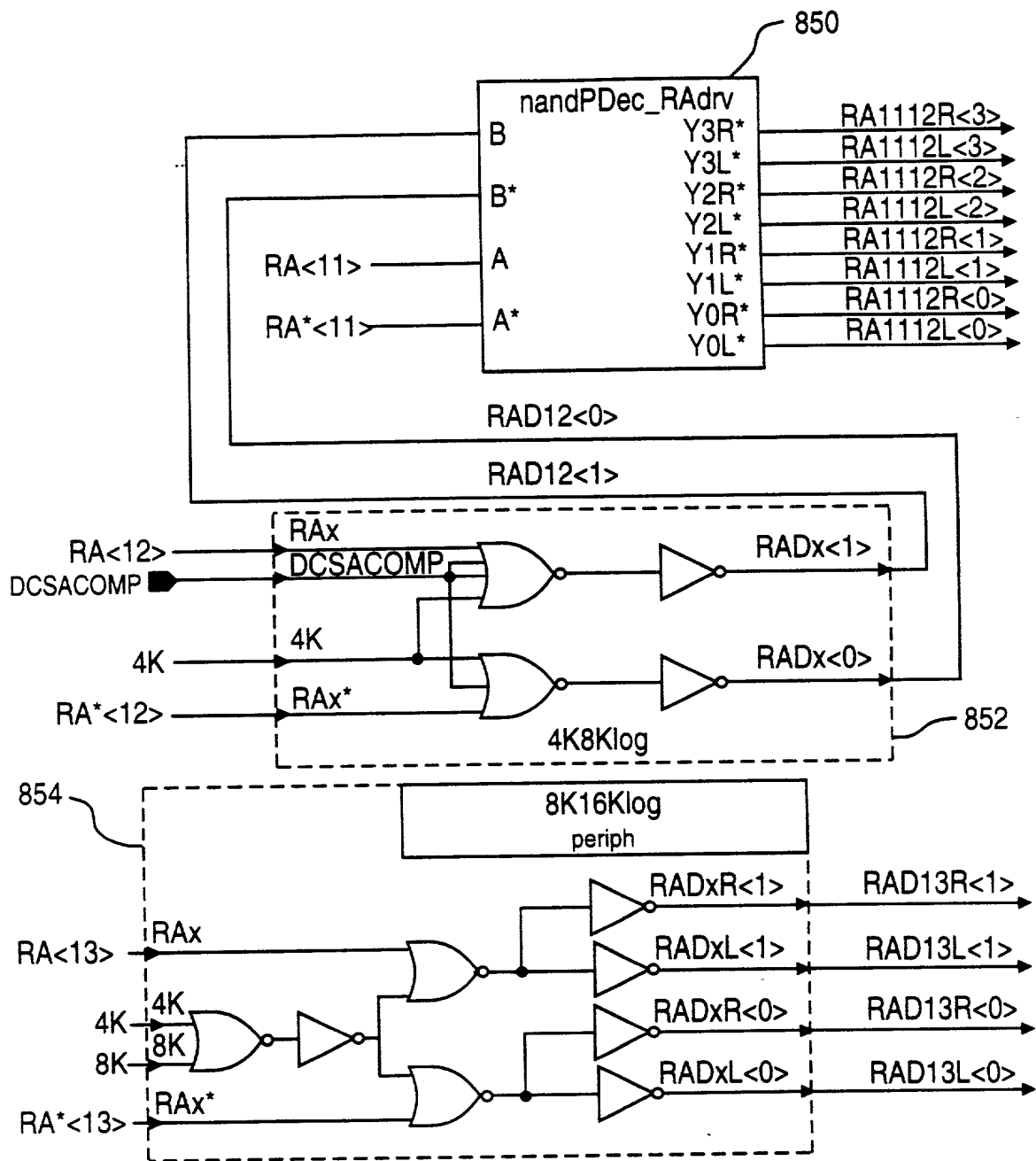


FIG. 50C

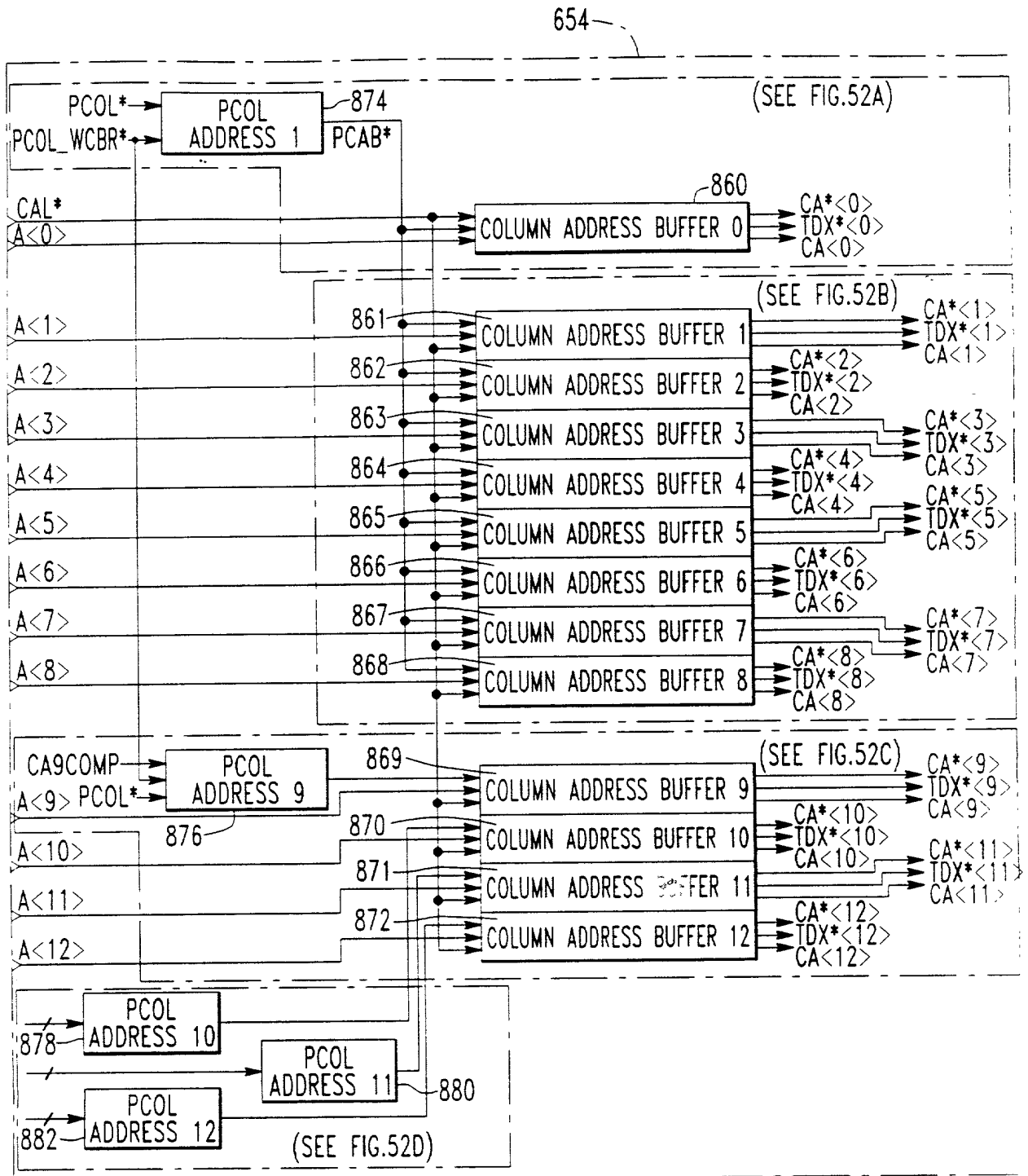


FIG. 51A

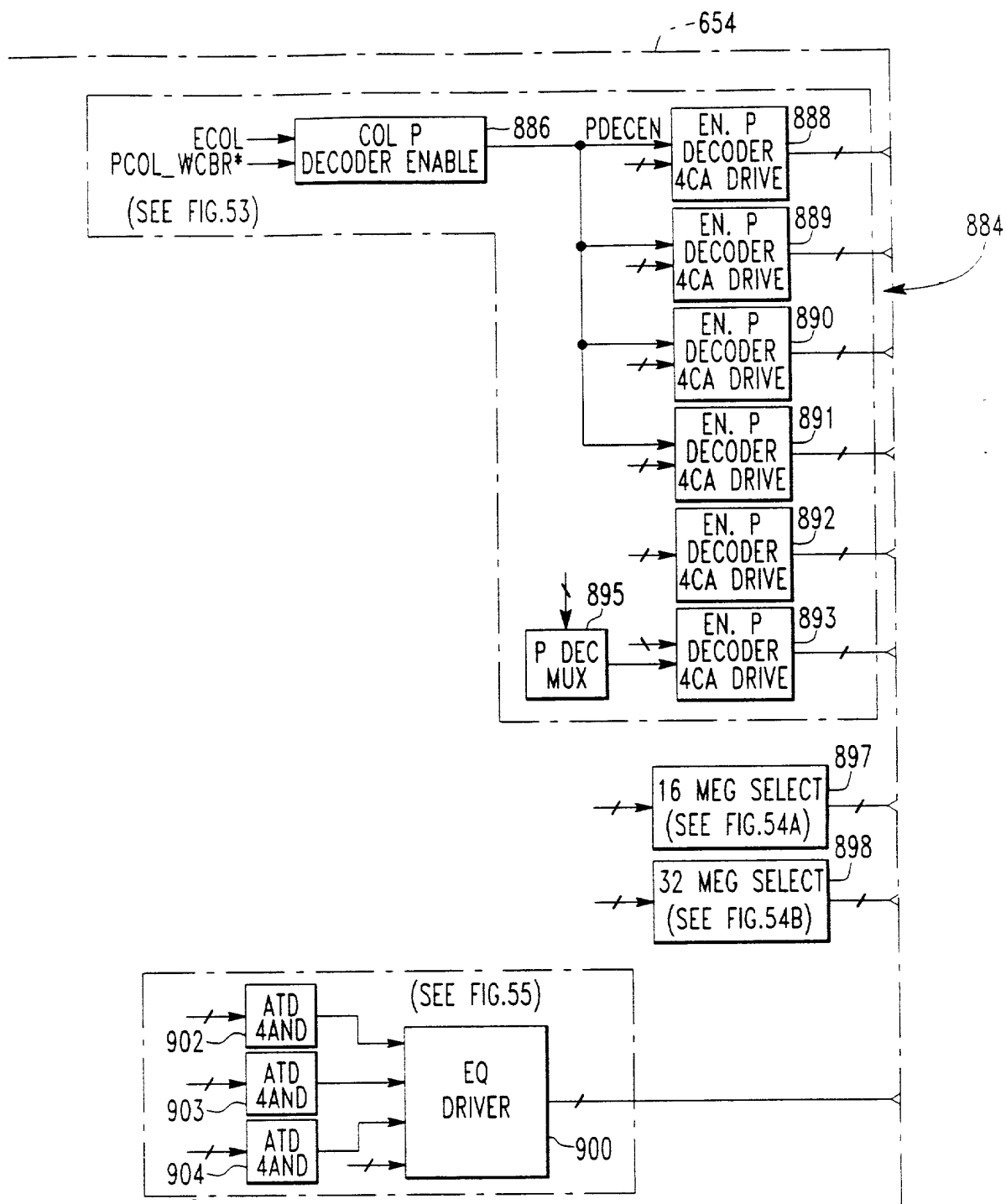


FIG. 51B



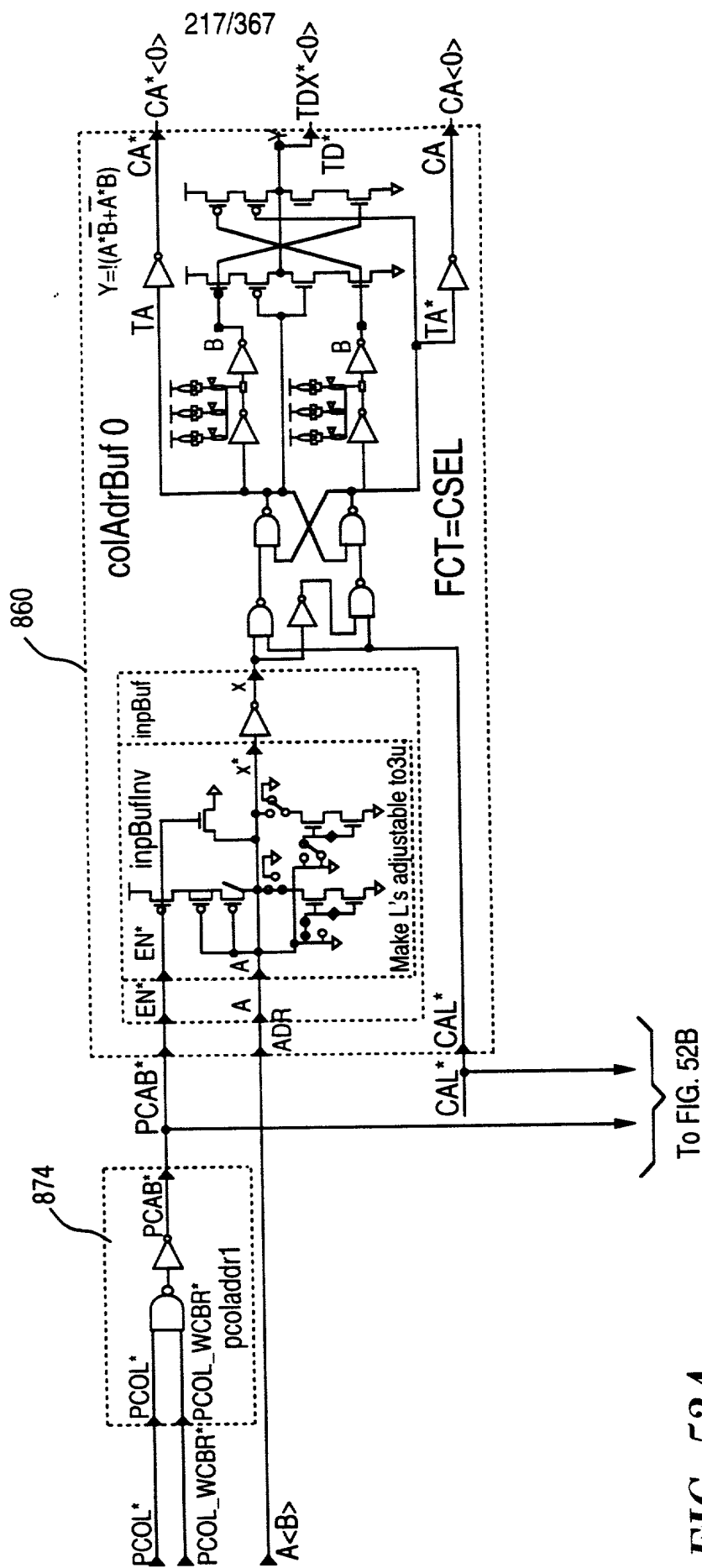


FIG. 52A

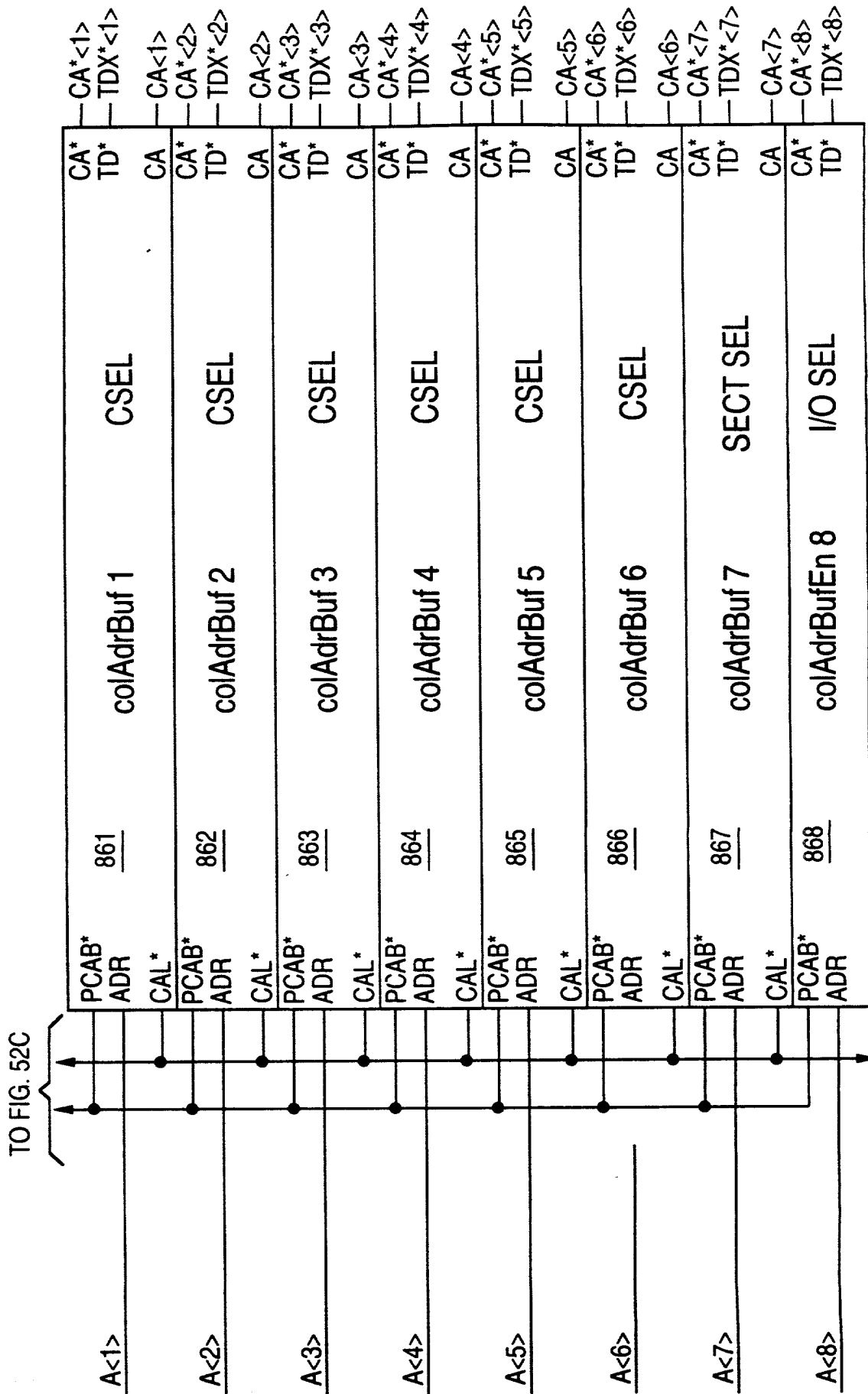


FIG. 52B



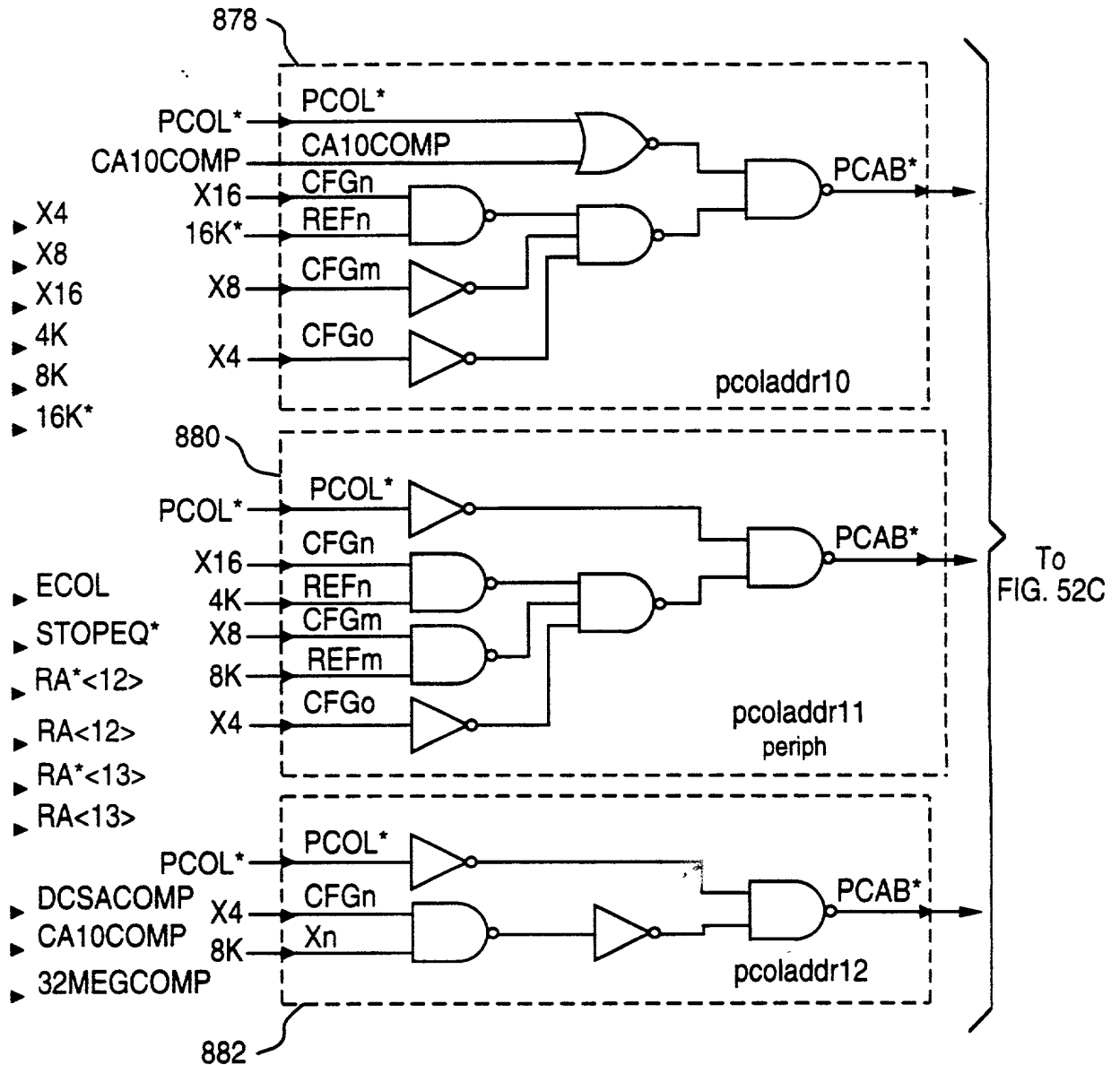
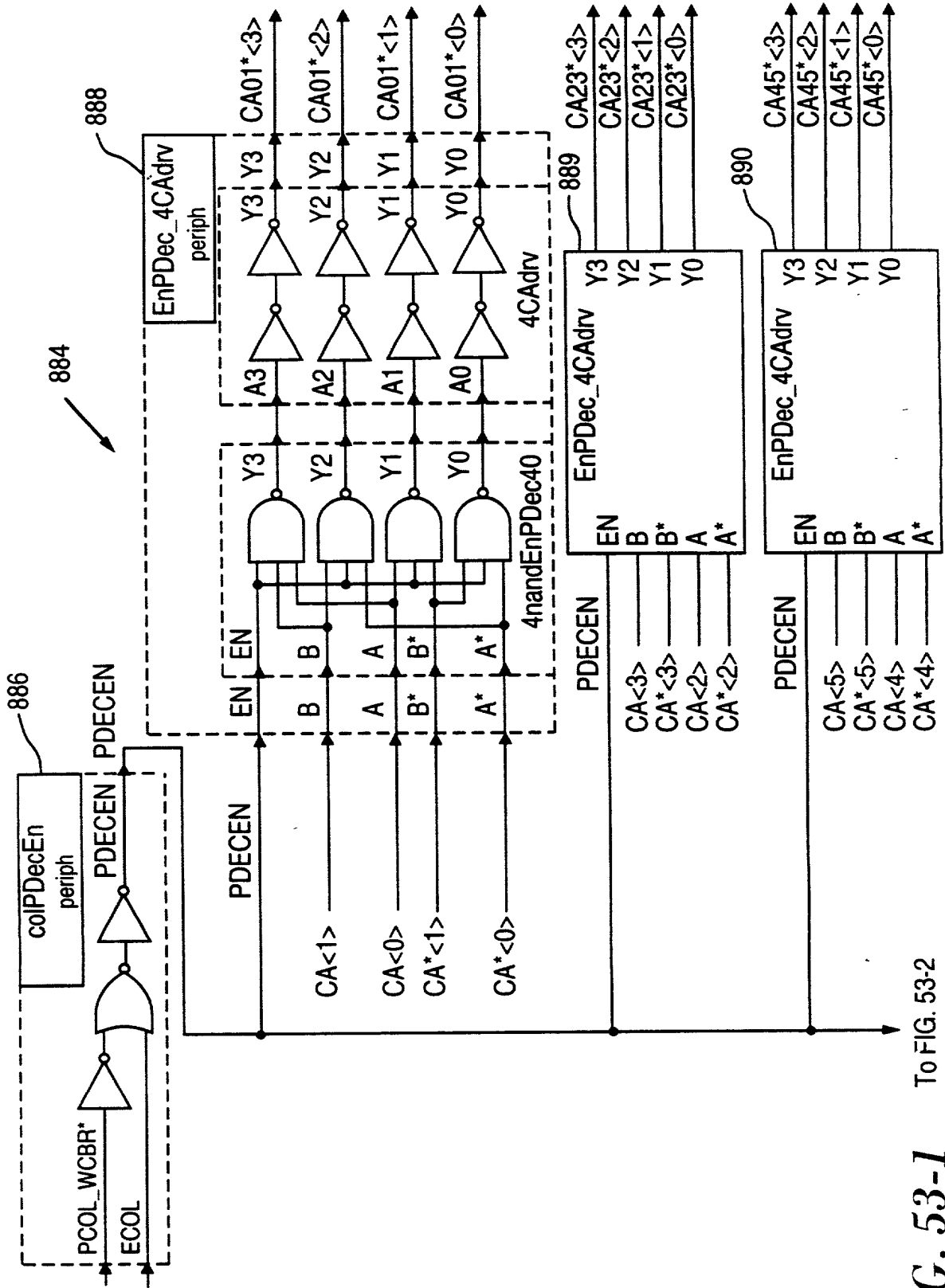


FIG. 52D



To FIG. 53-2

FIG. 53-1

To FIG. 53-1

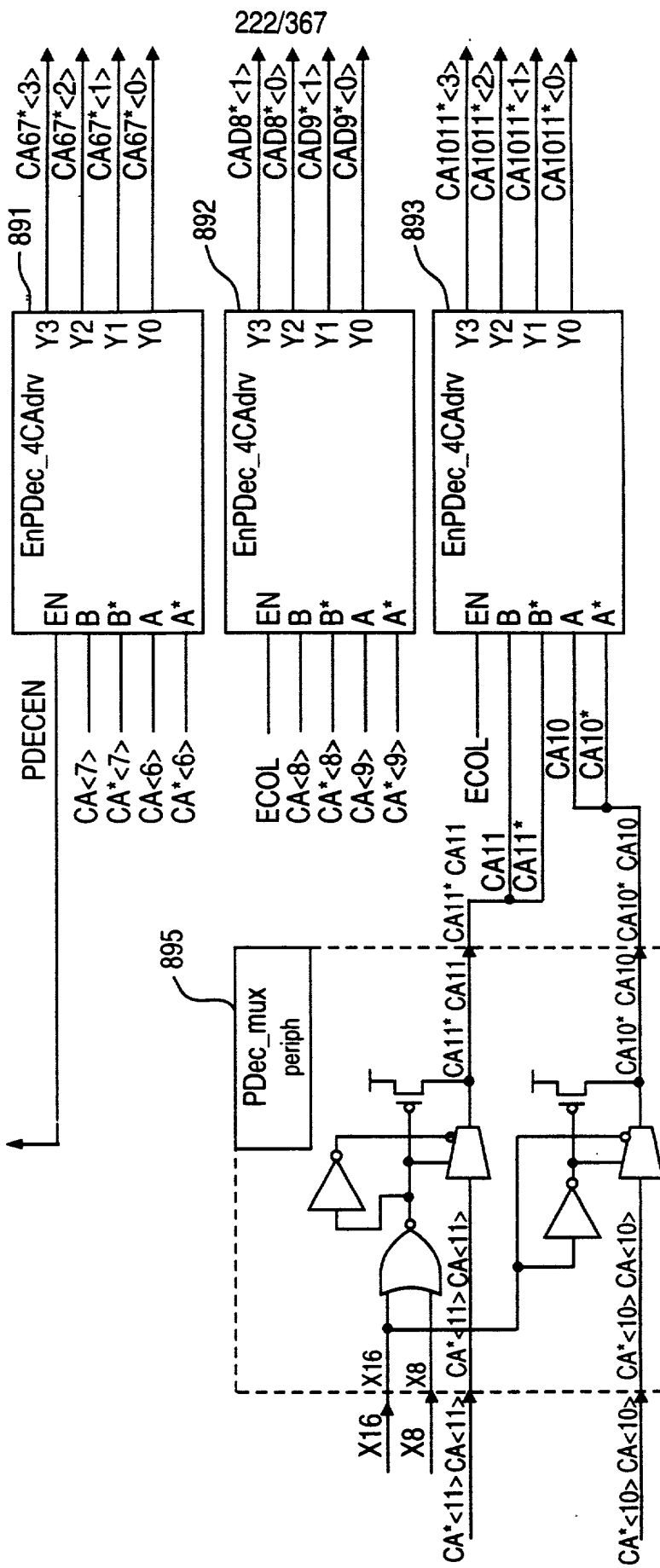


FIG. 53-2

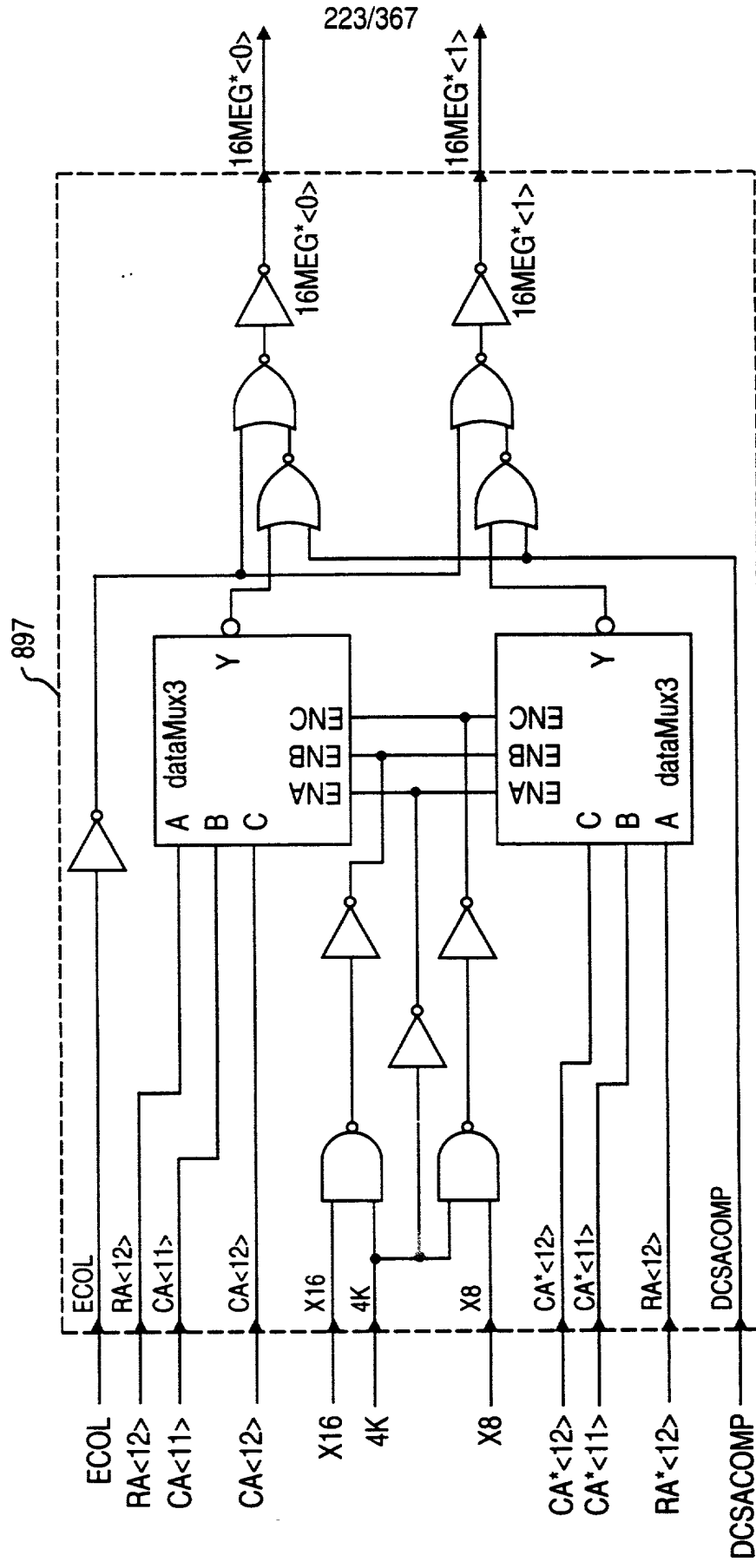


FIG. 54A

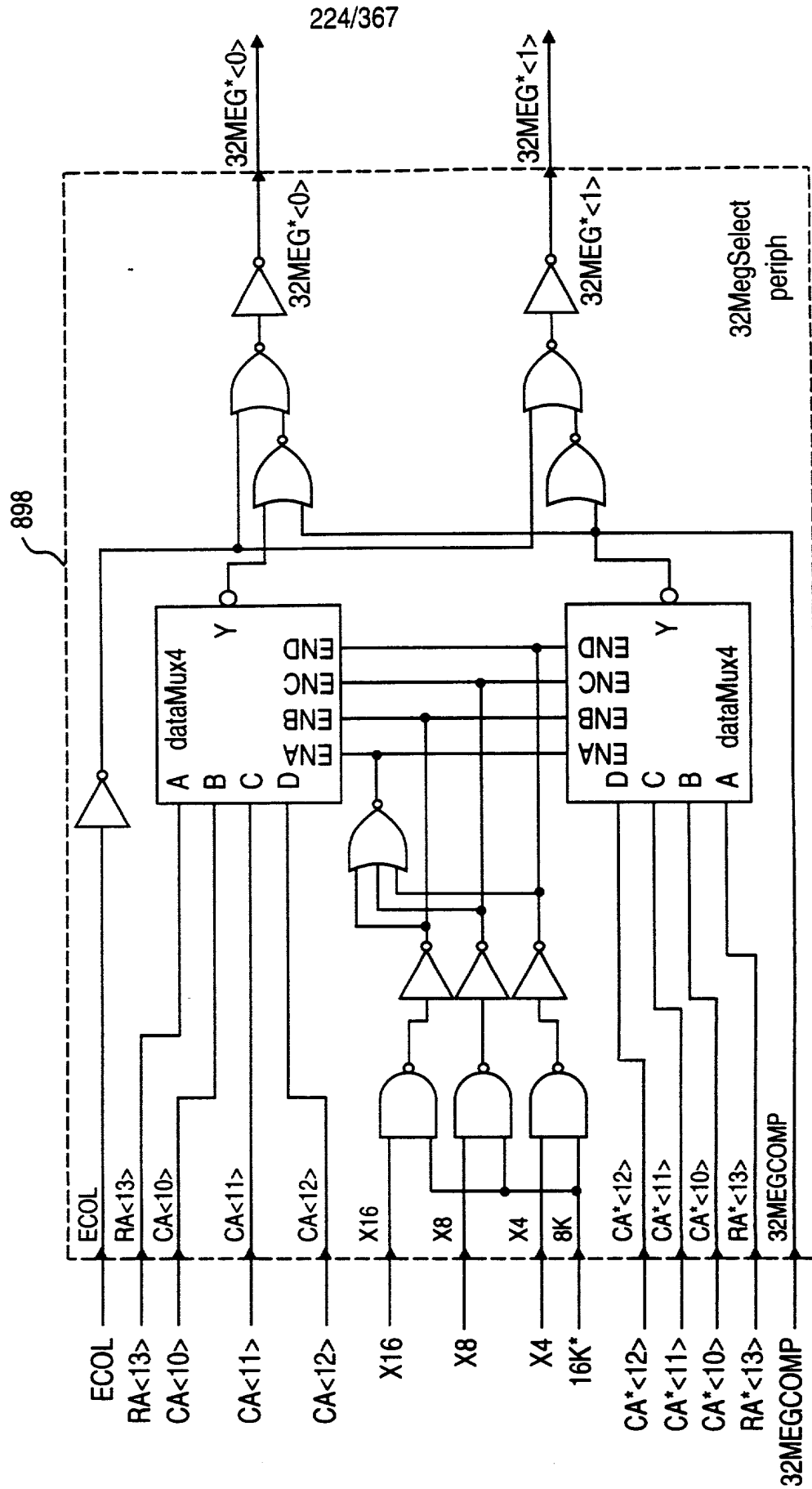


FIG. 54B





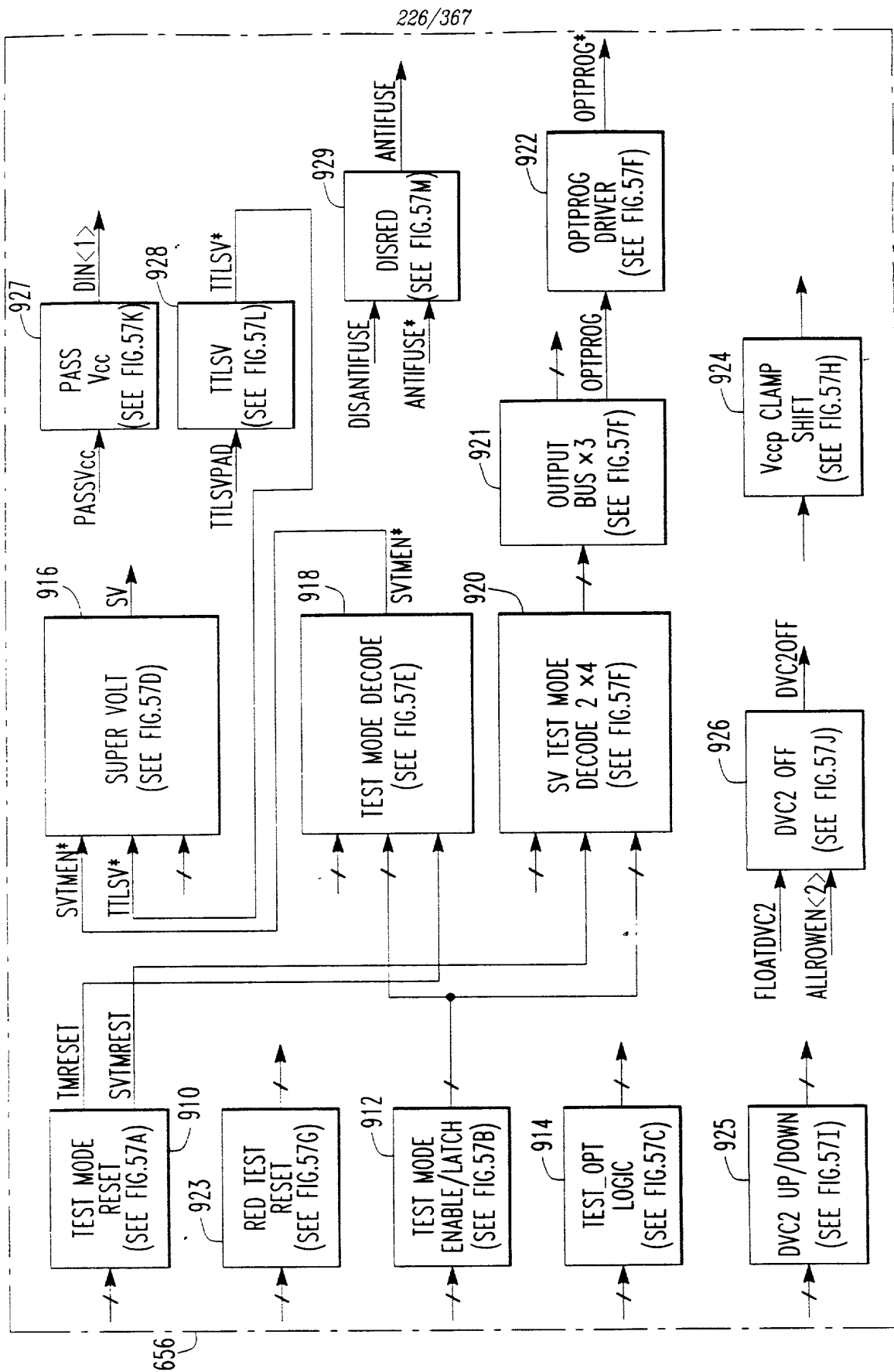


FIG. 56

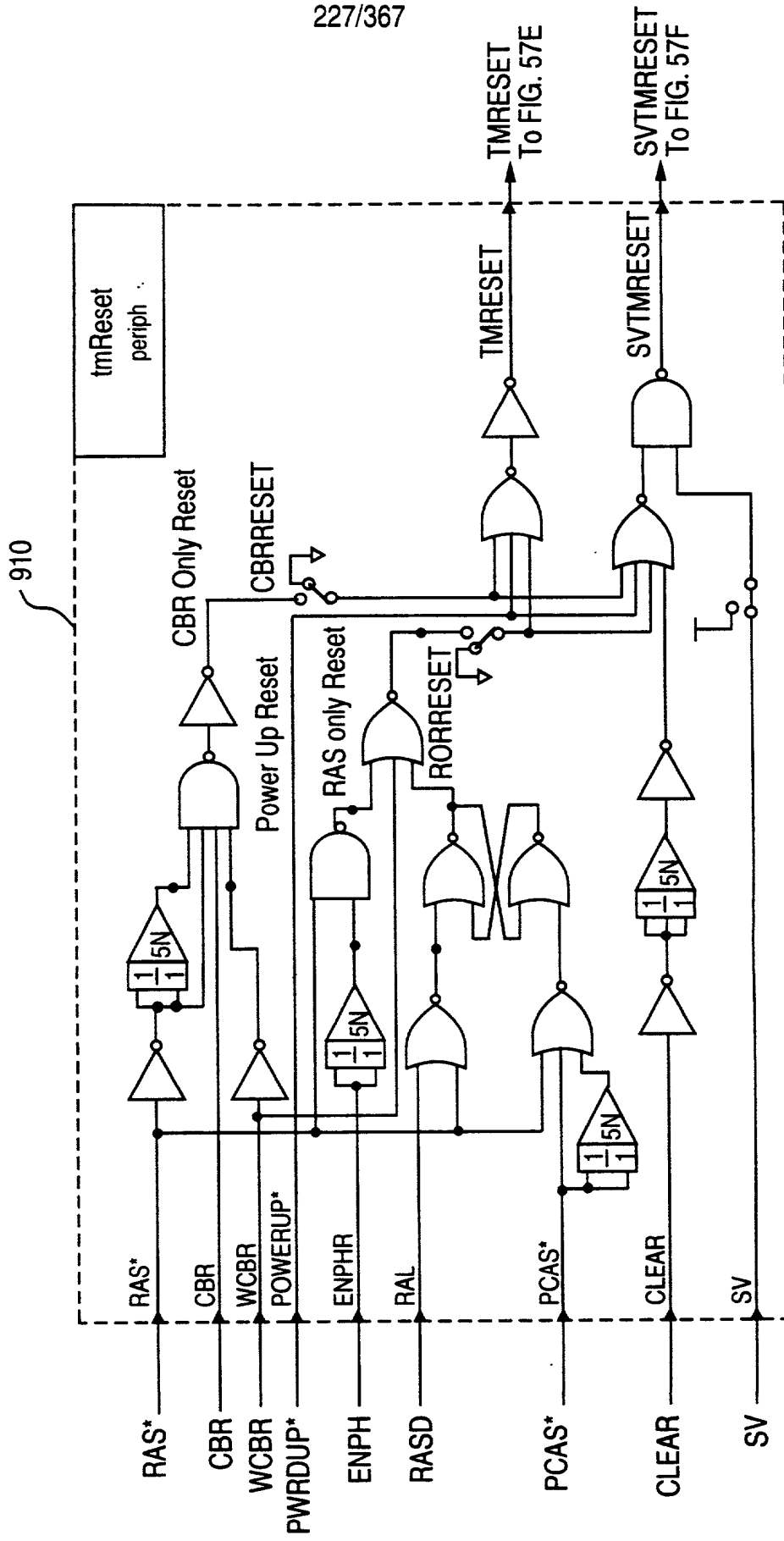


FIG. 57A

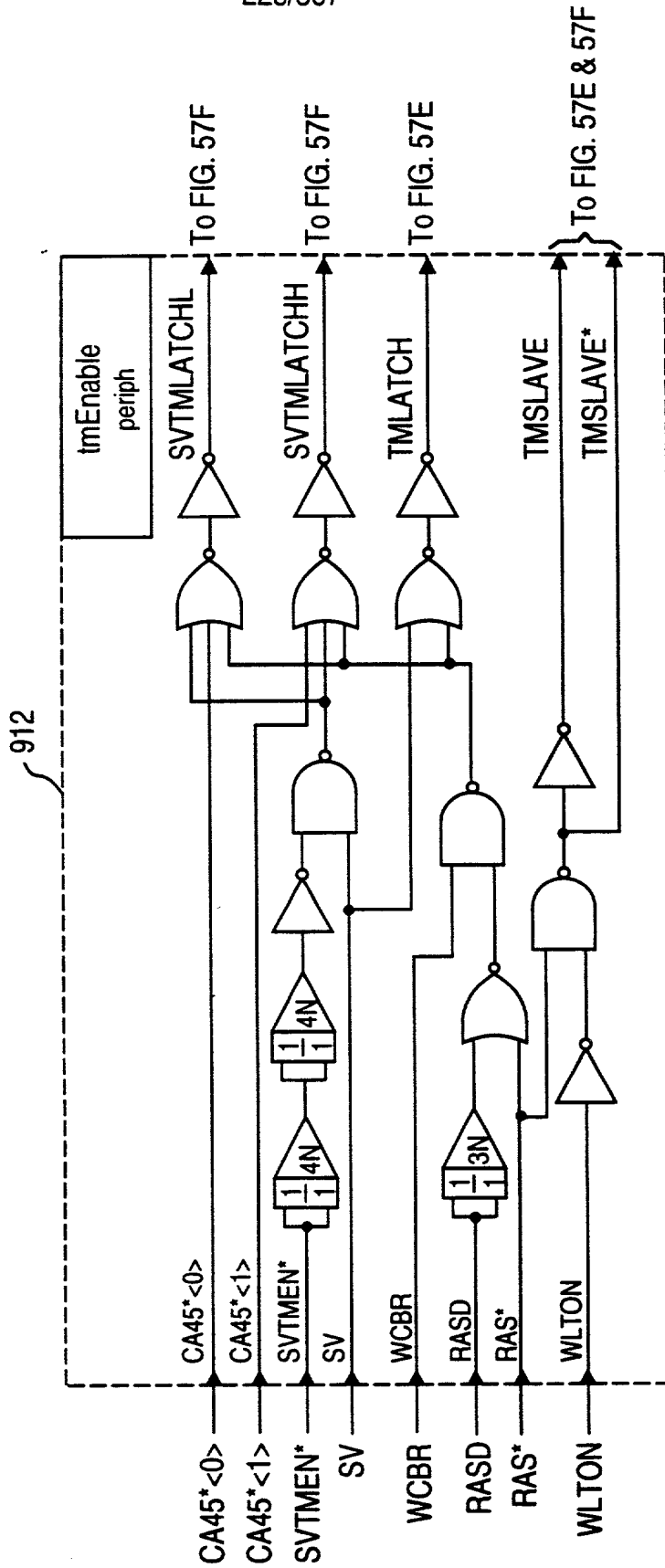


FIG. 57B

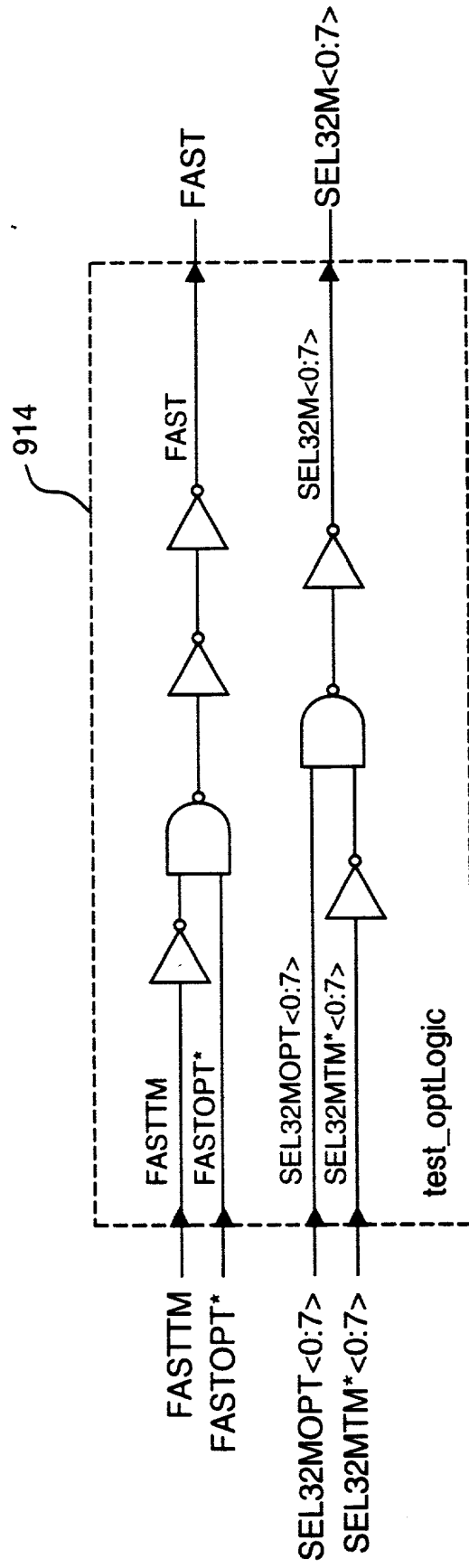
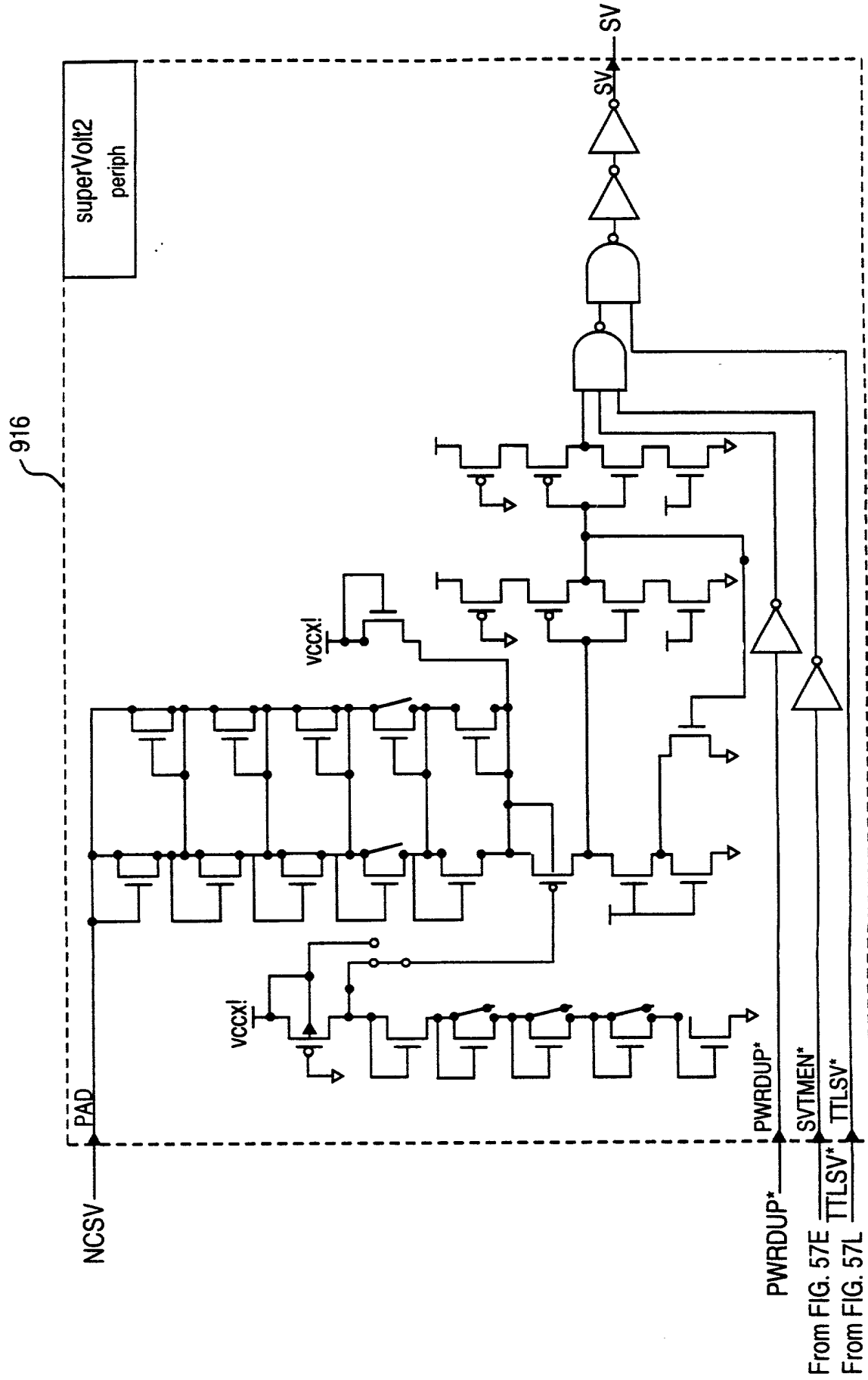


FIG. 57C

FIG. 57D



From FIG. 57E

From FIG. 57L

FIG. 57D



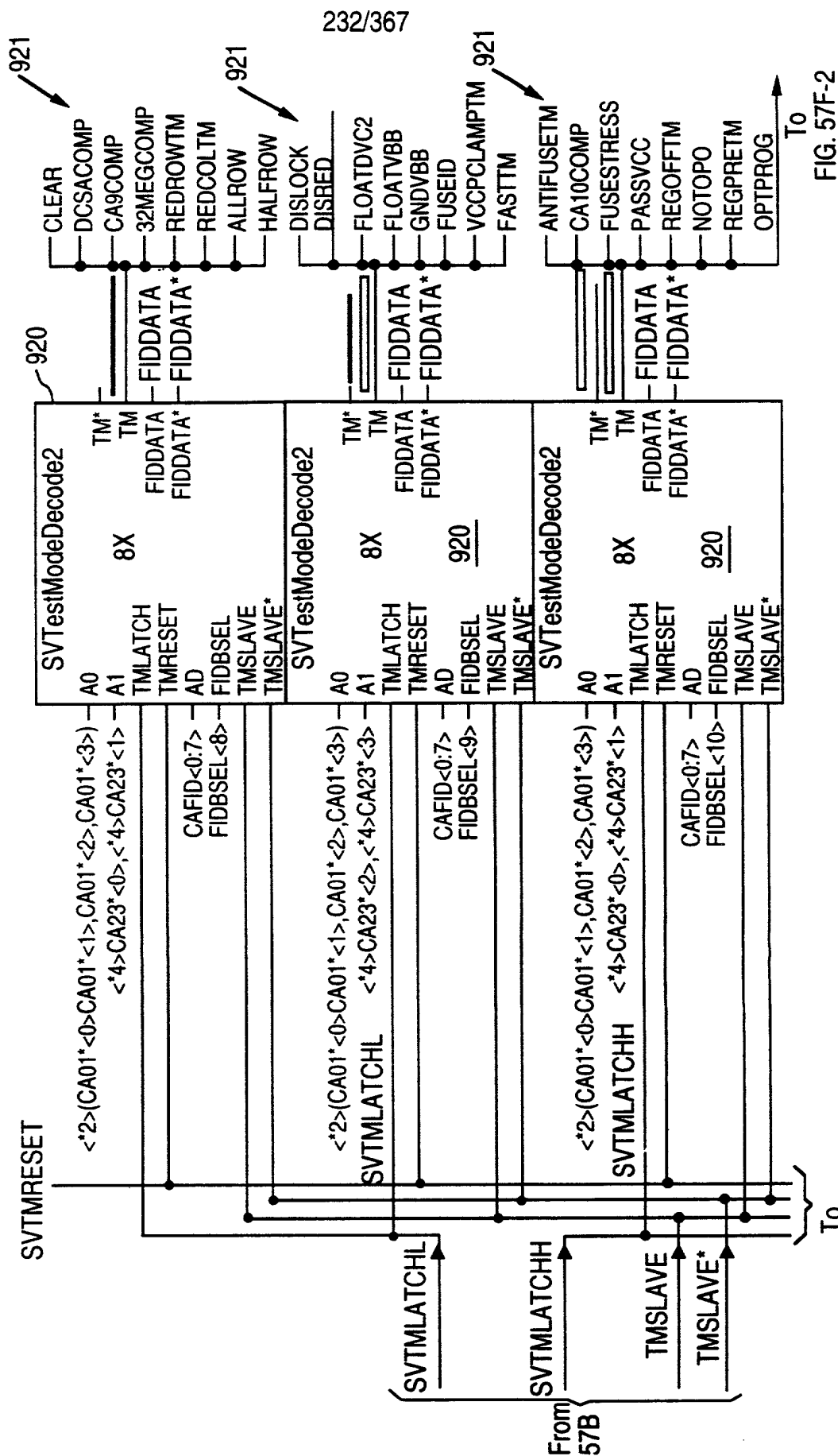


FIG. 57F-3

**FIG. 57F-1**



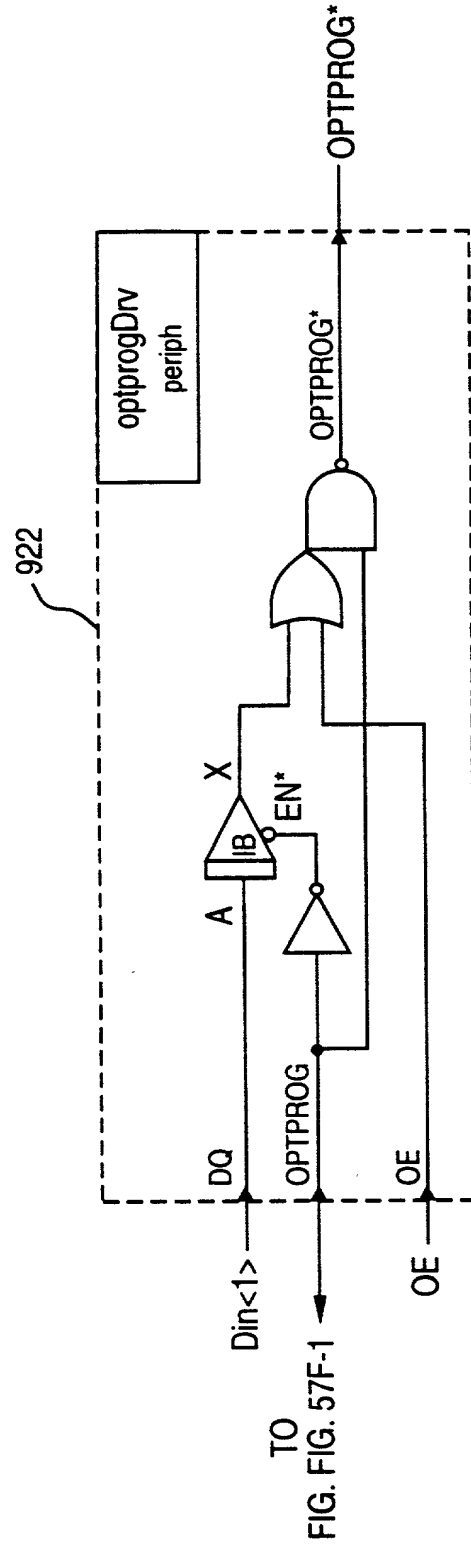


FIG. 57F-2

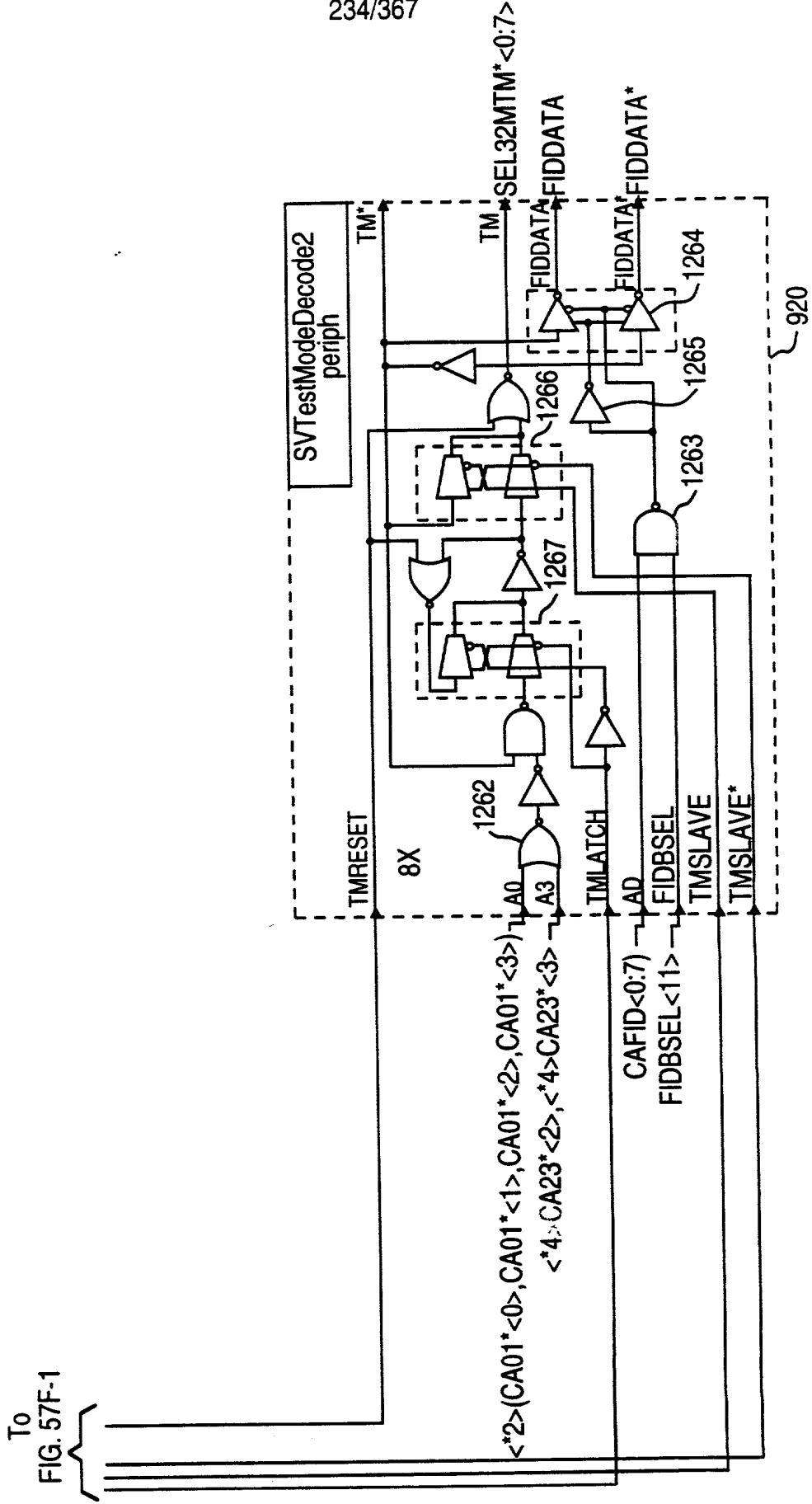


FIG. 57F-3

FIG. 57C

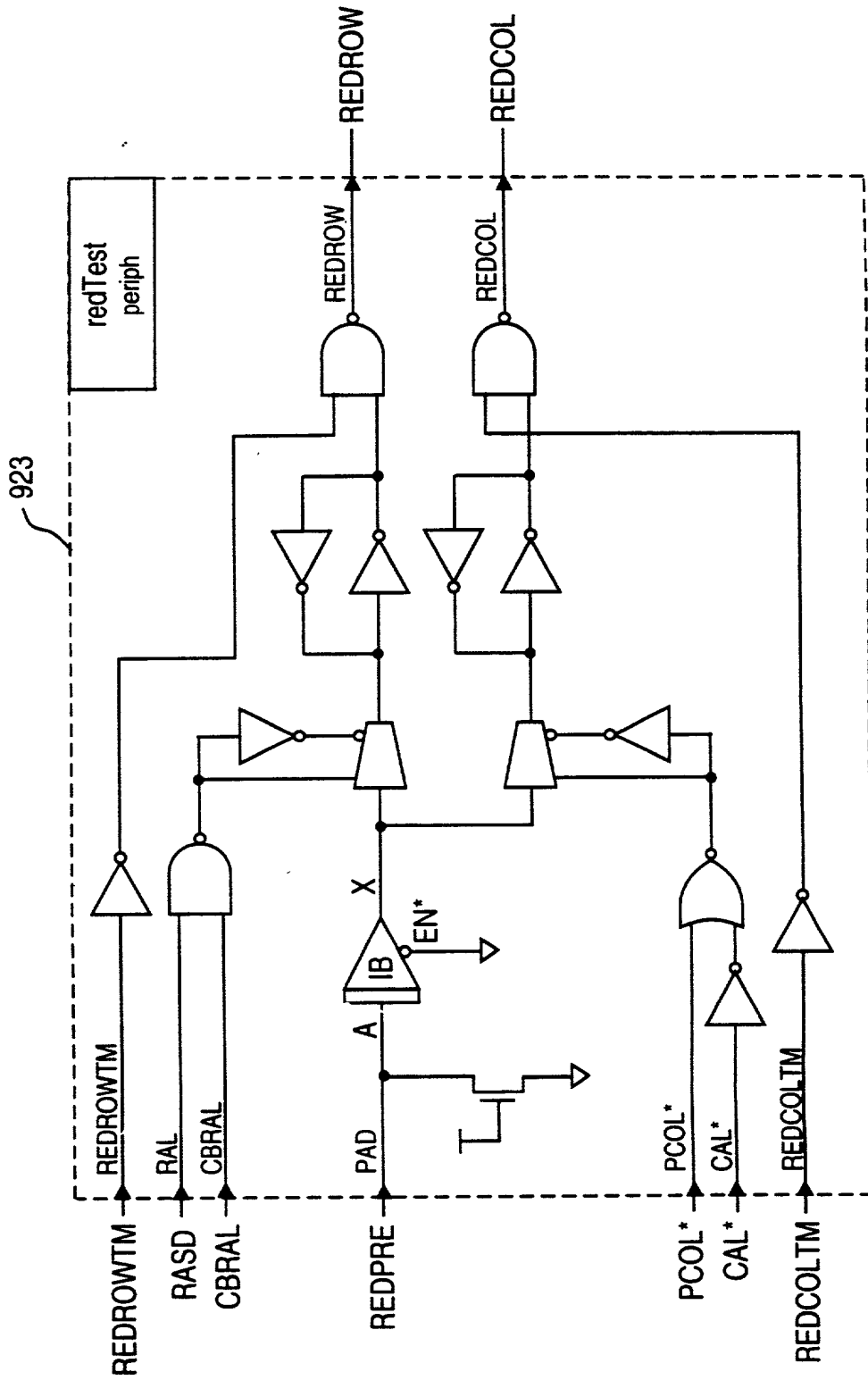
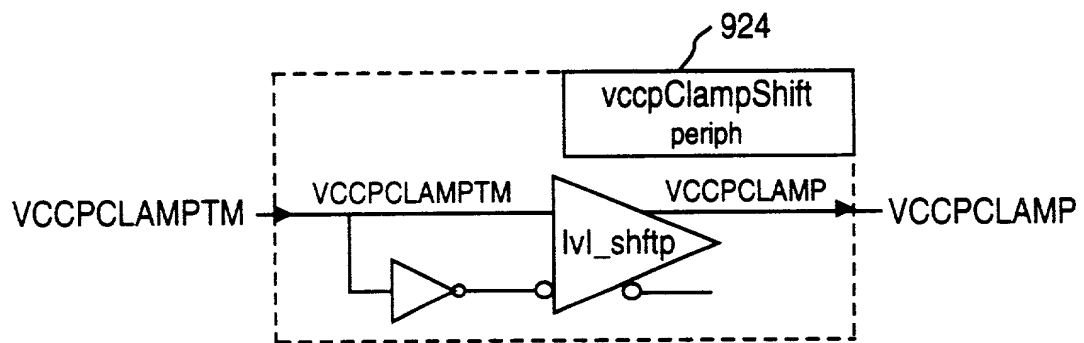


FIG. 57C

**FIG. 57H**

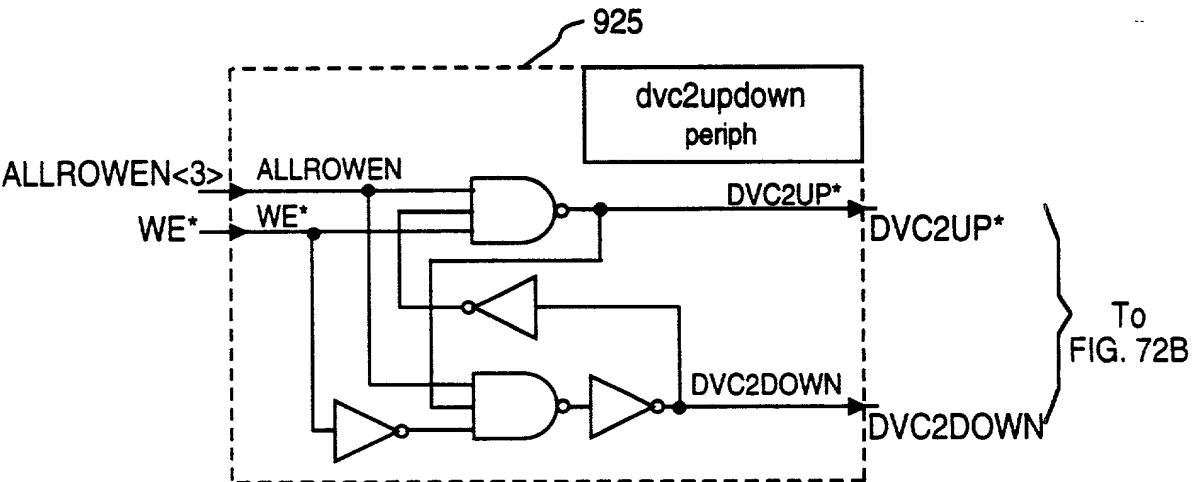
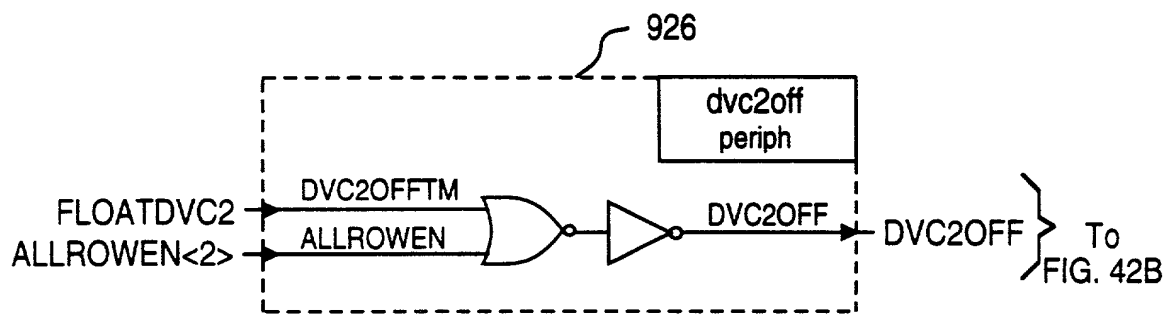
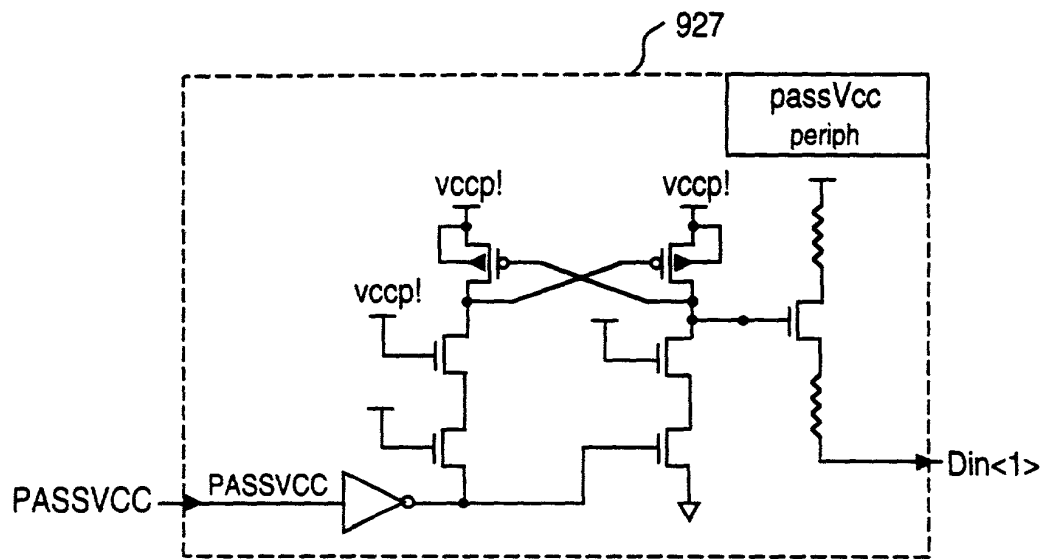
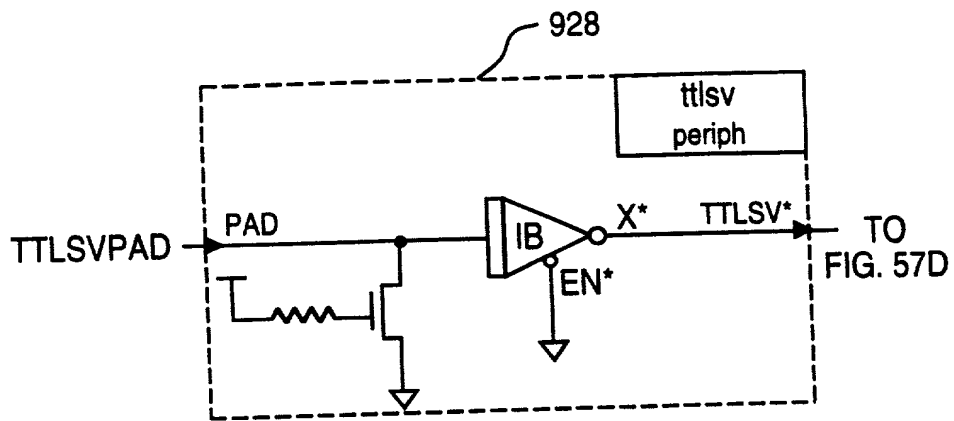


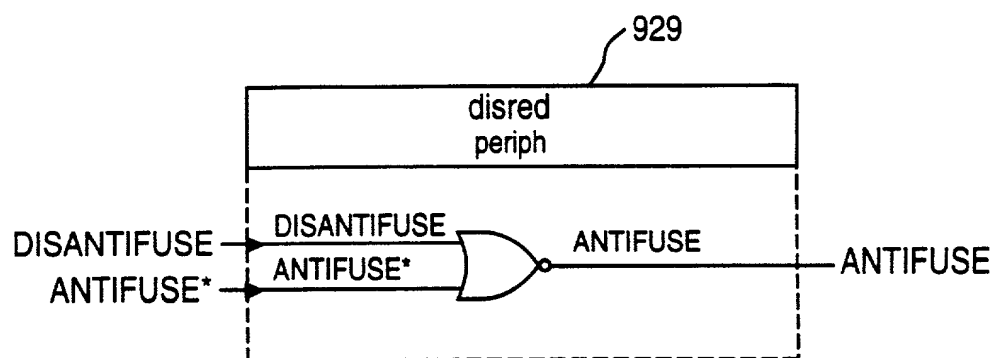
FIG. 57I

**FIG. 57J**

**FIG. 57K**

**FIG. 57L**



**FIG. 57M**

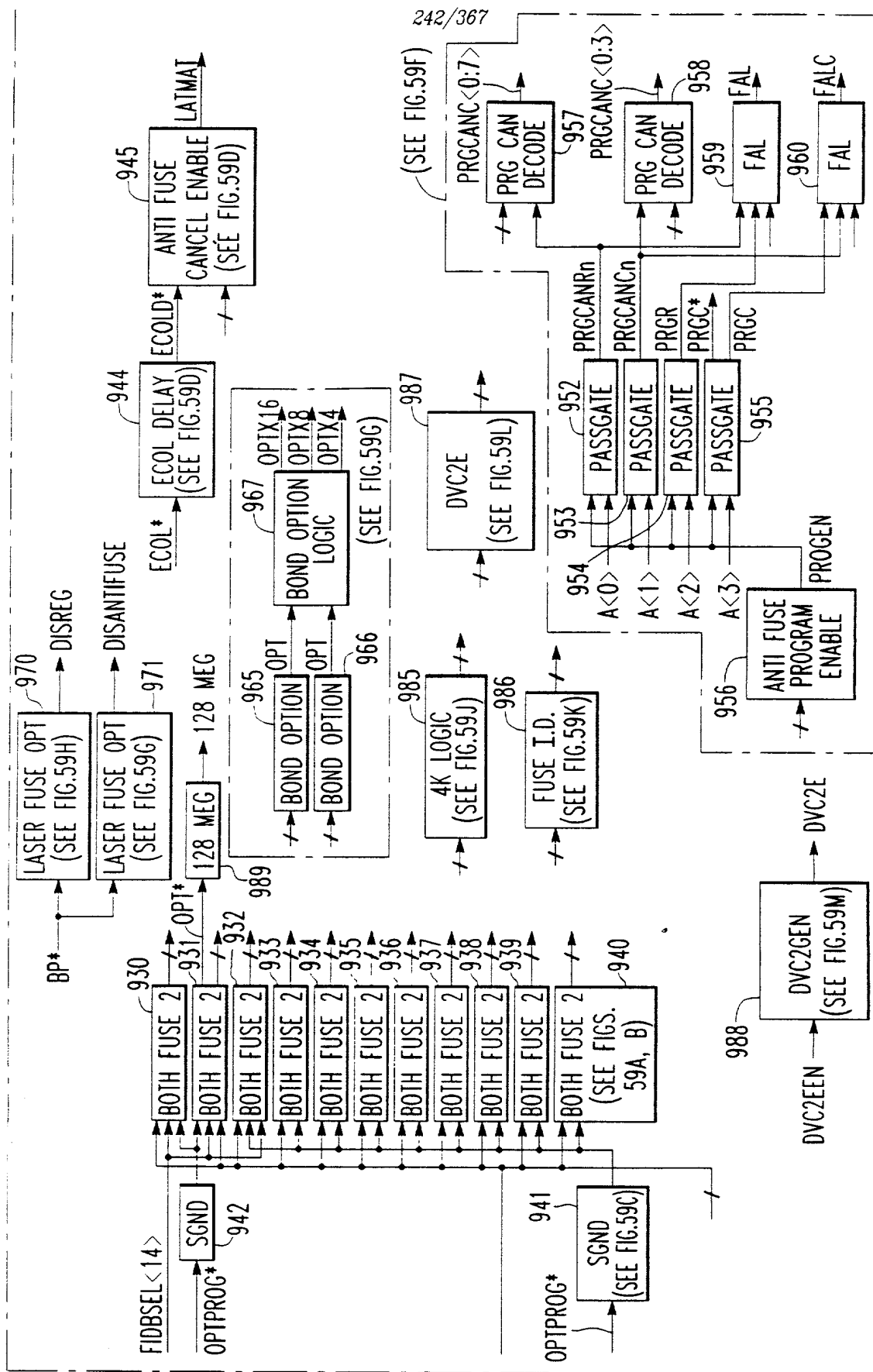


FIG. 58A

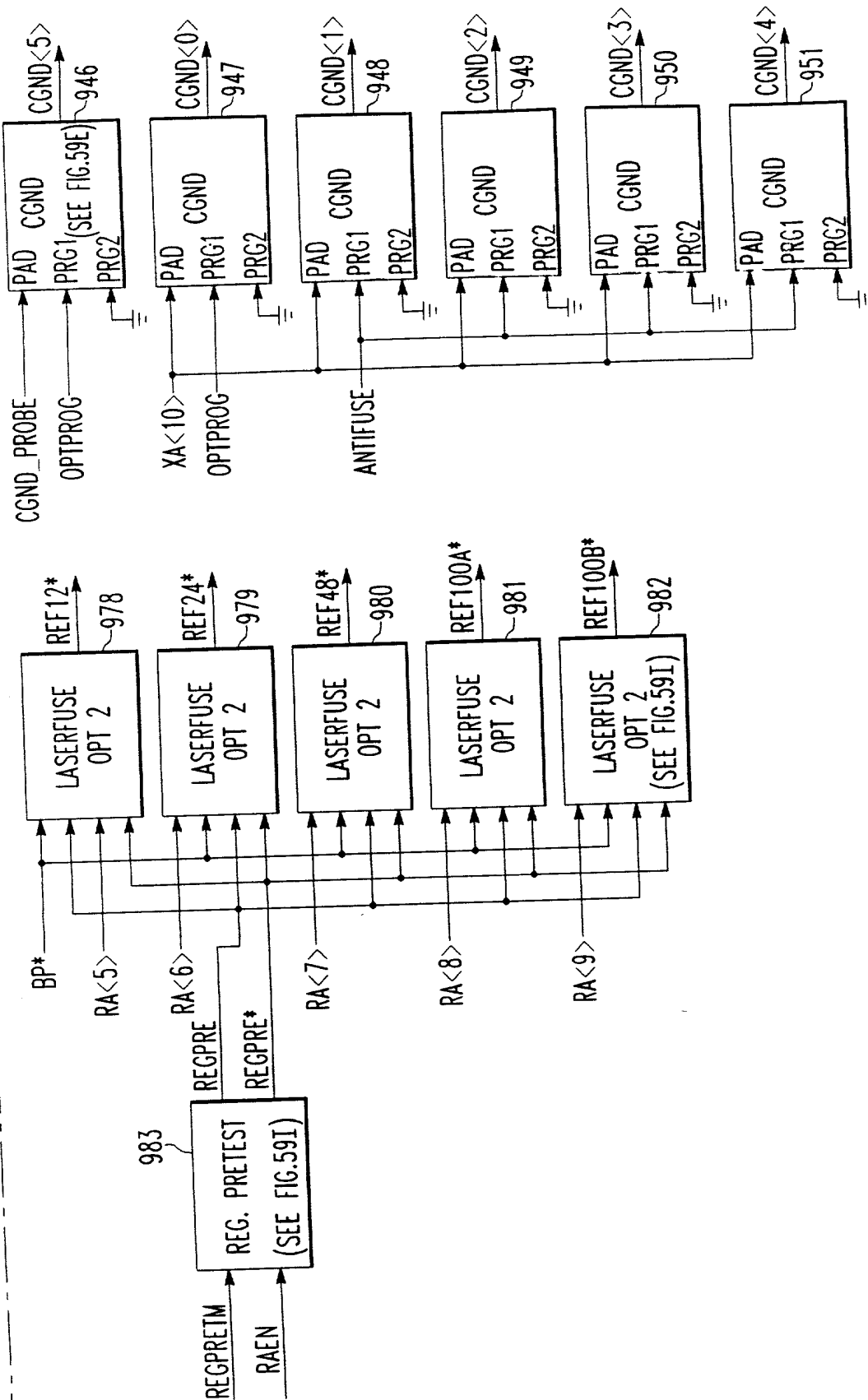


FIG. 58B

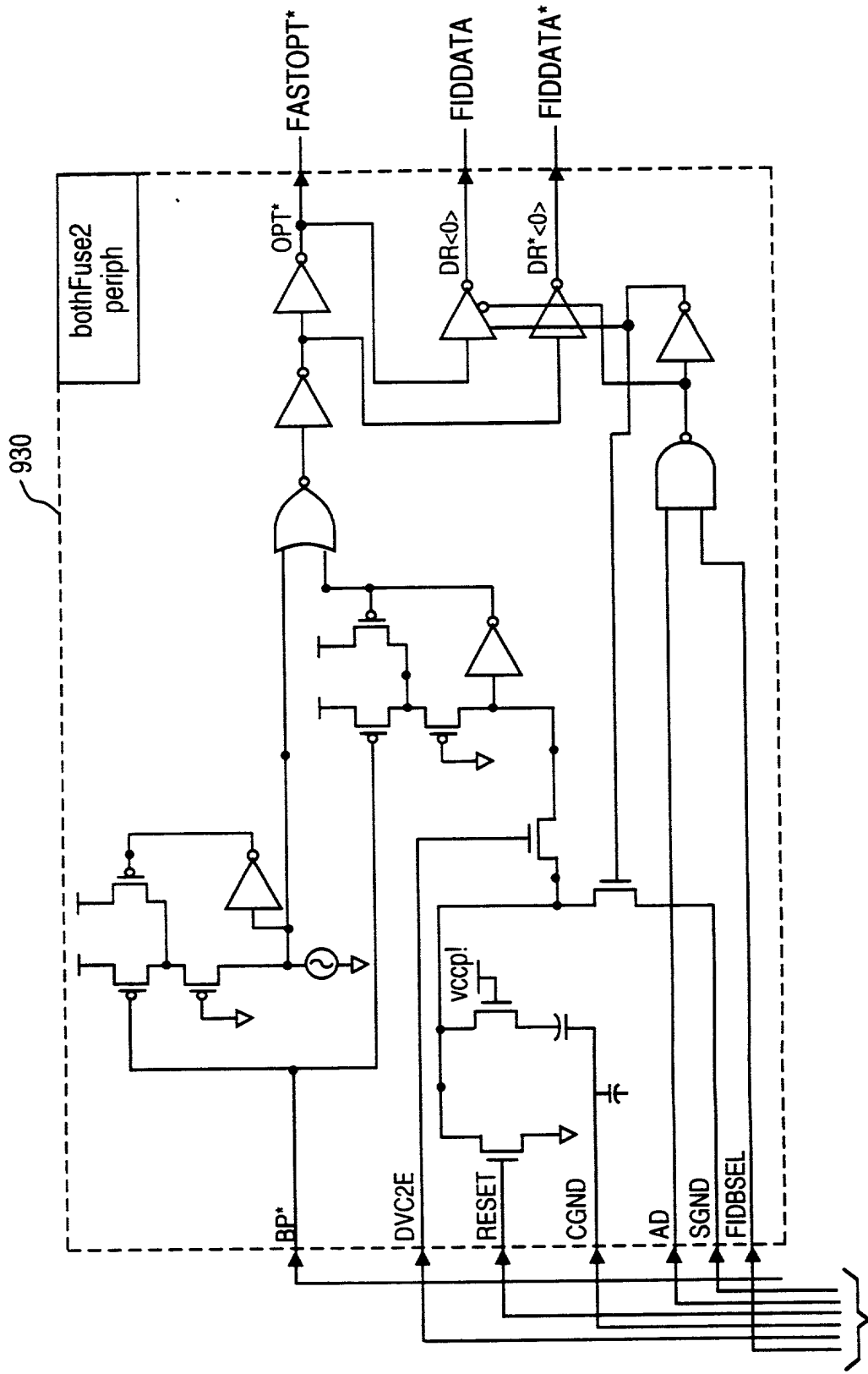


FIG. 59B

FIG. 59A

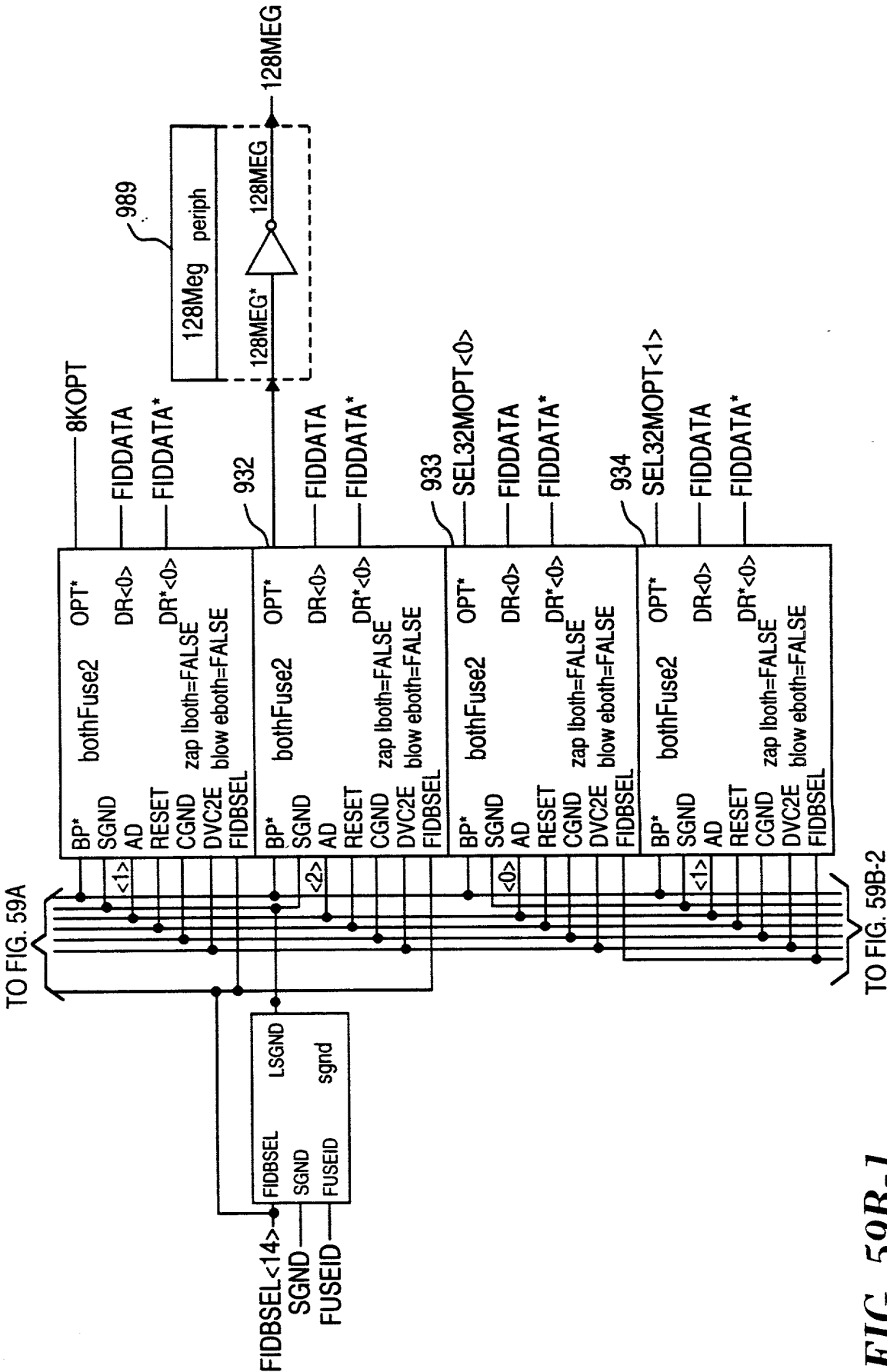


FIG. 59B-1

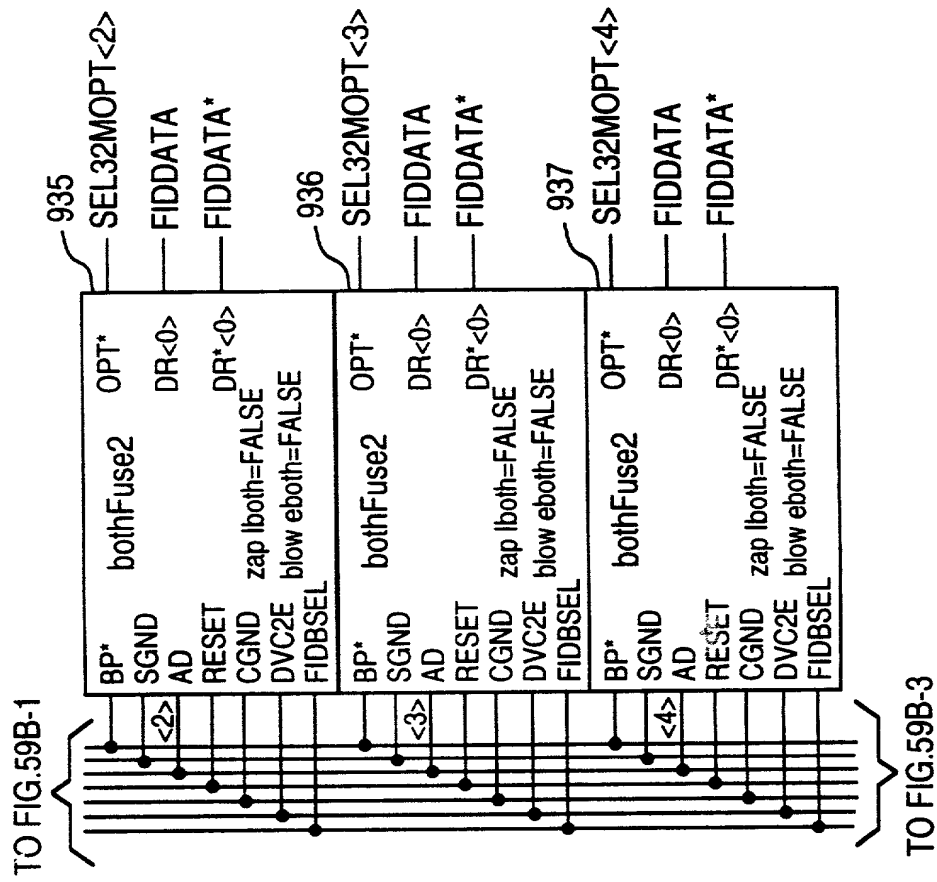


FIG. 59B-2

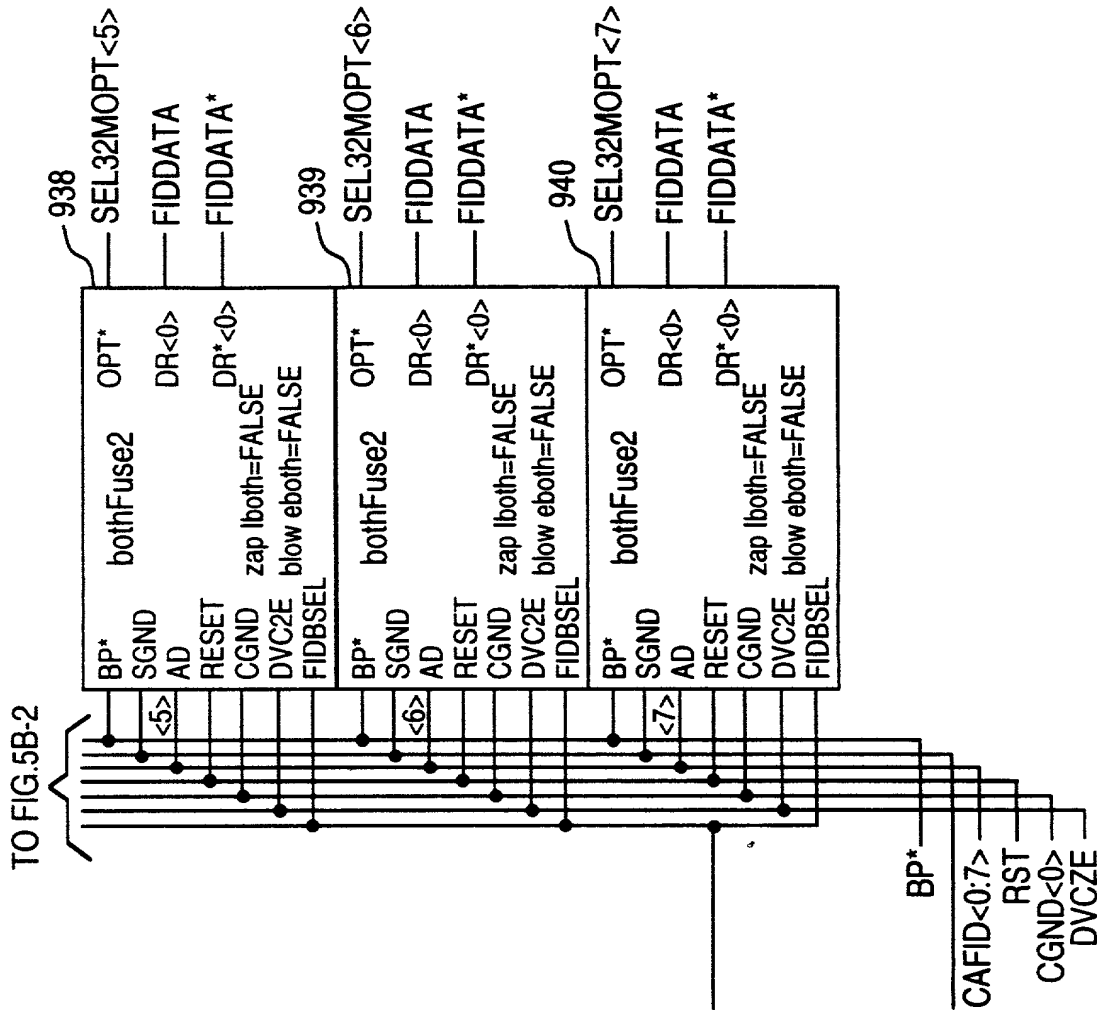


FIG. 59B-3

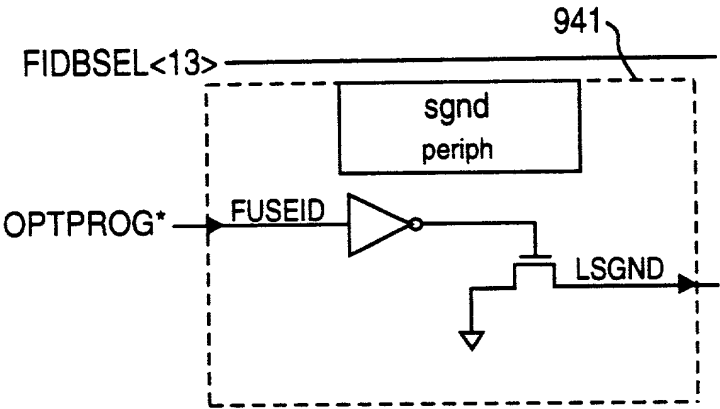


FIG. 59C





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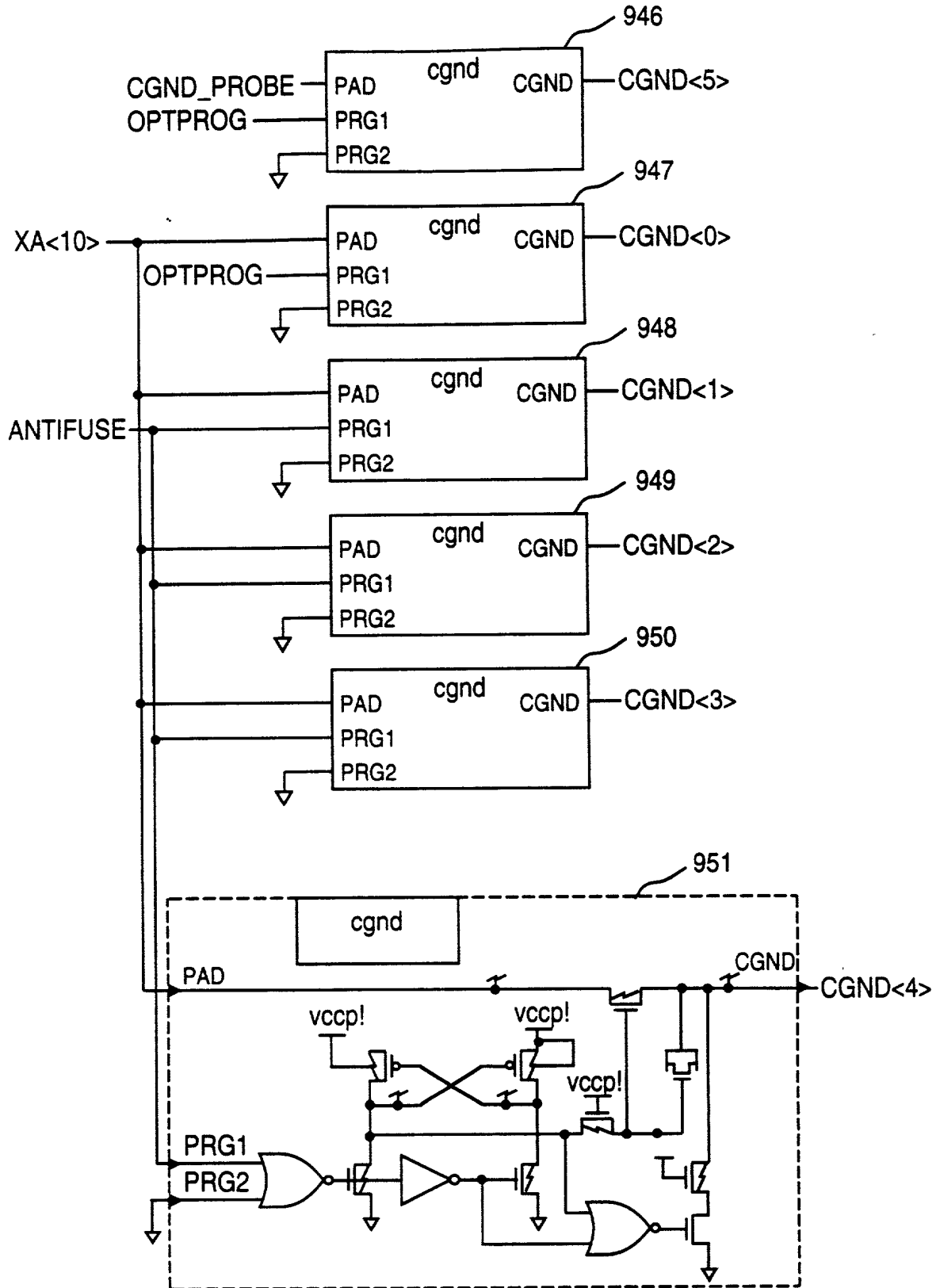


FIG. 59E

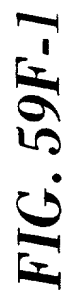


FIG. 59F-2

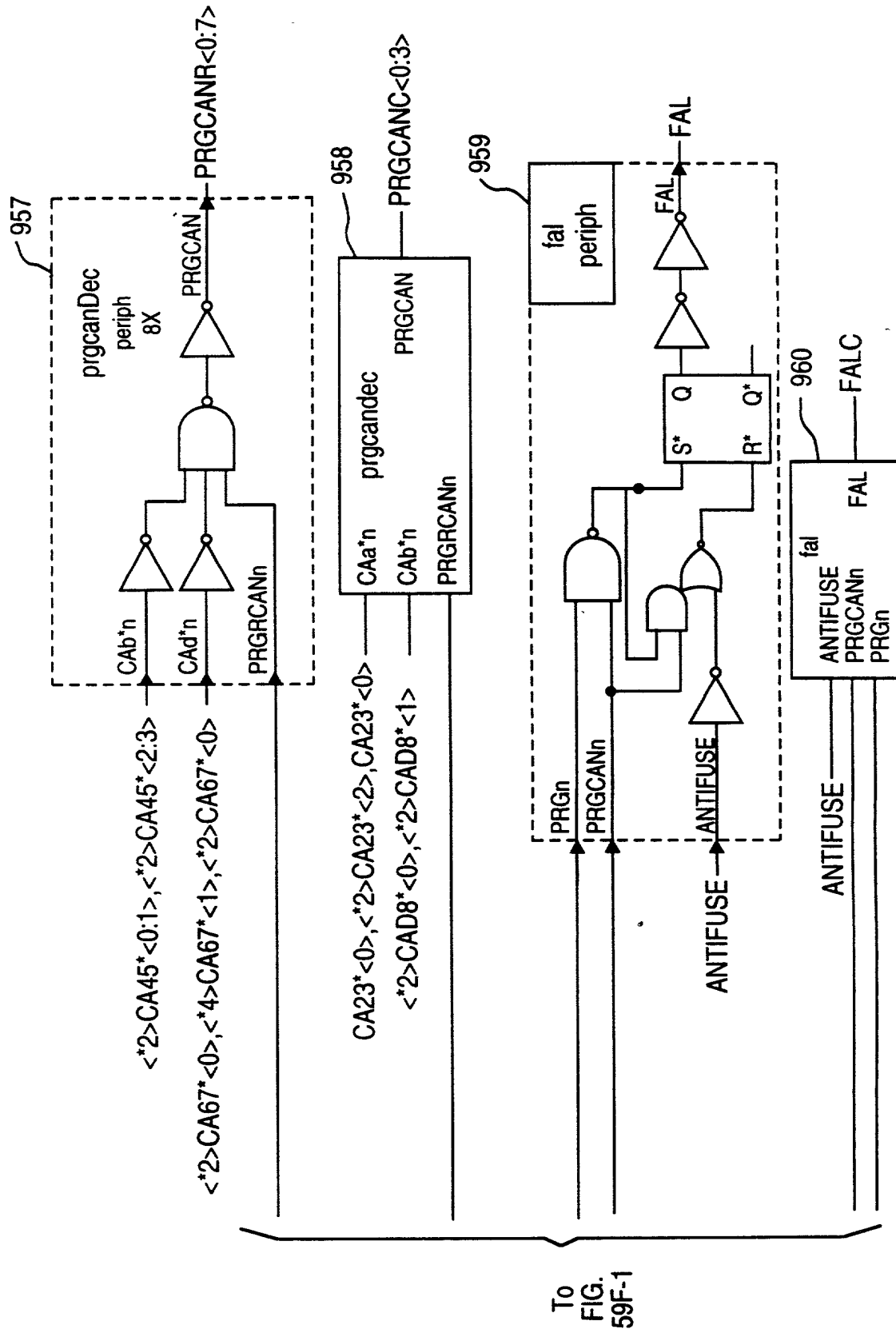


FIG. 59F-2

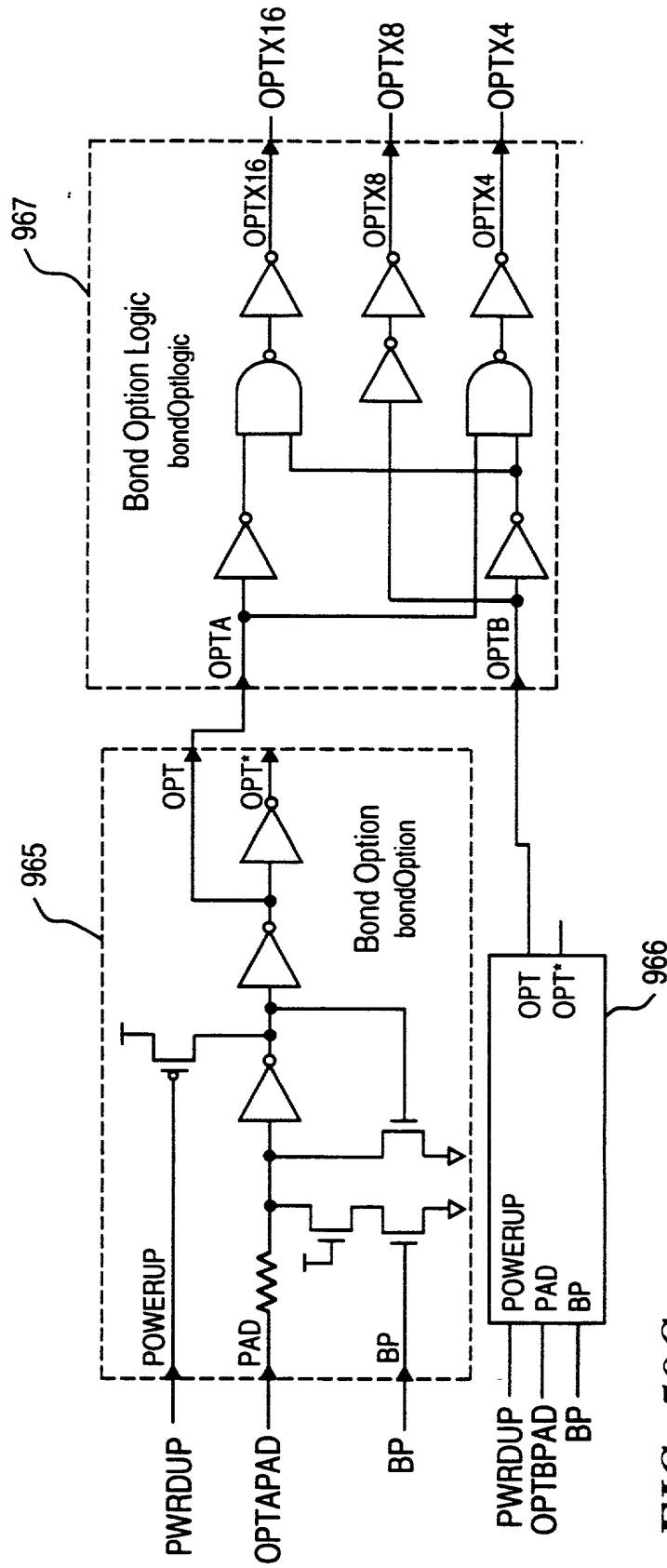
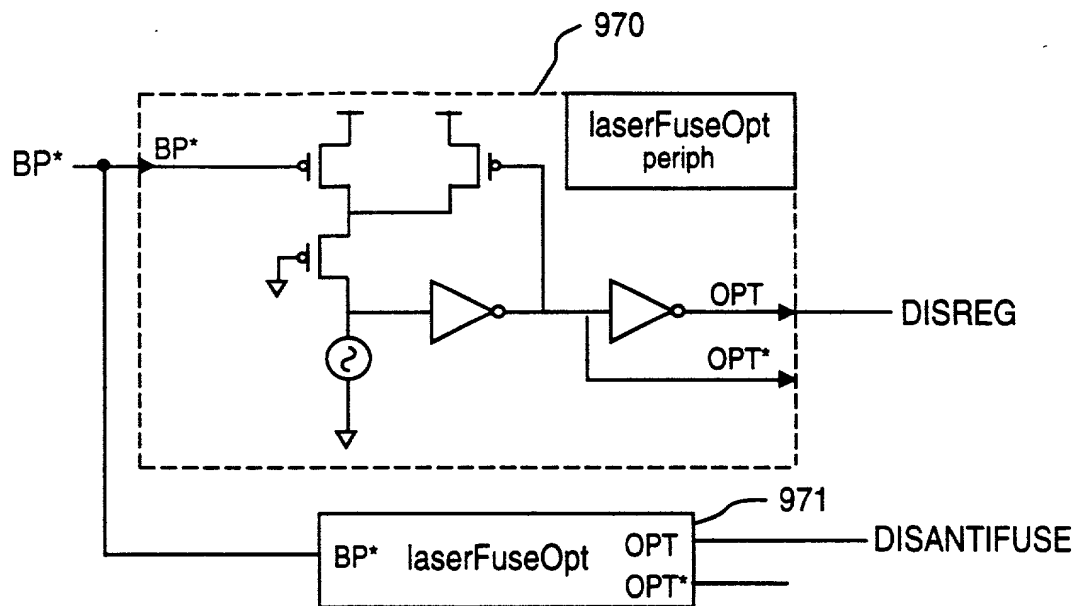
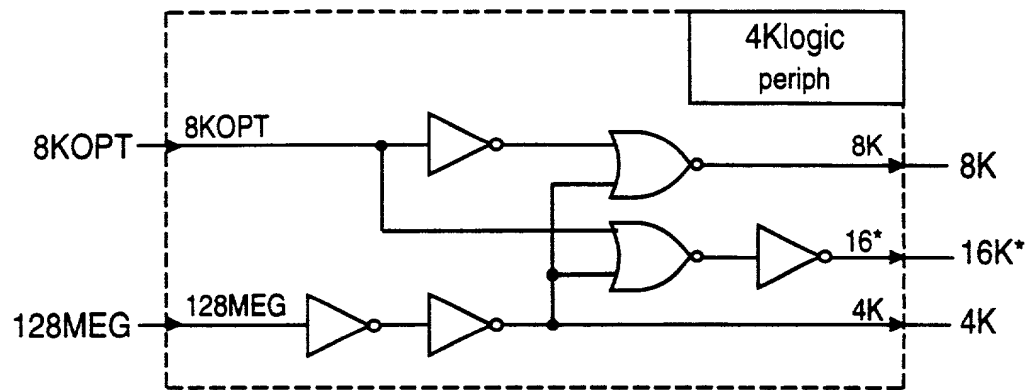


FIG. 59C

**FIG. 59H**



**FIG. 59J**



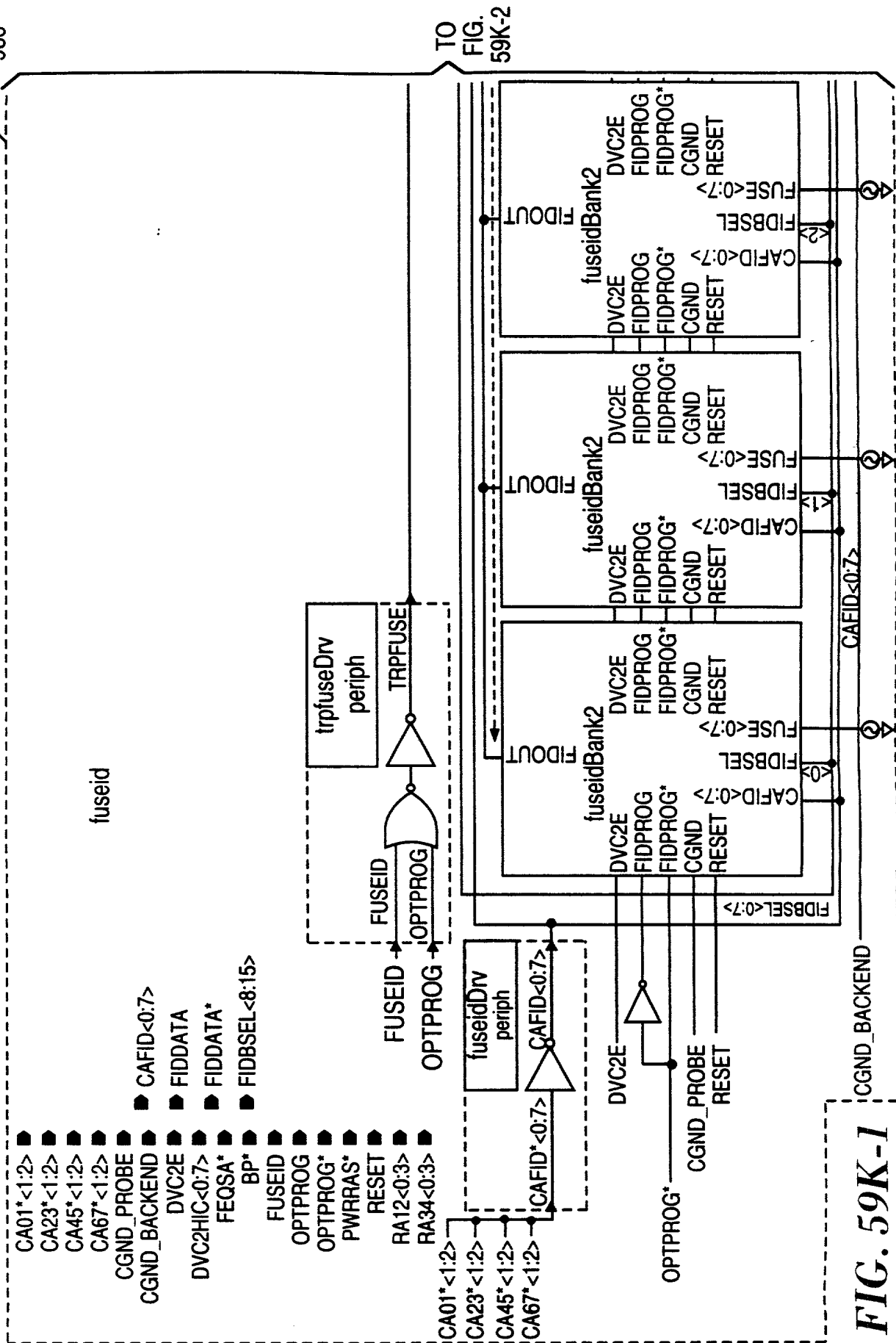


FIG. 59K-1

TO  
FIG.  
59K-2

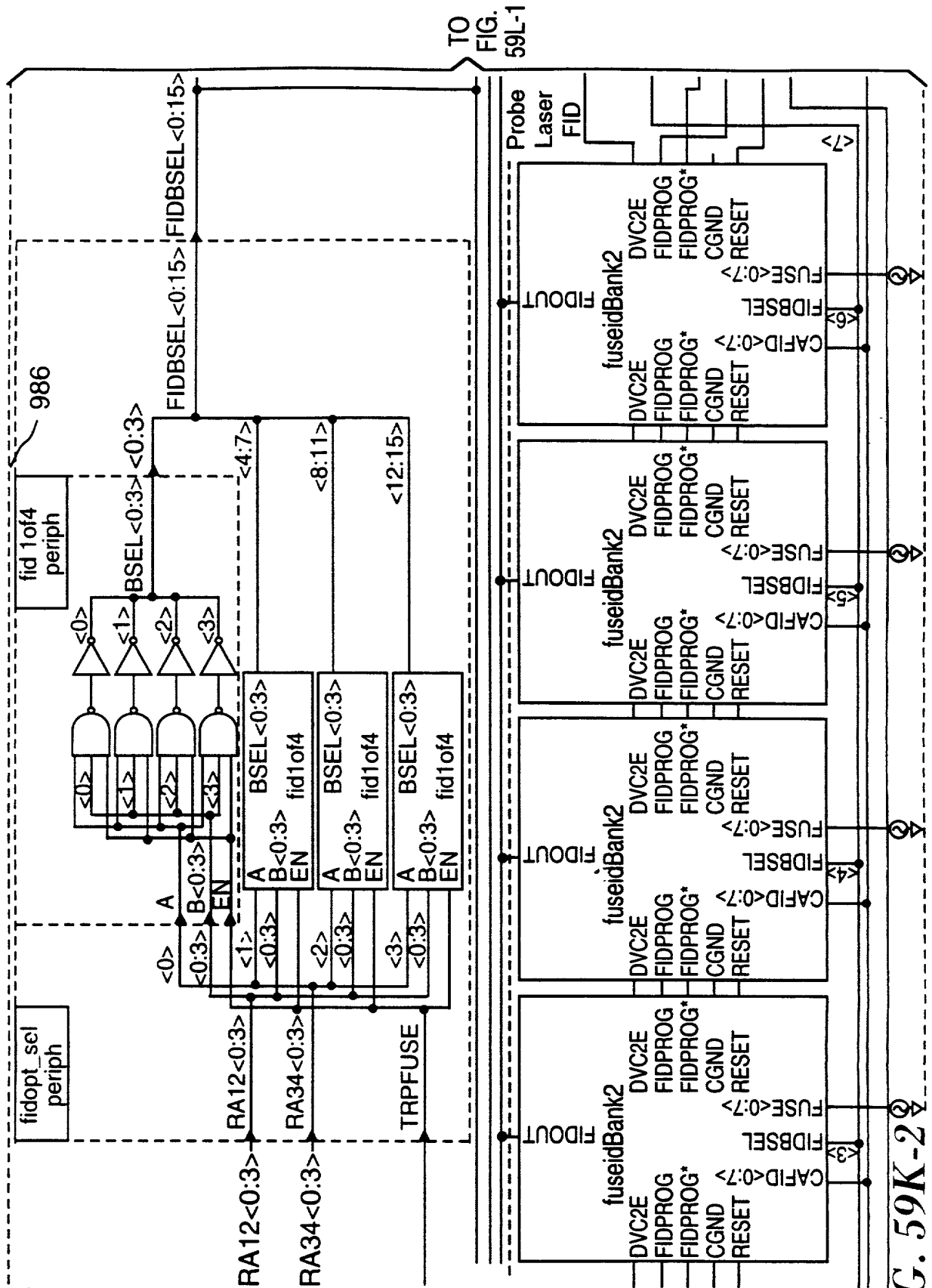


FIG. 59K-2

TO  
FIG.  
59L-1

TO  
FIG.  
59L-2

TO  
FIG.  
59K-1

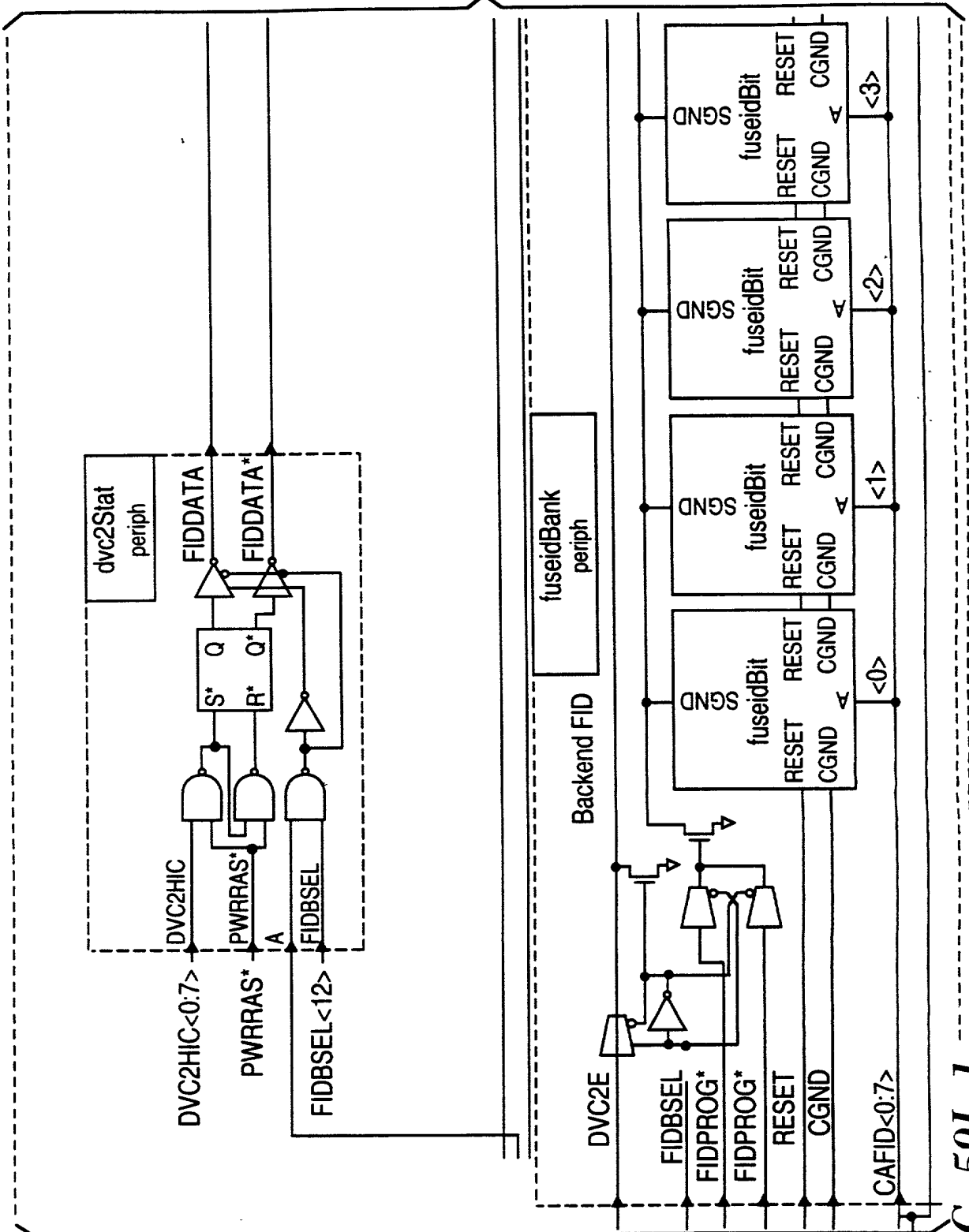
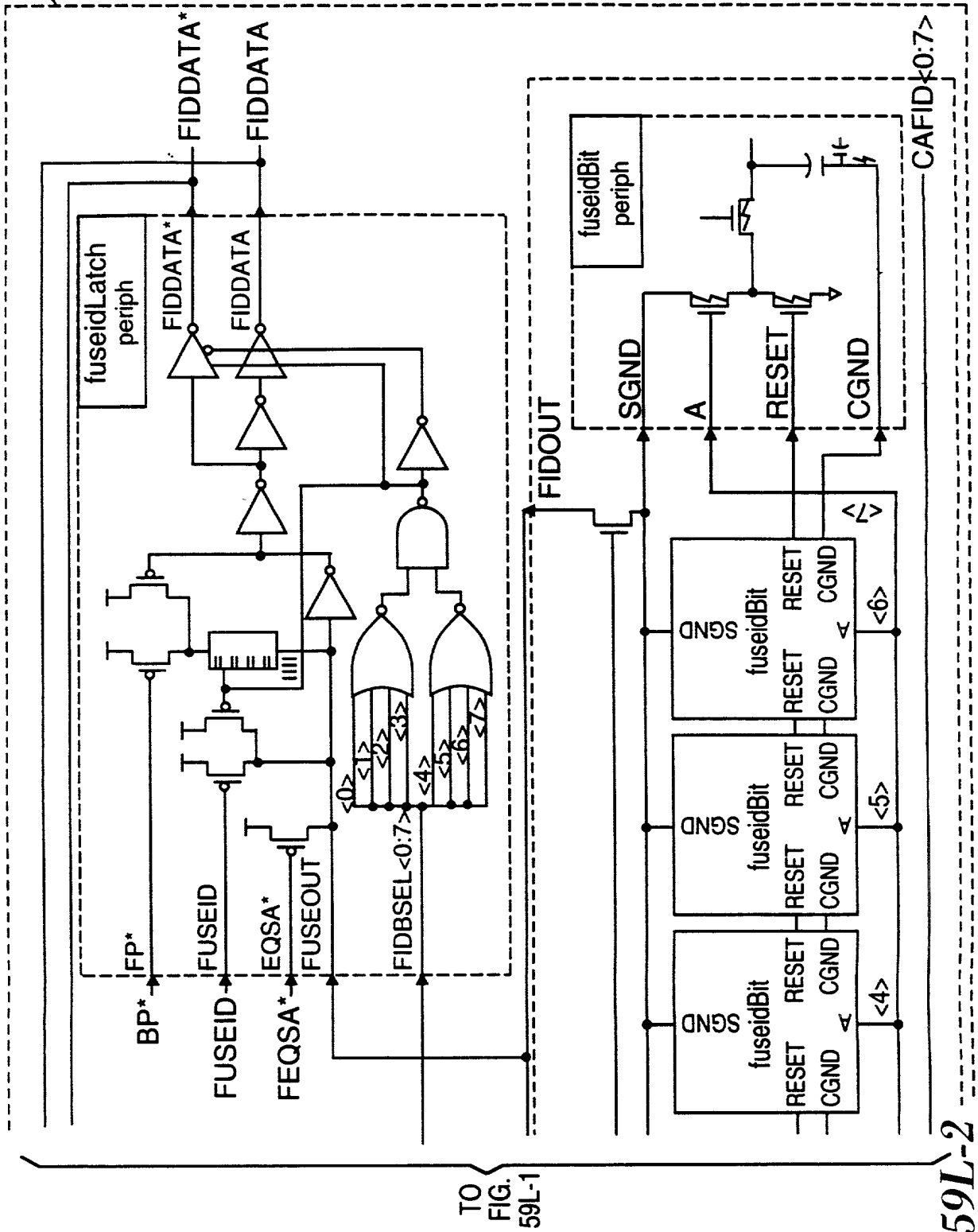


FIG. 59L-1

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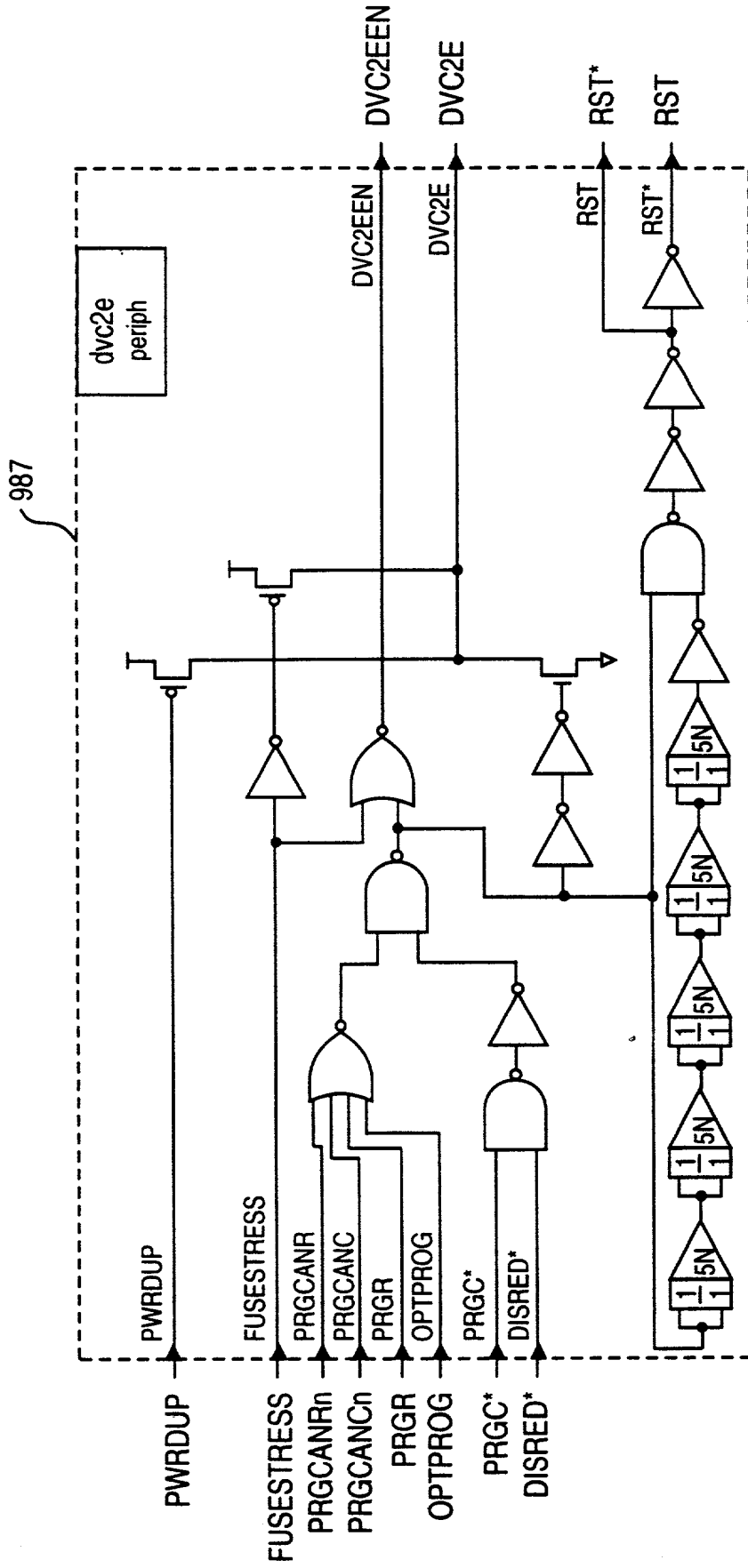
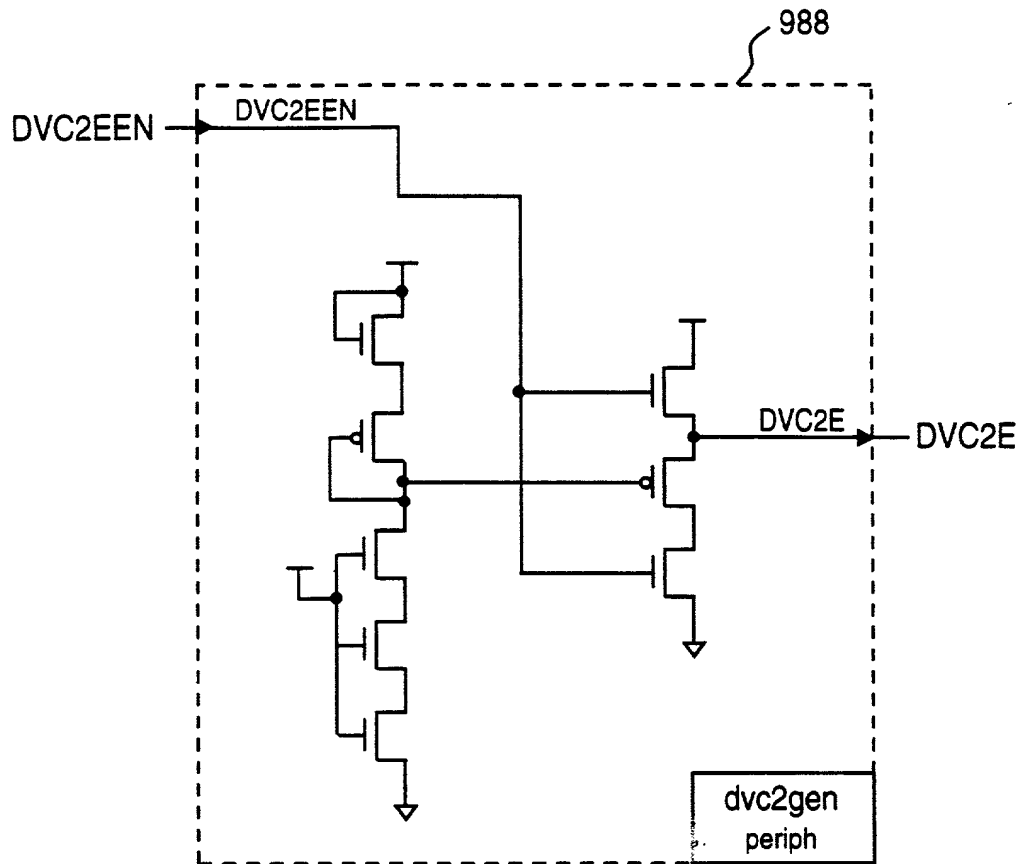
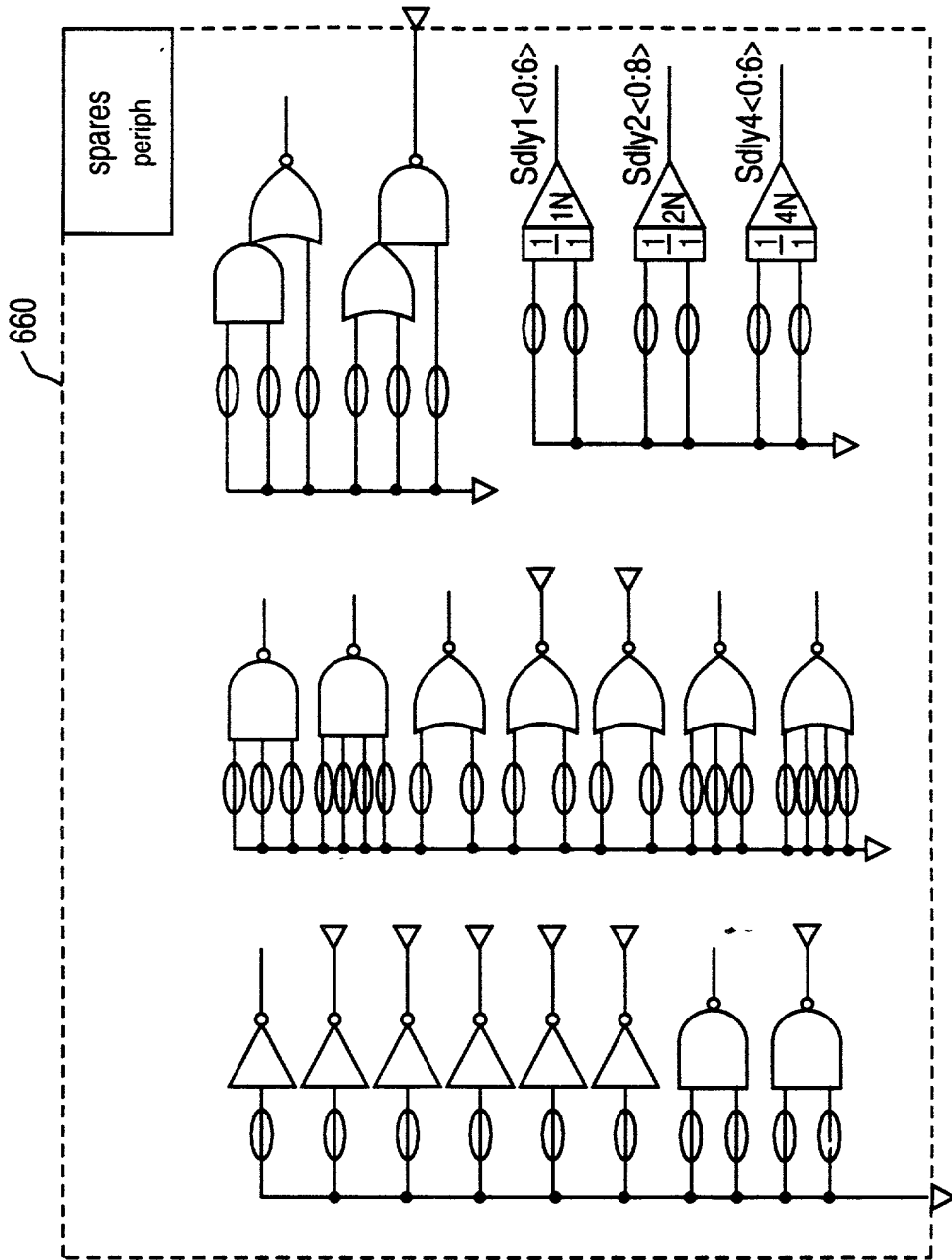


FIG. 59M

**FIG. 59N**



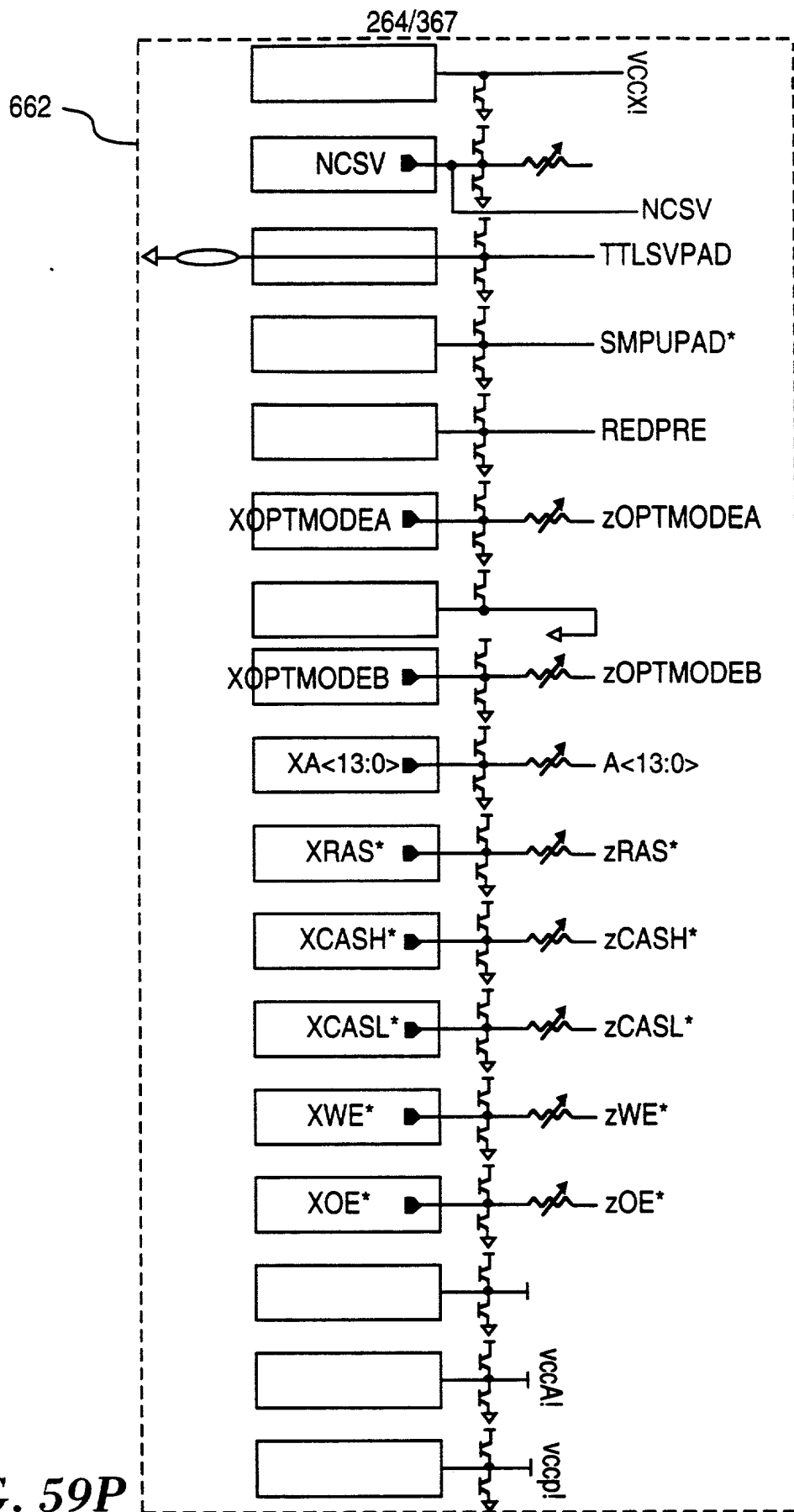


FIG. 59P



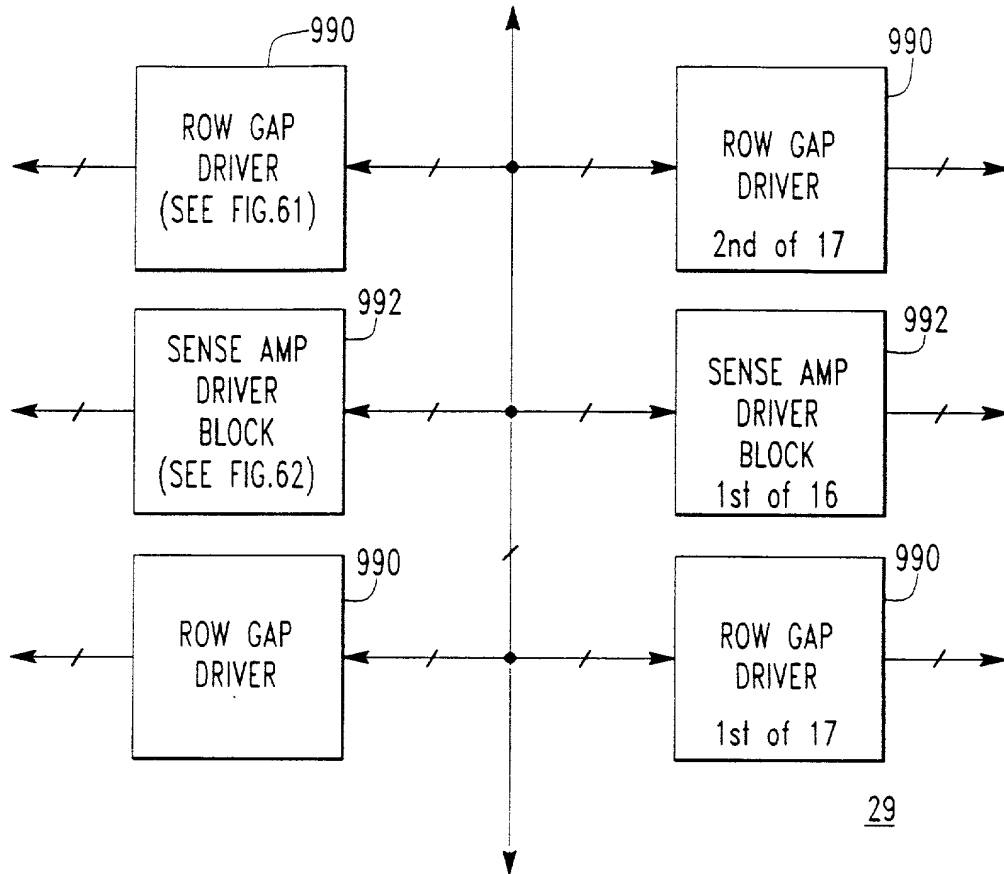


FIG. 60

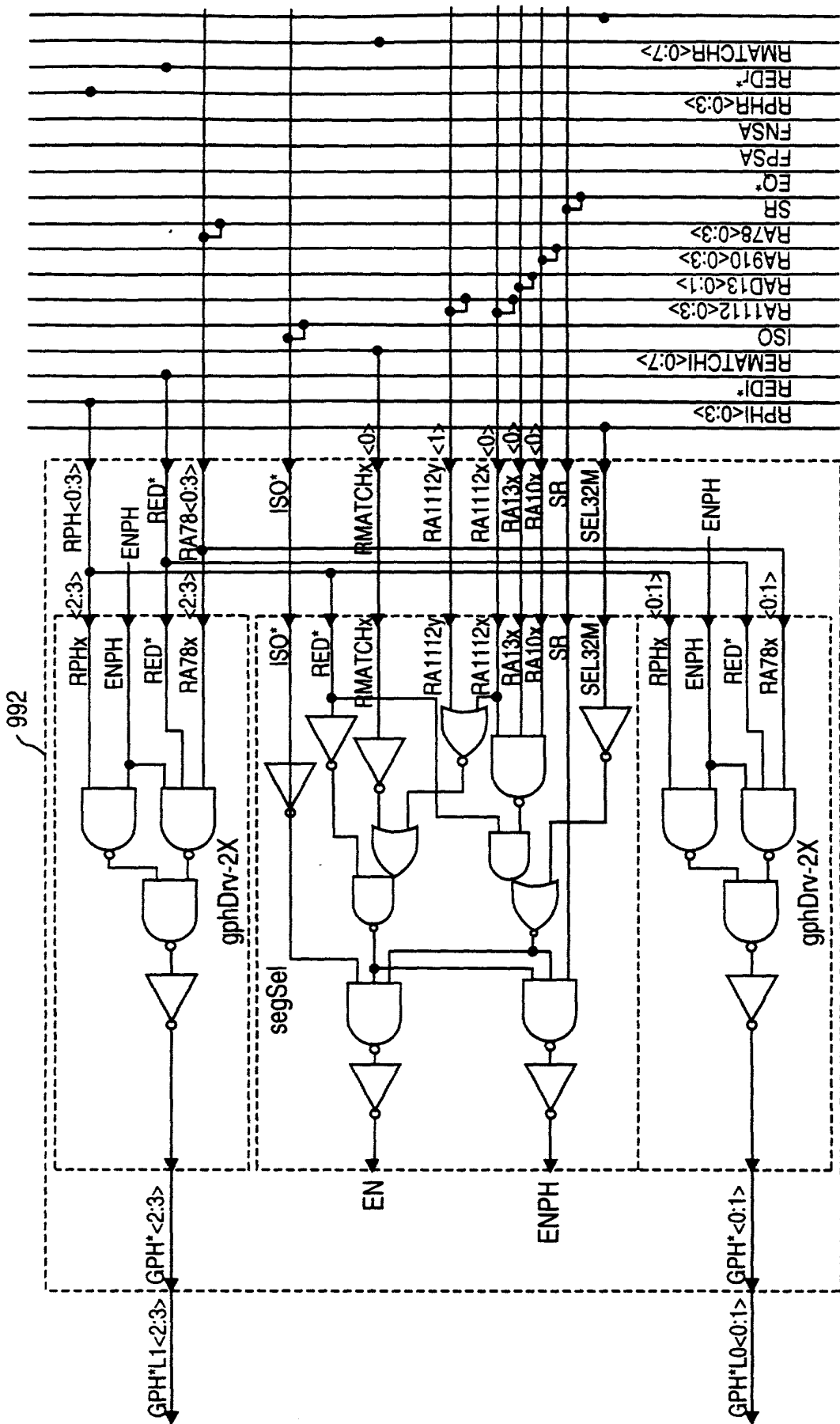
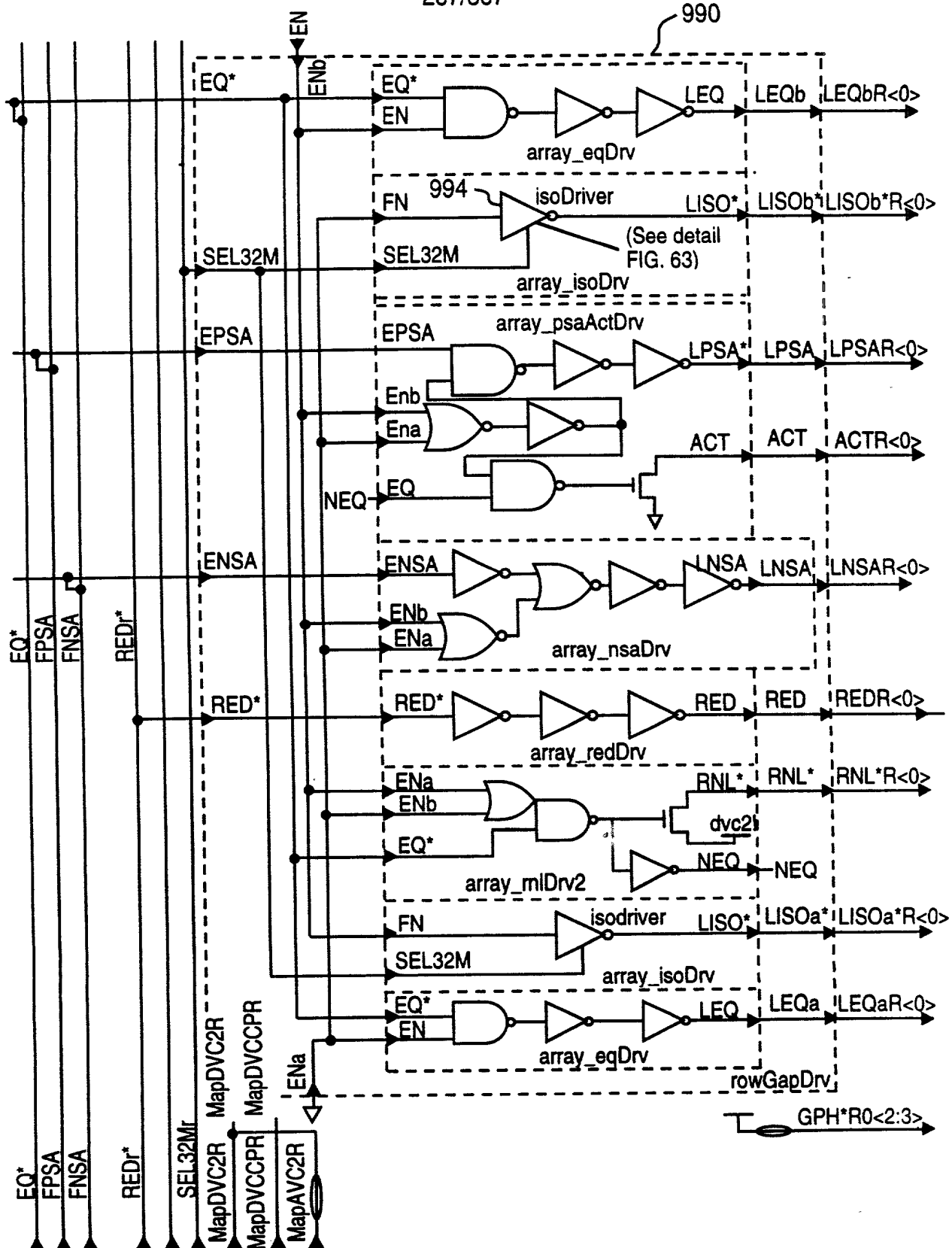


FIG. 61



**FIG. 62**

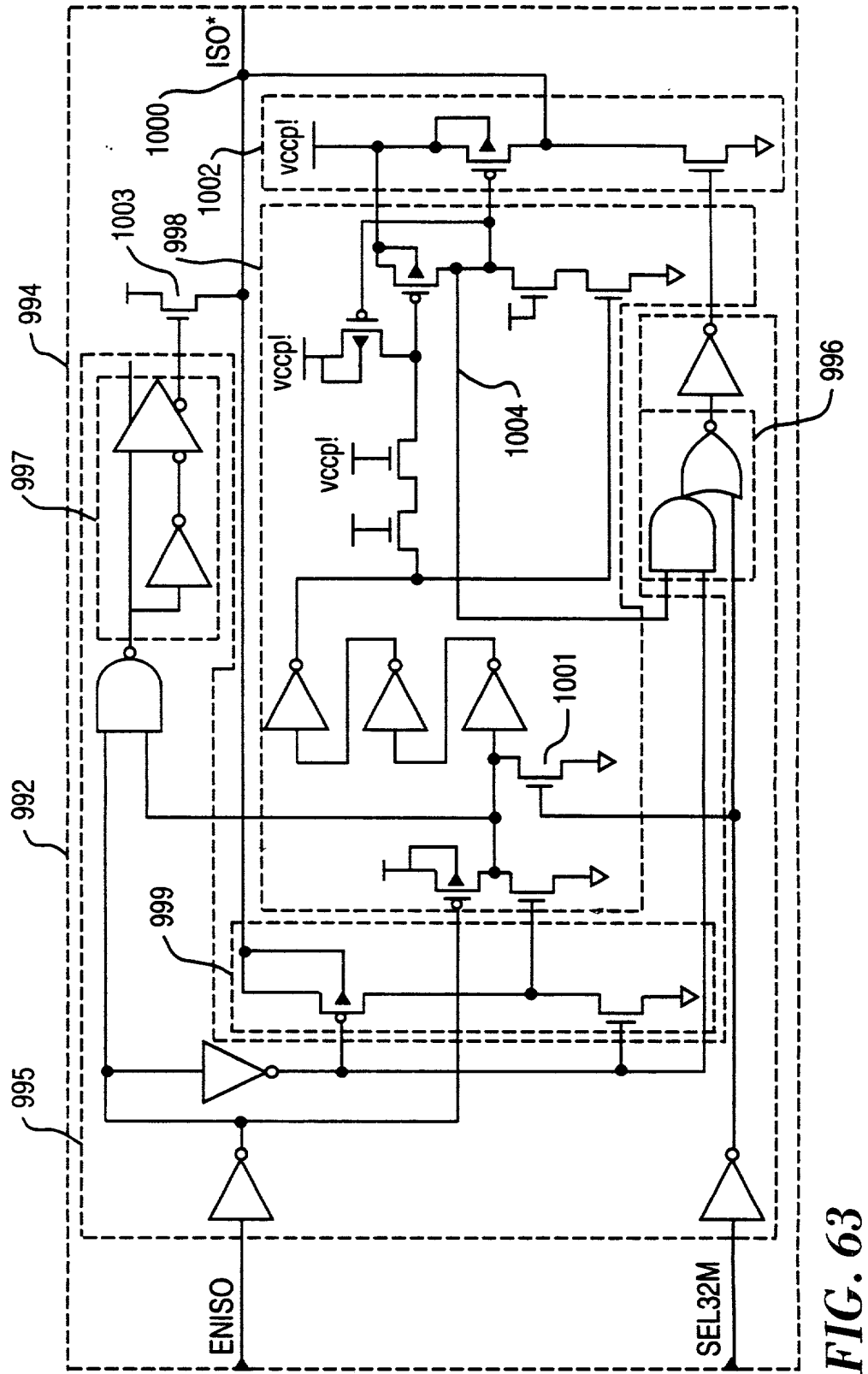


FIG. 63

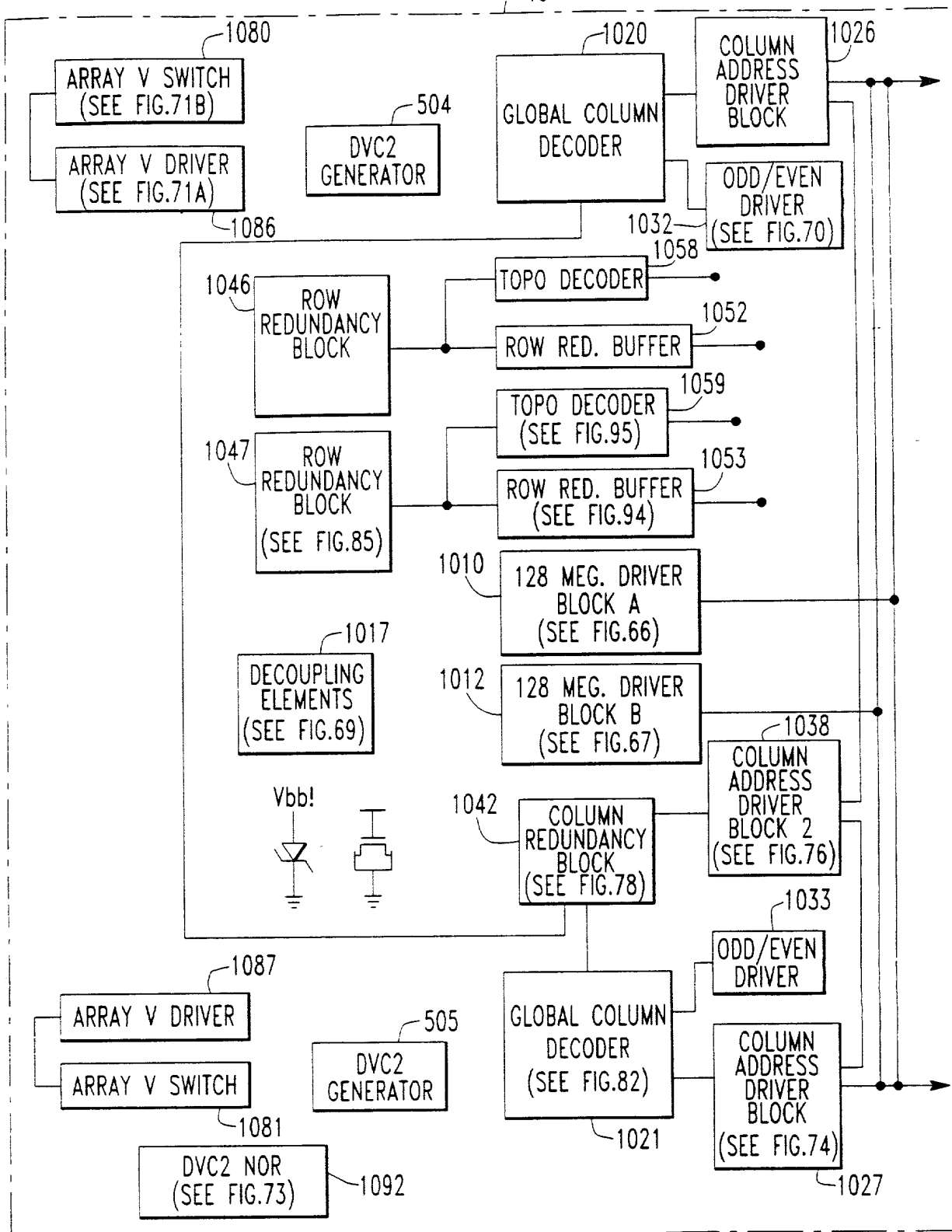
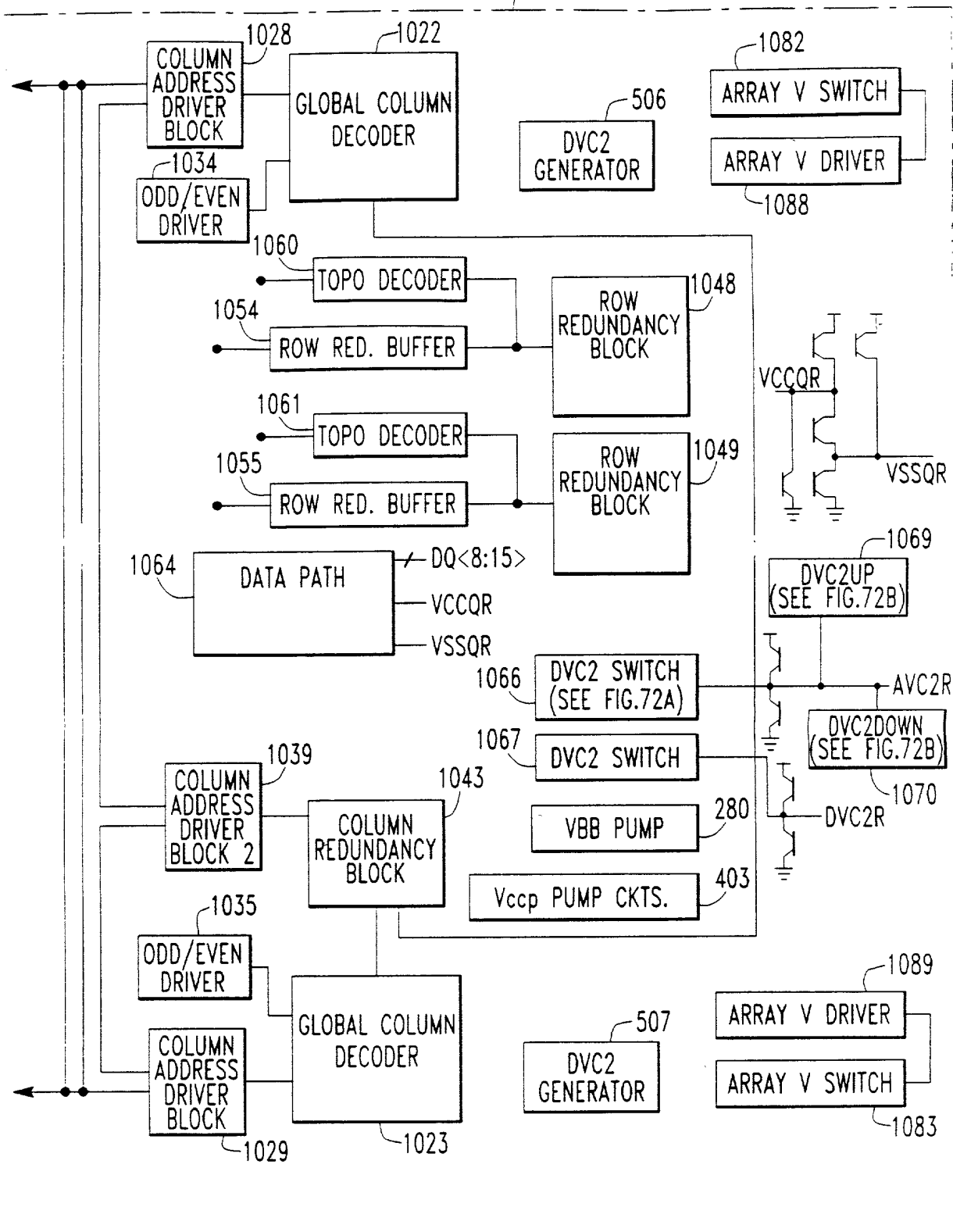


FIG. 64A



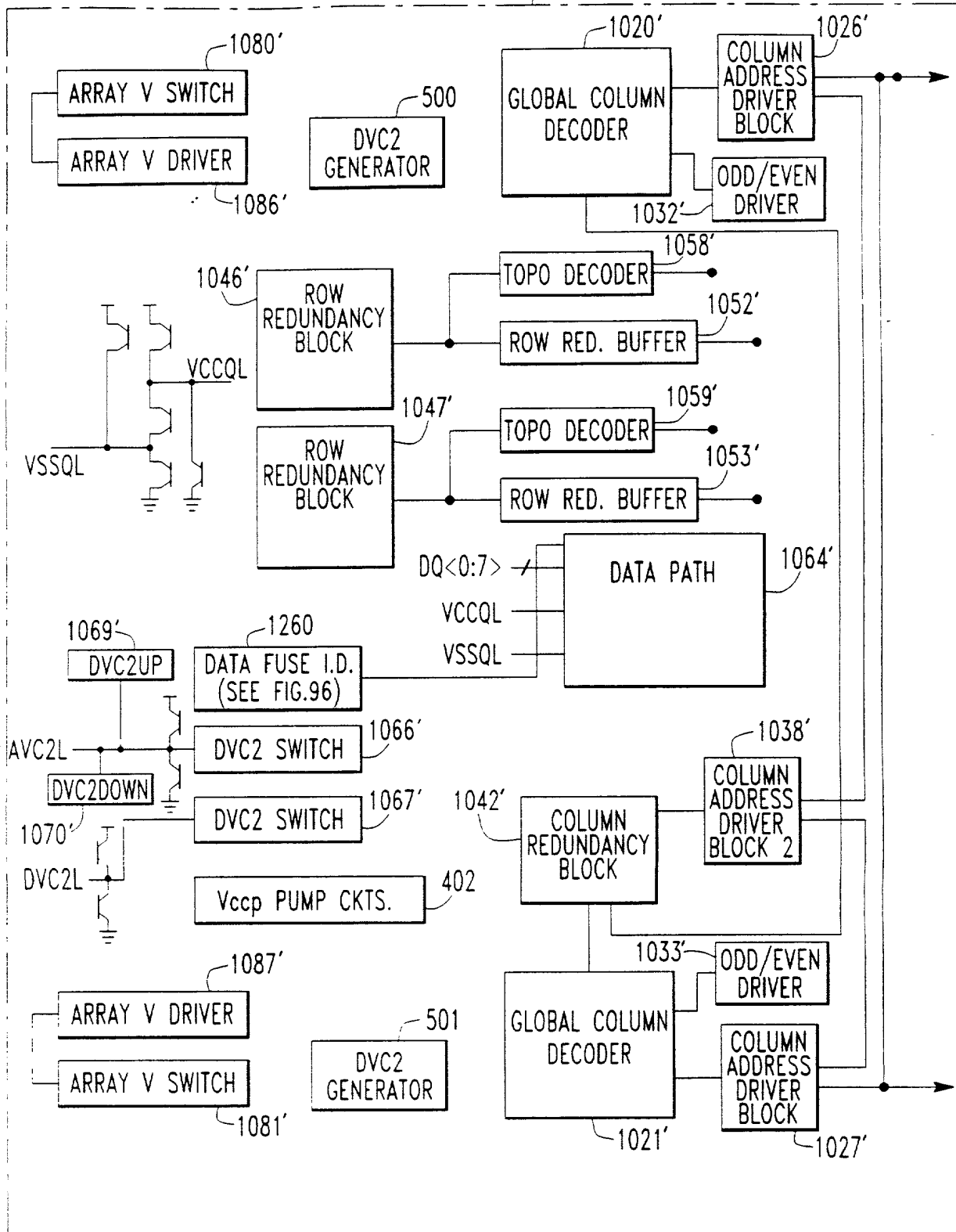


FIG. 65A

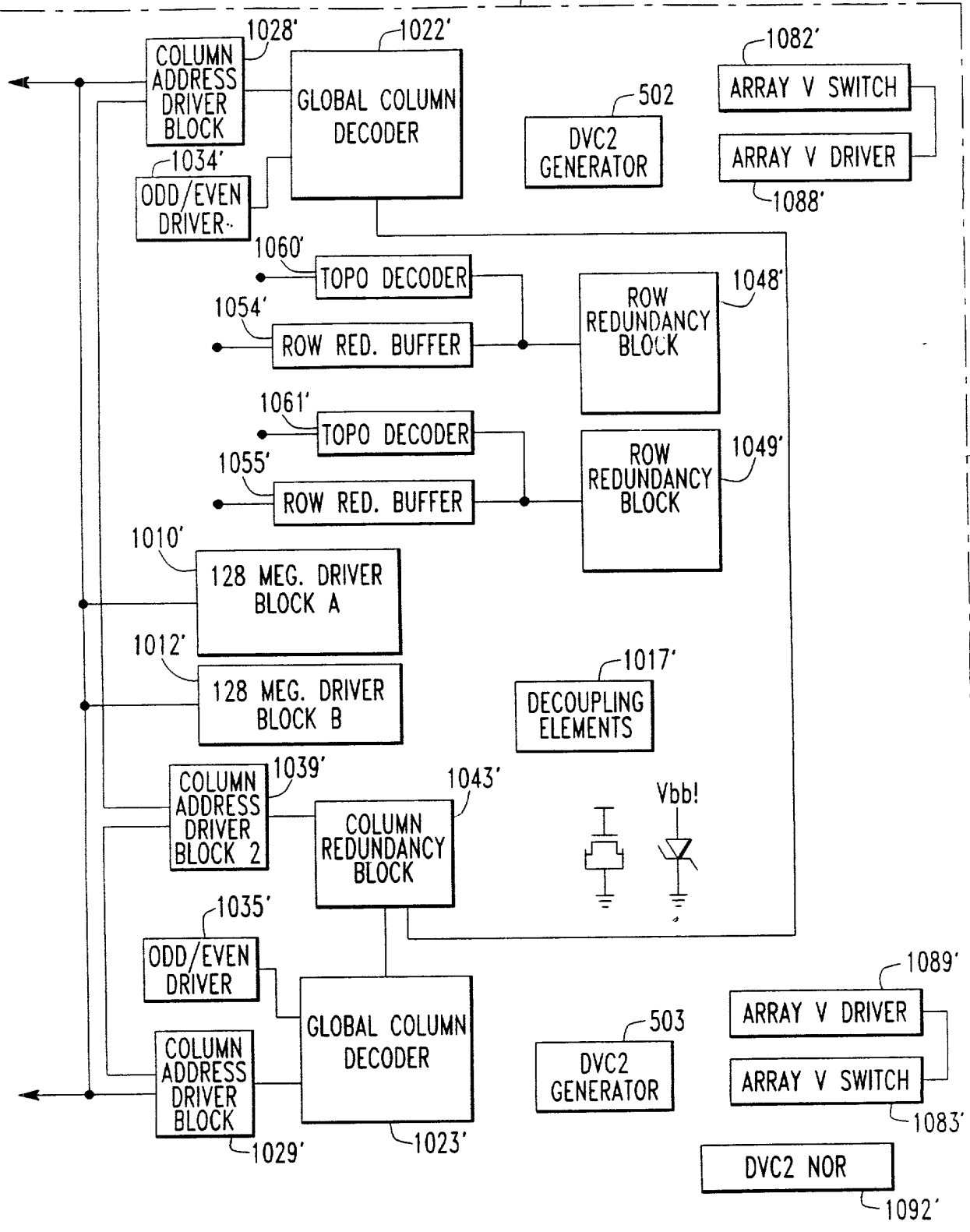


FIG. 65B



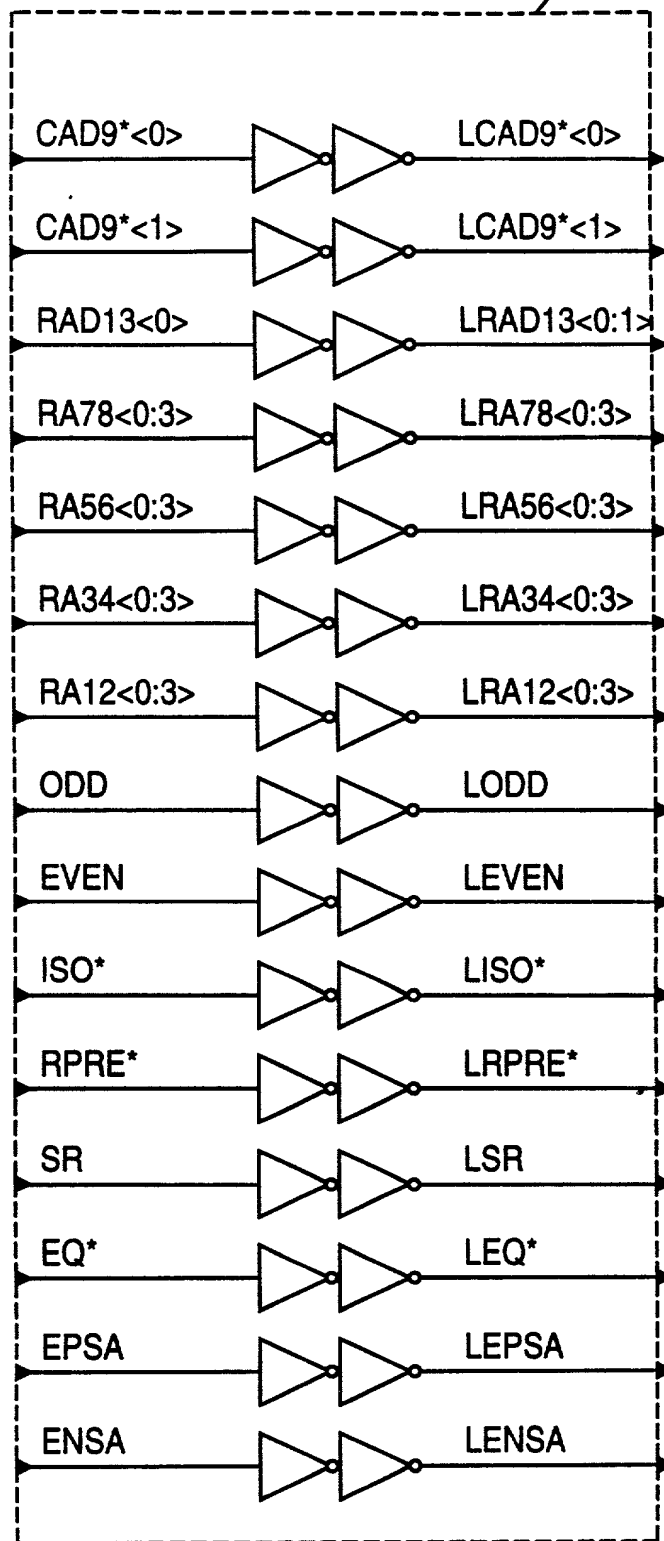


FIG. 66

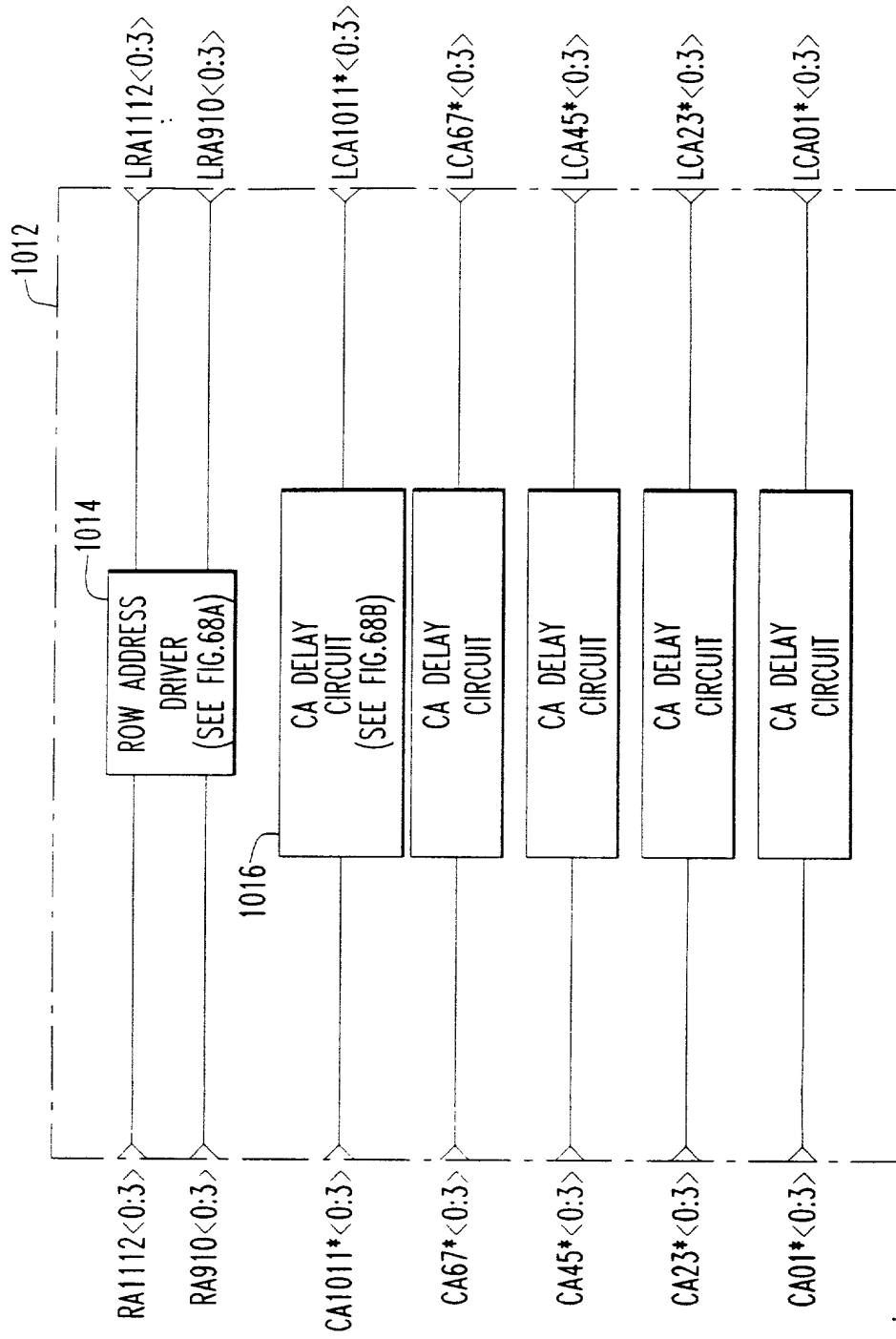
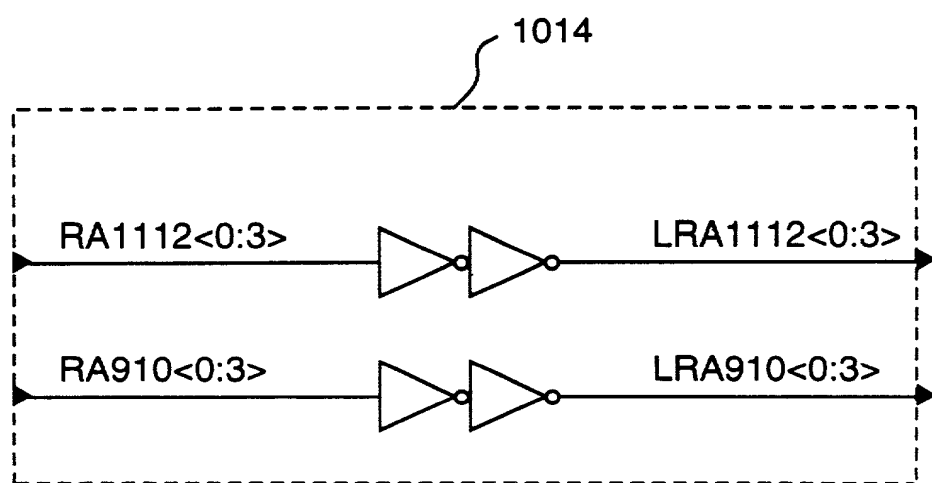
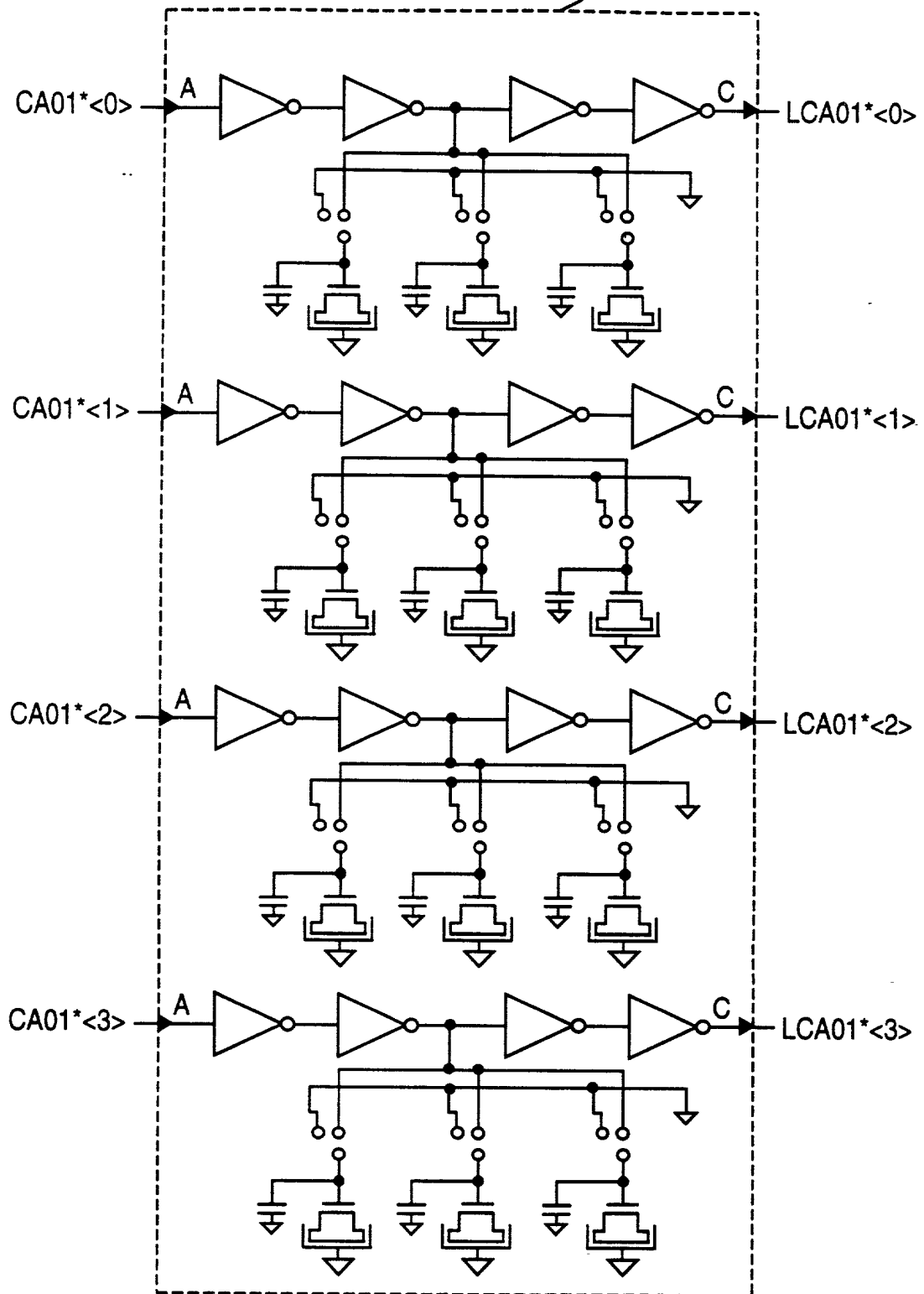
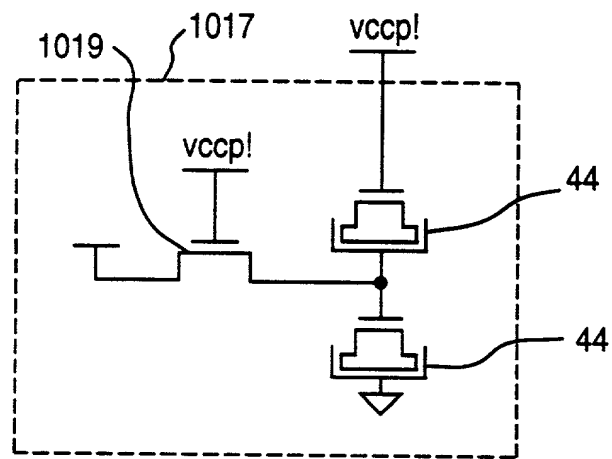
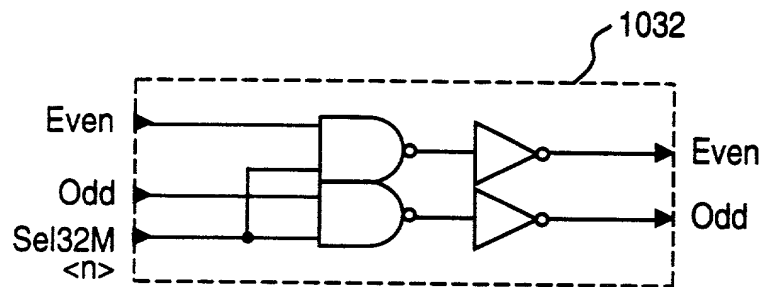


FIG. 67

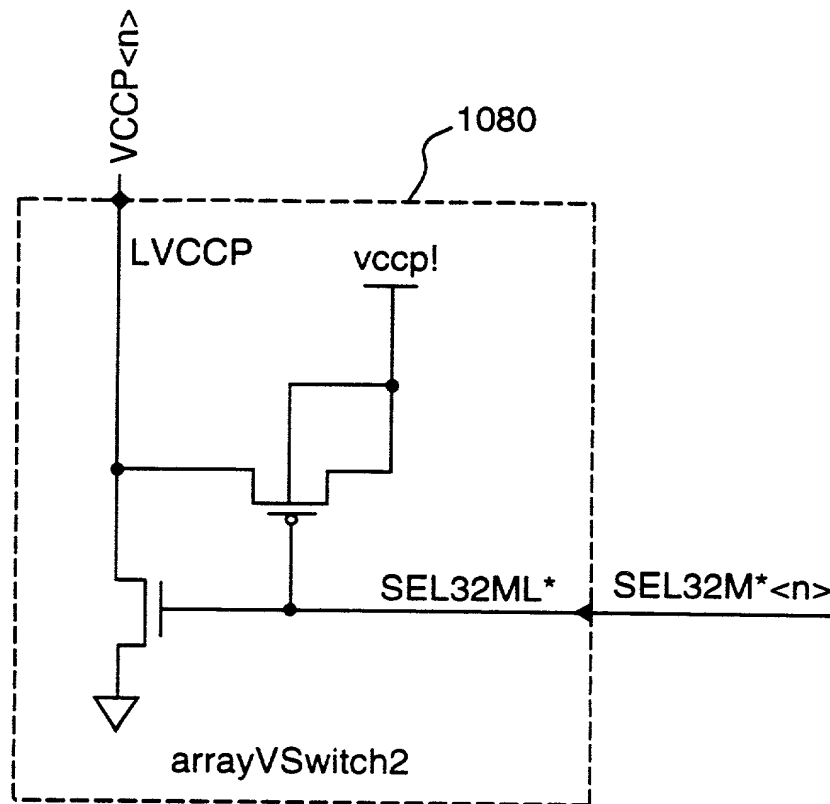
**FIG. 68A**

**FIG. 68B**

**FIG. 69**

**FIG. 70**



**FIG. 71B**



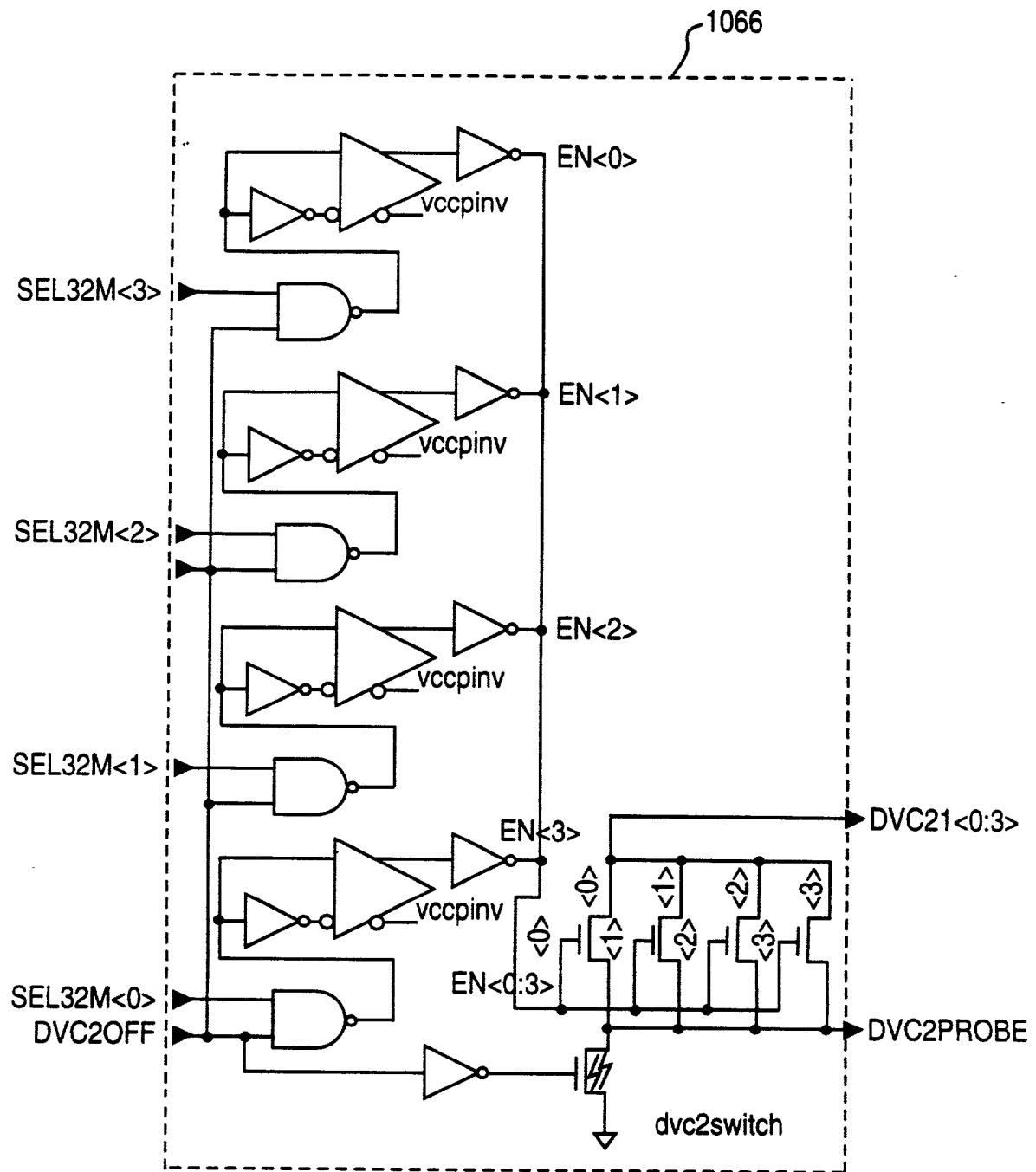
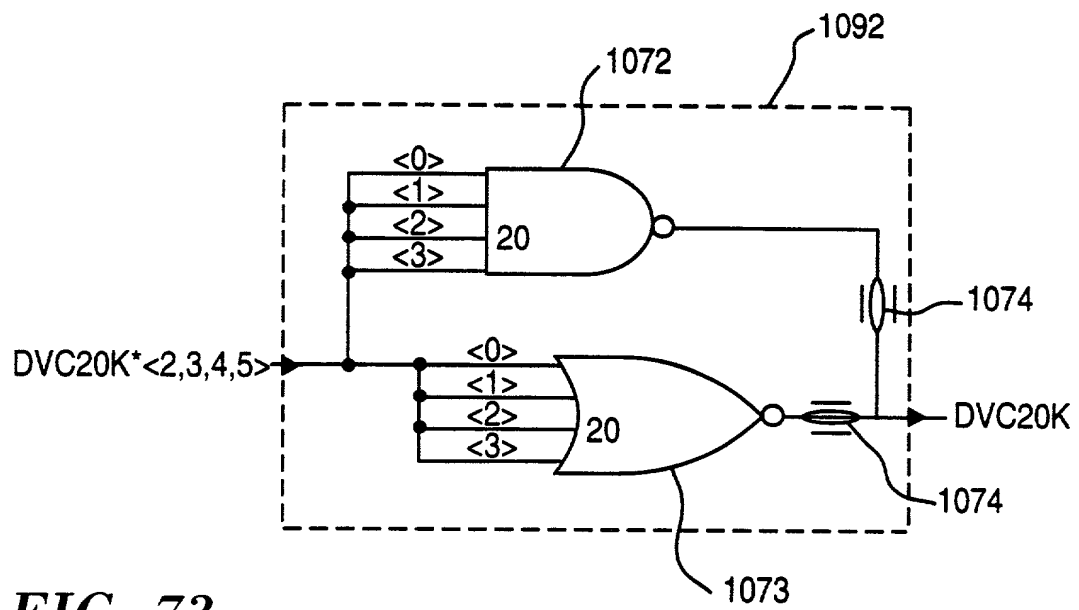
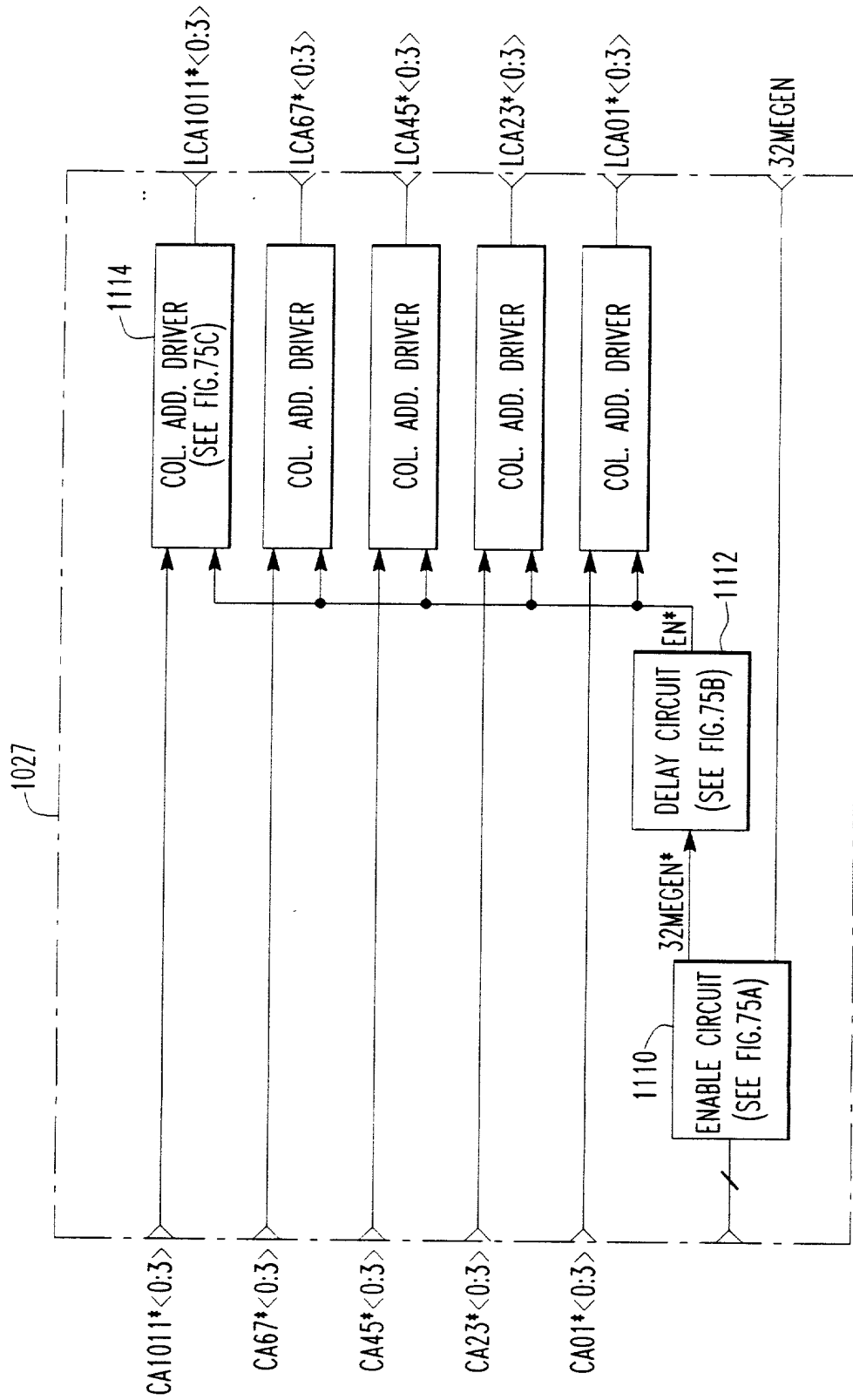


FIG. 72A



**FIG. 73**



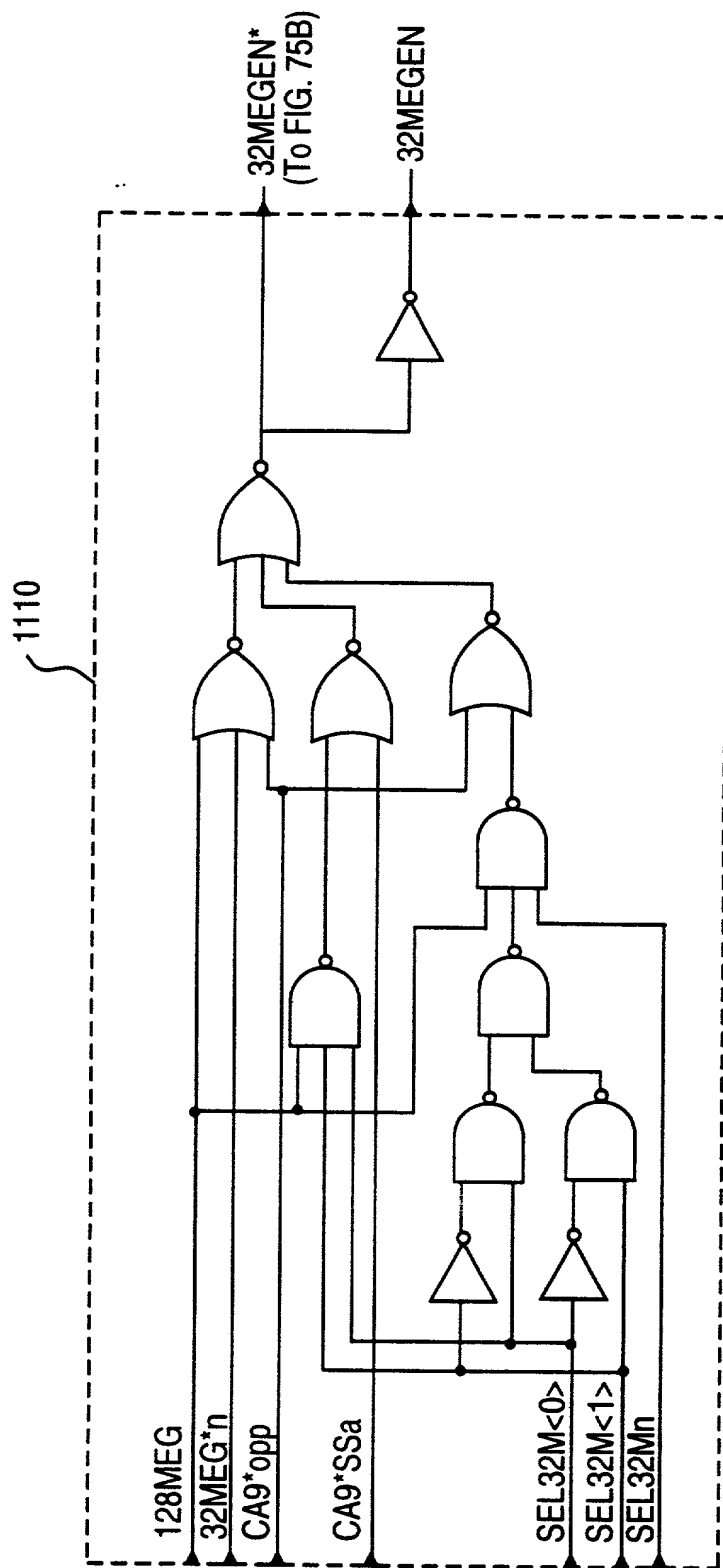


FIG. 75A

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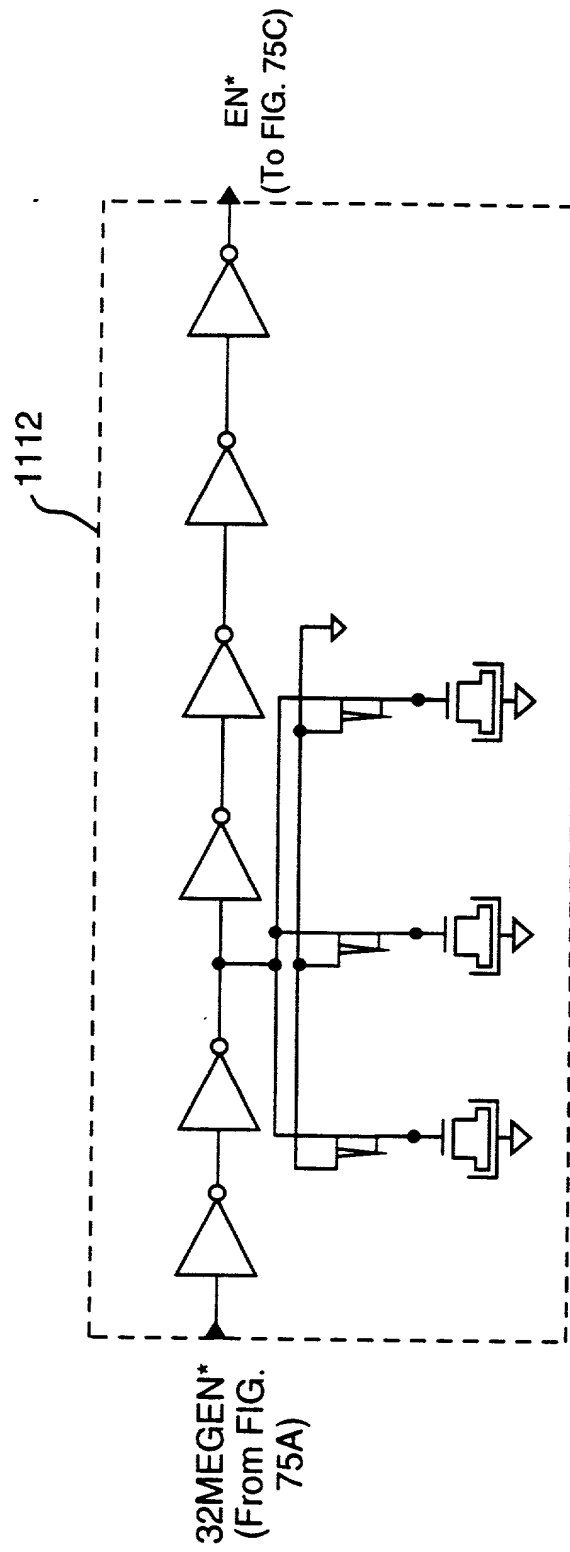


FIG. 75B

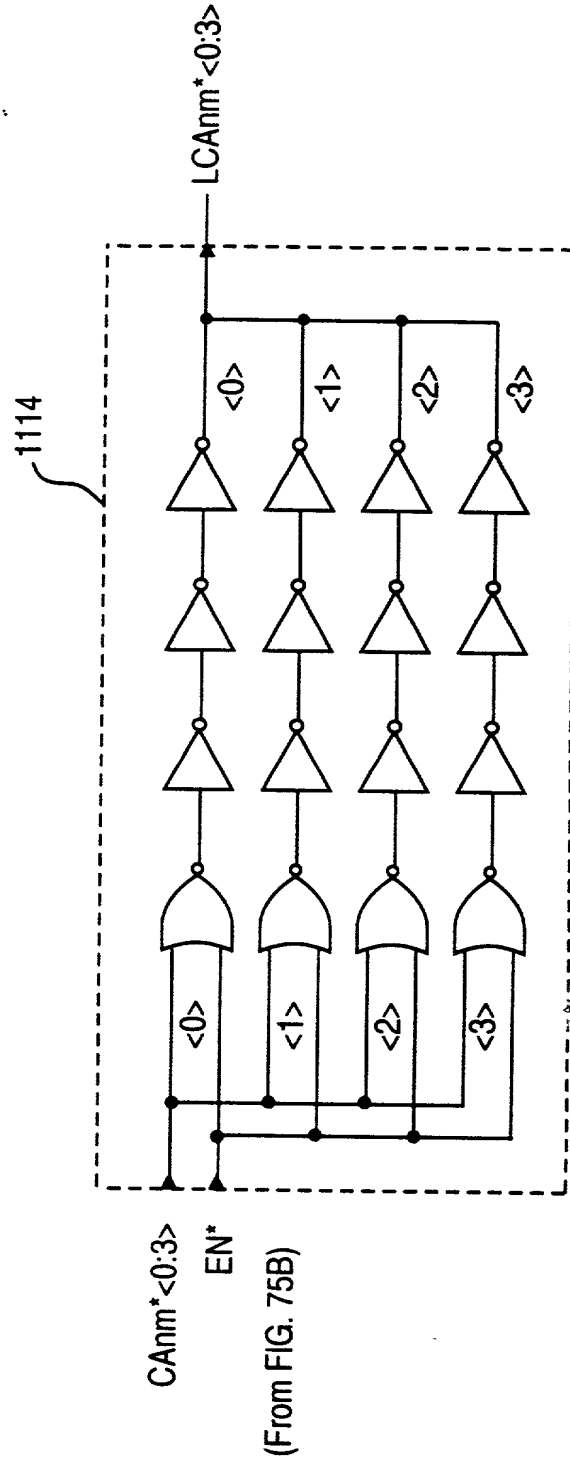


FIG. 75C

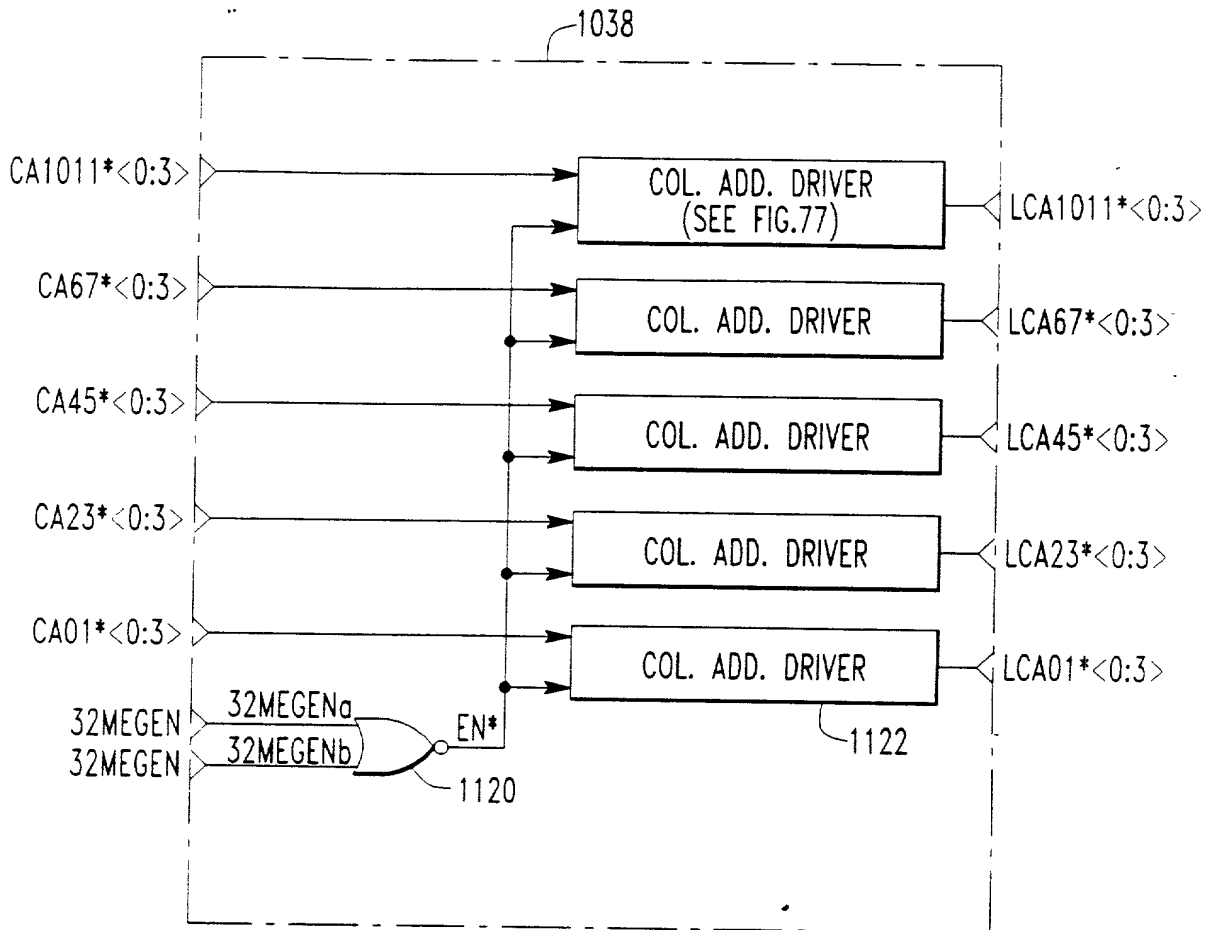
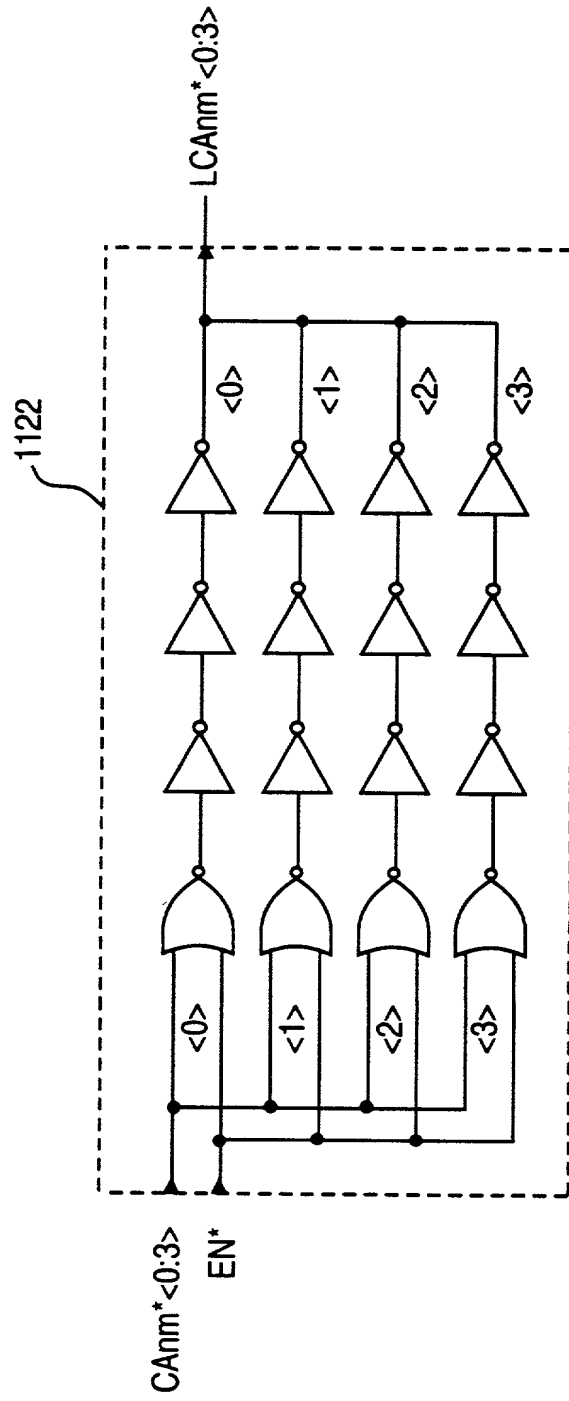


FIG. 76





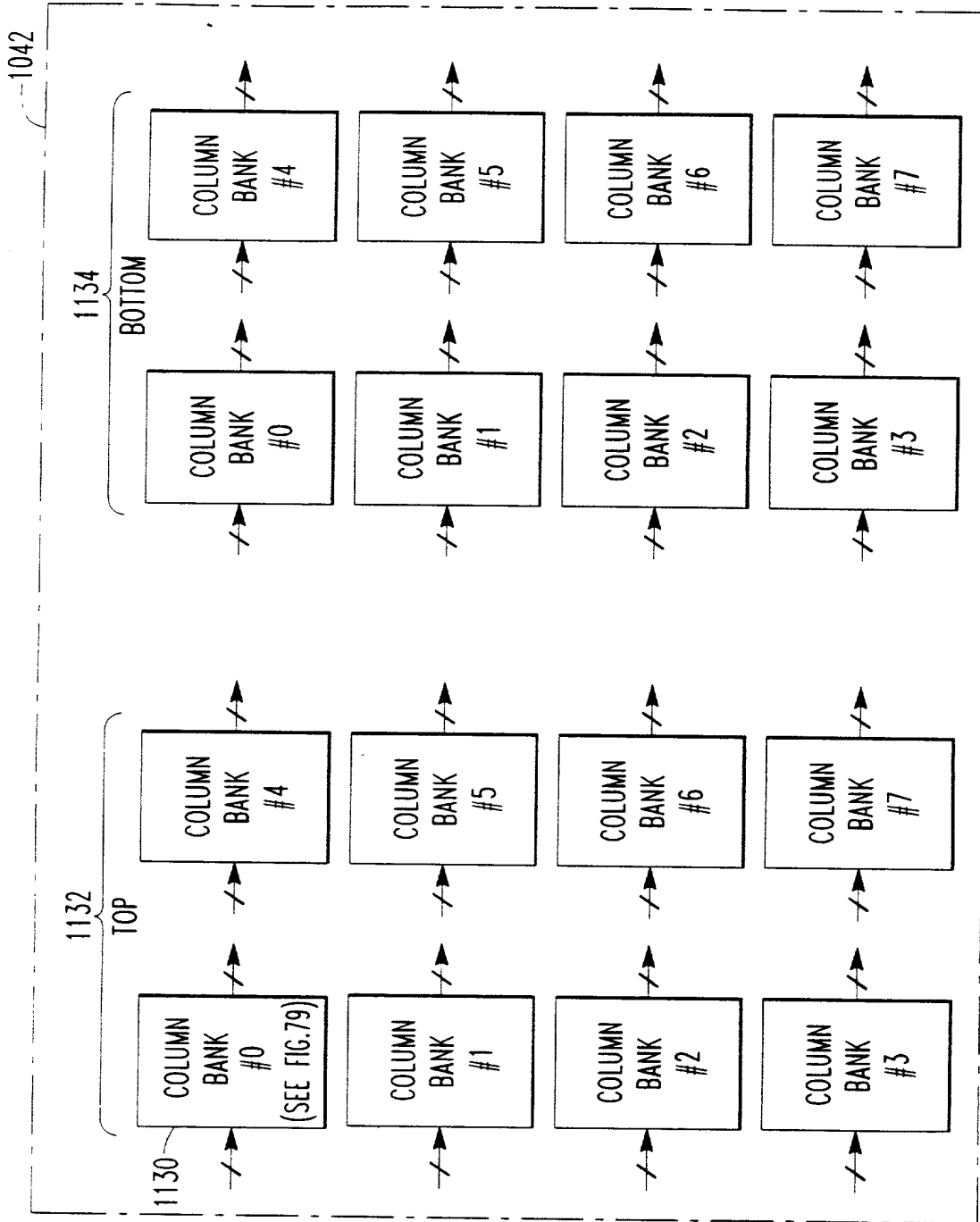
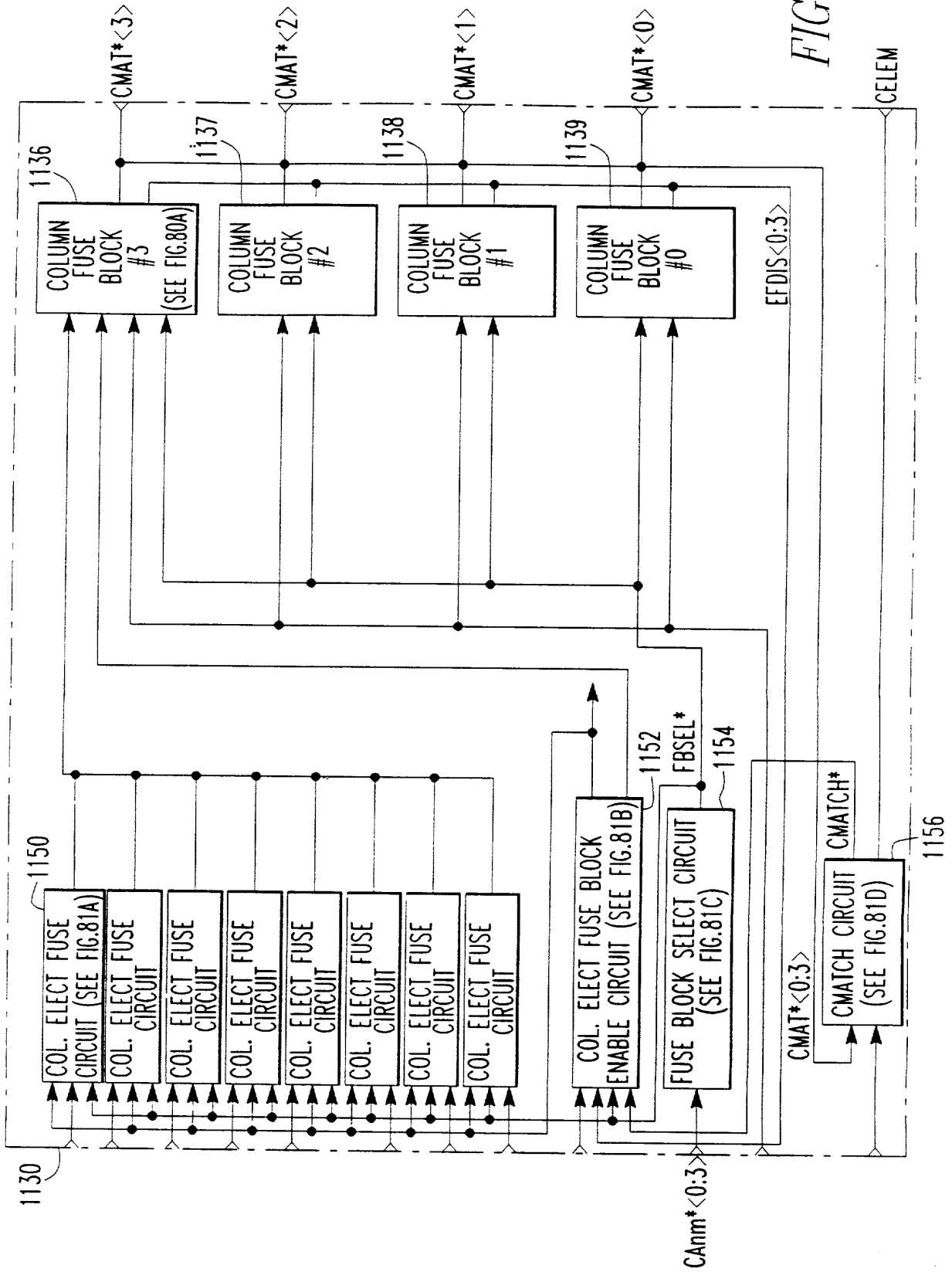


FIG. 78

FIG. 79



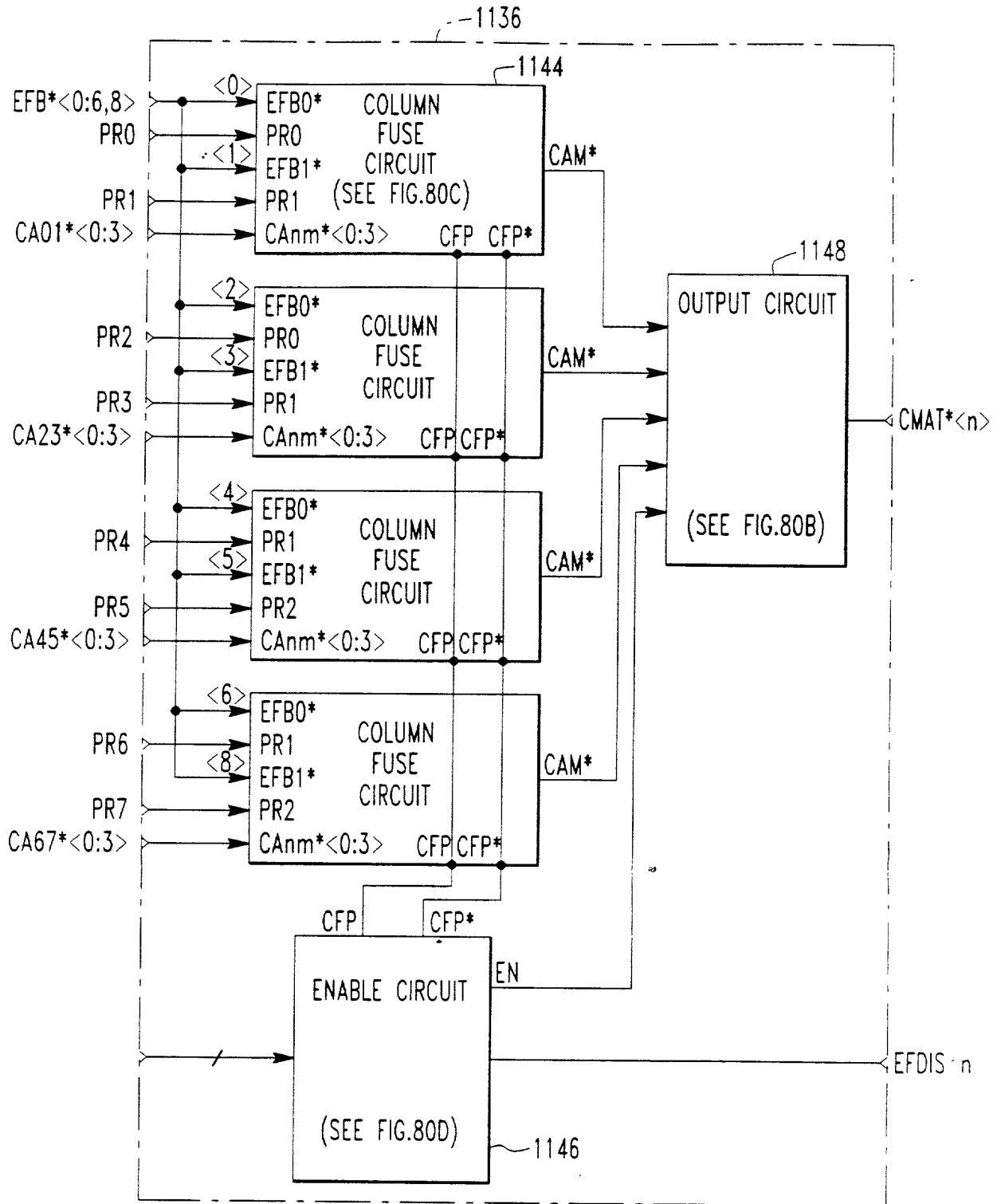
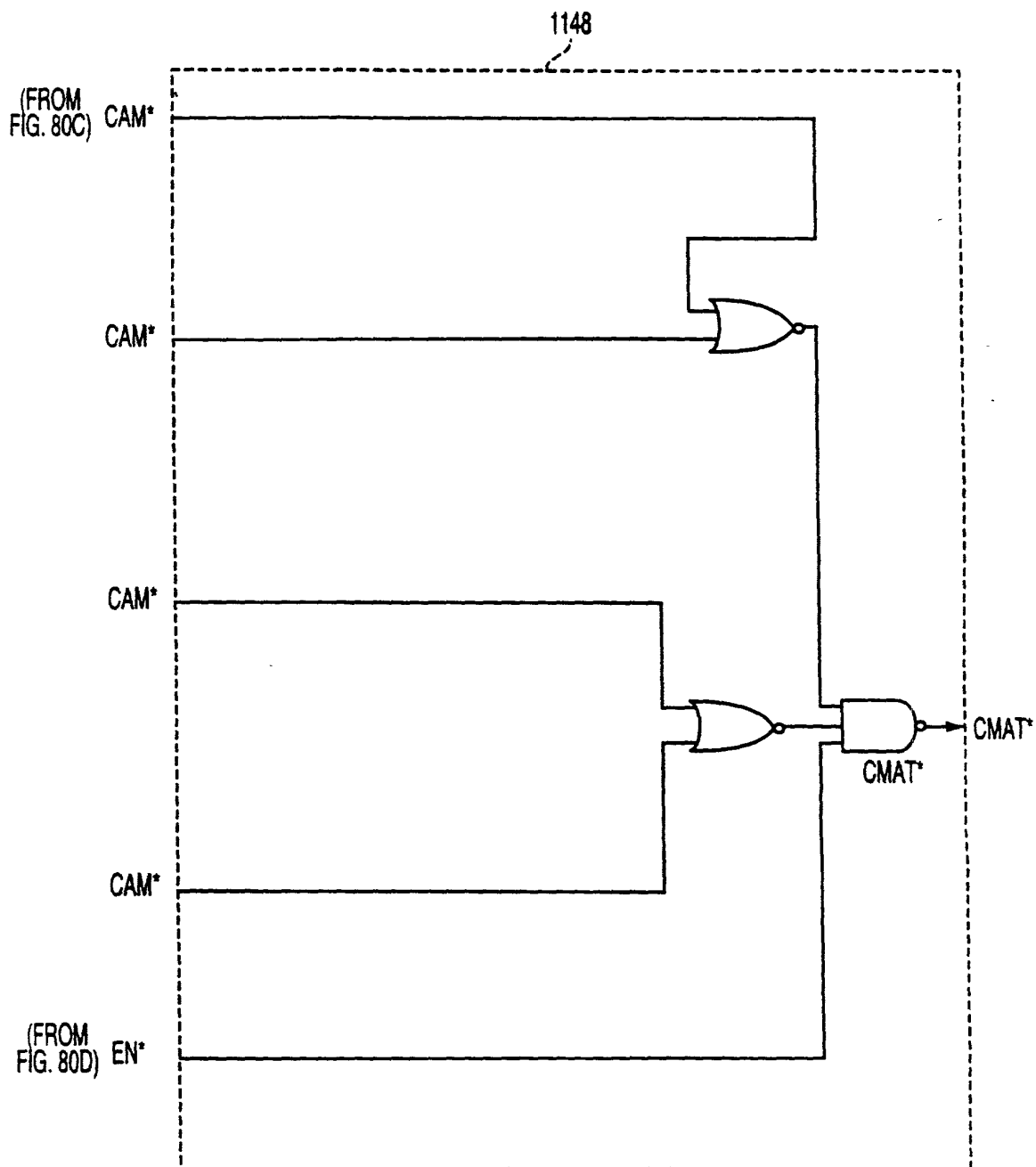


FIG. 80A

**FIG. 80B**

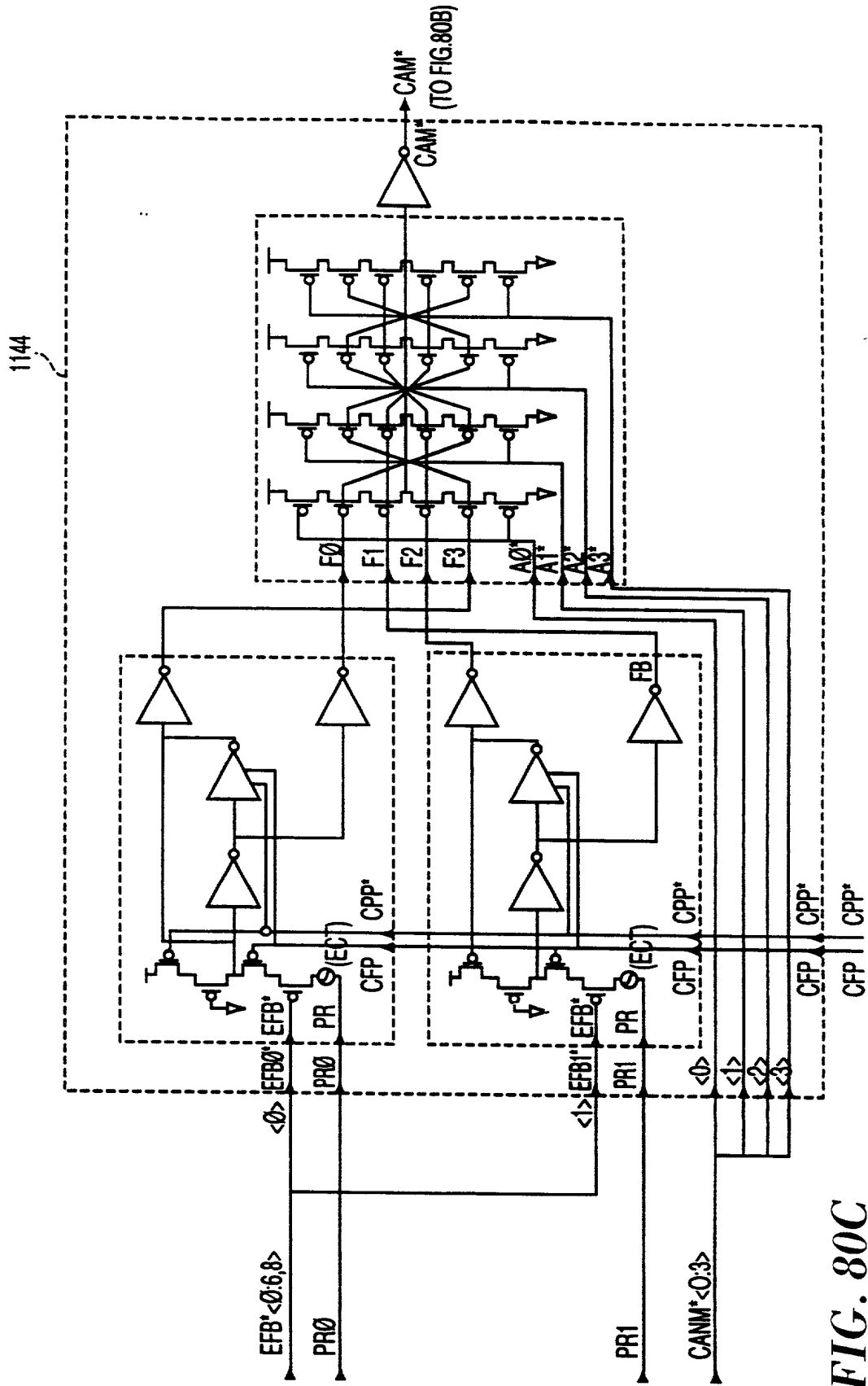
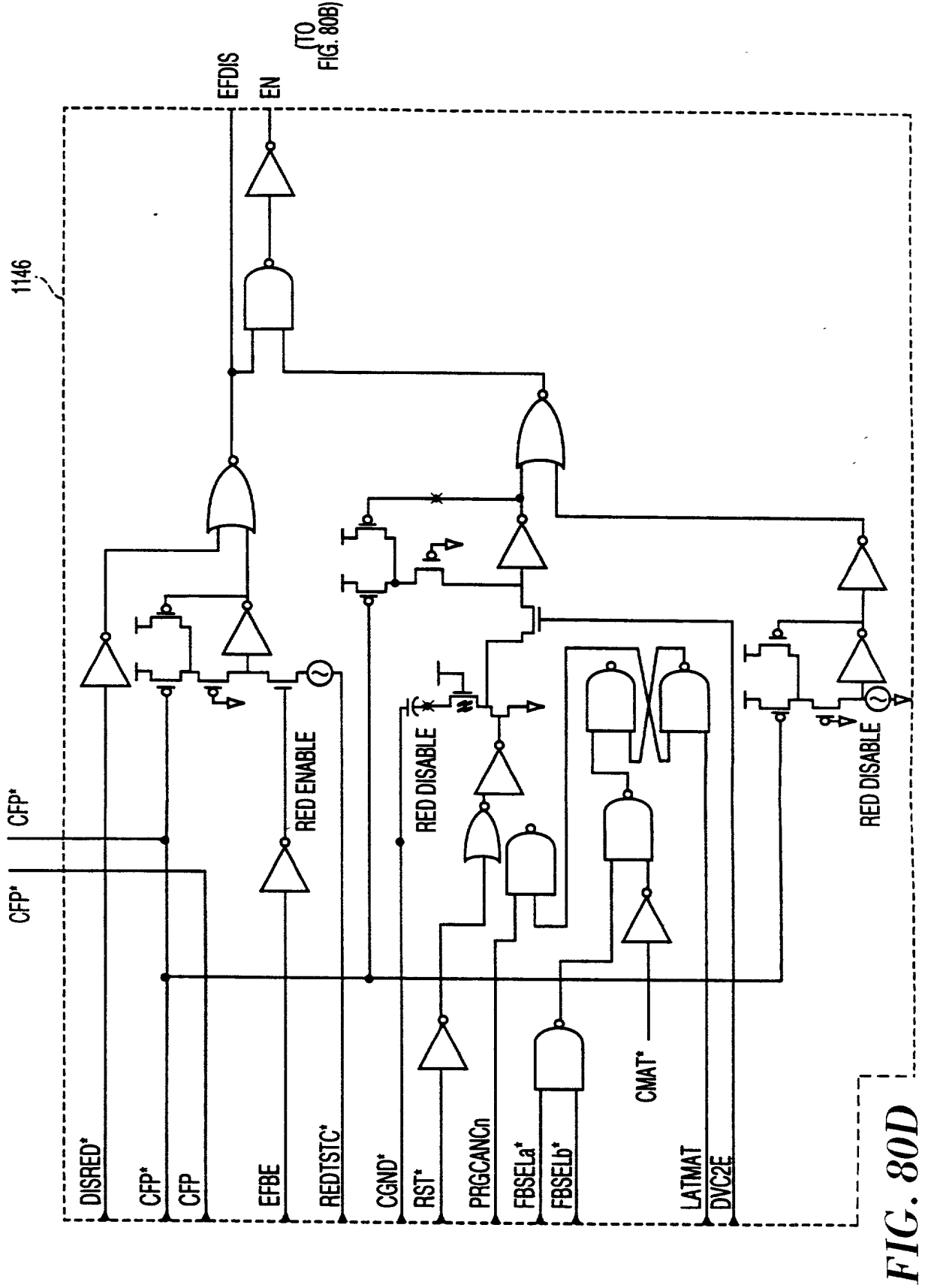


FIG. 80C



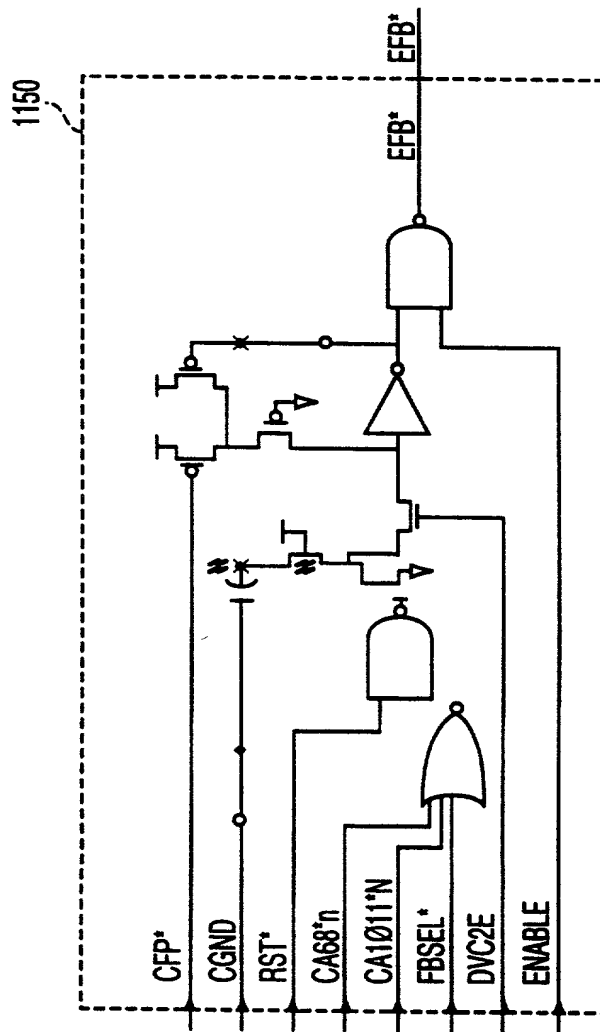


FIG. 81A



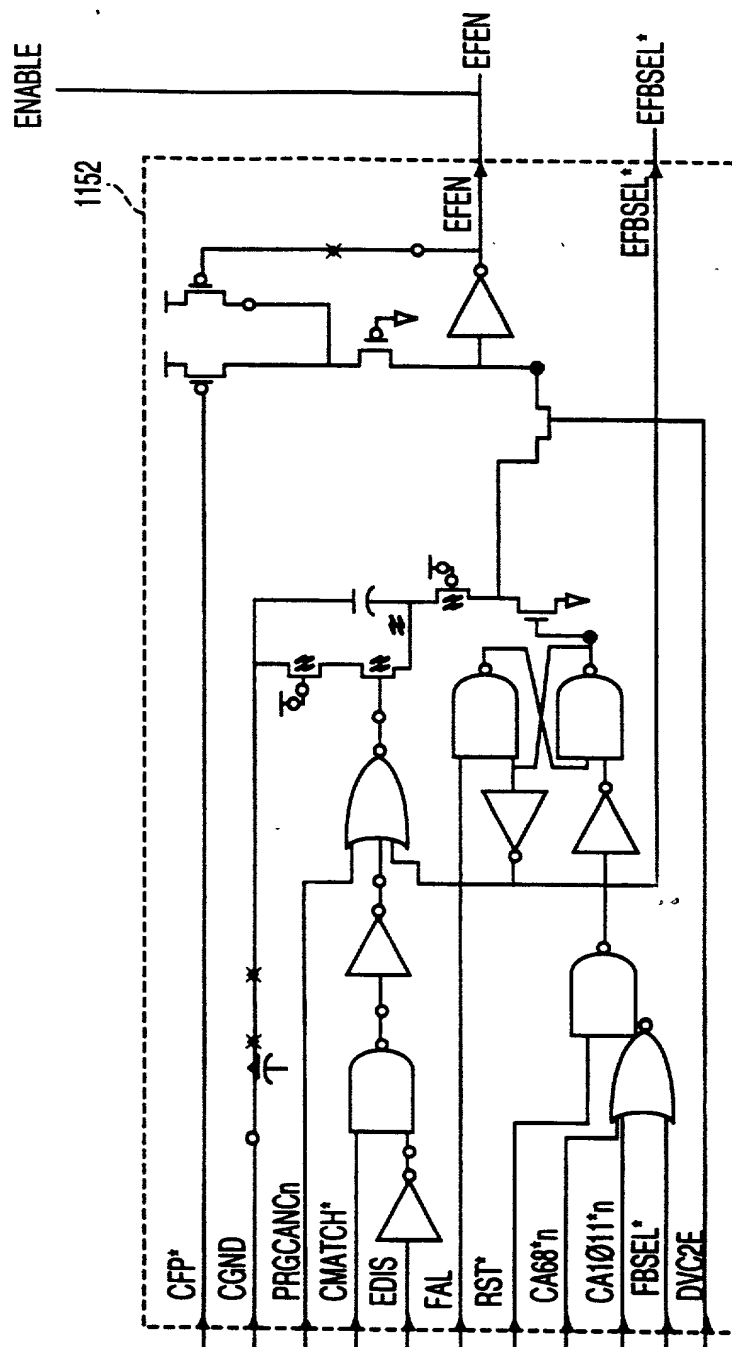


FIG. 81B

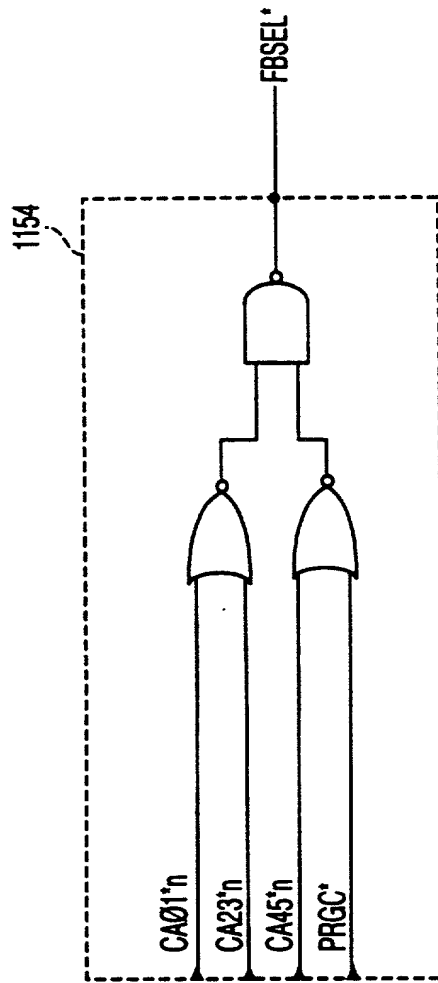


FIG. 81C

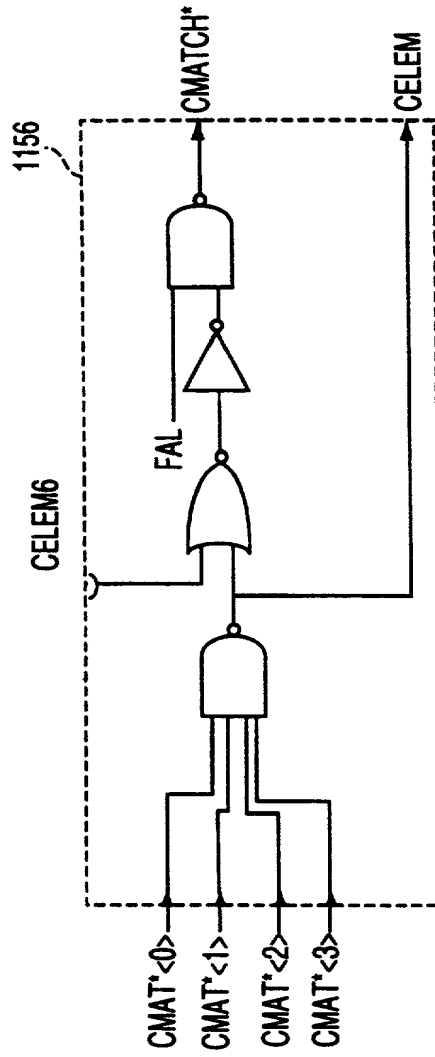


FIG. 81D

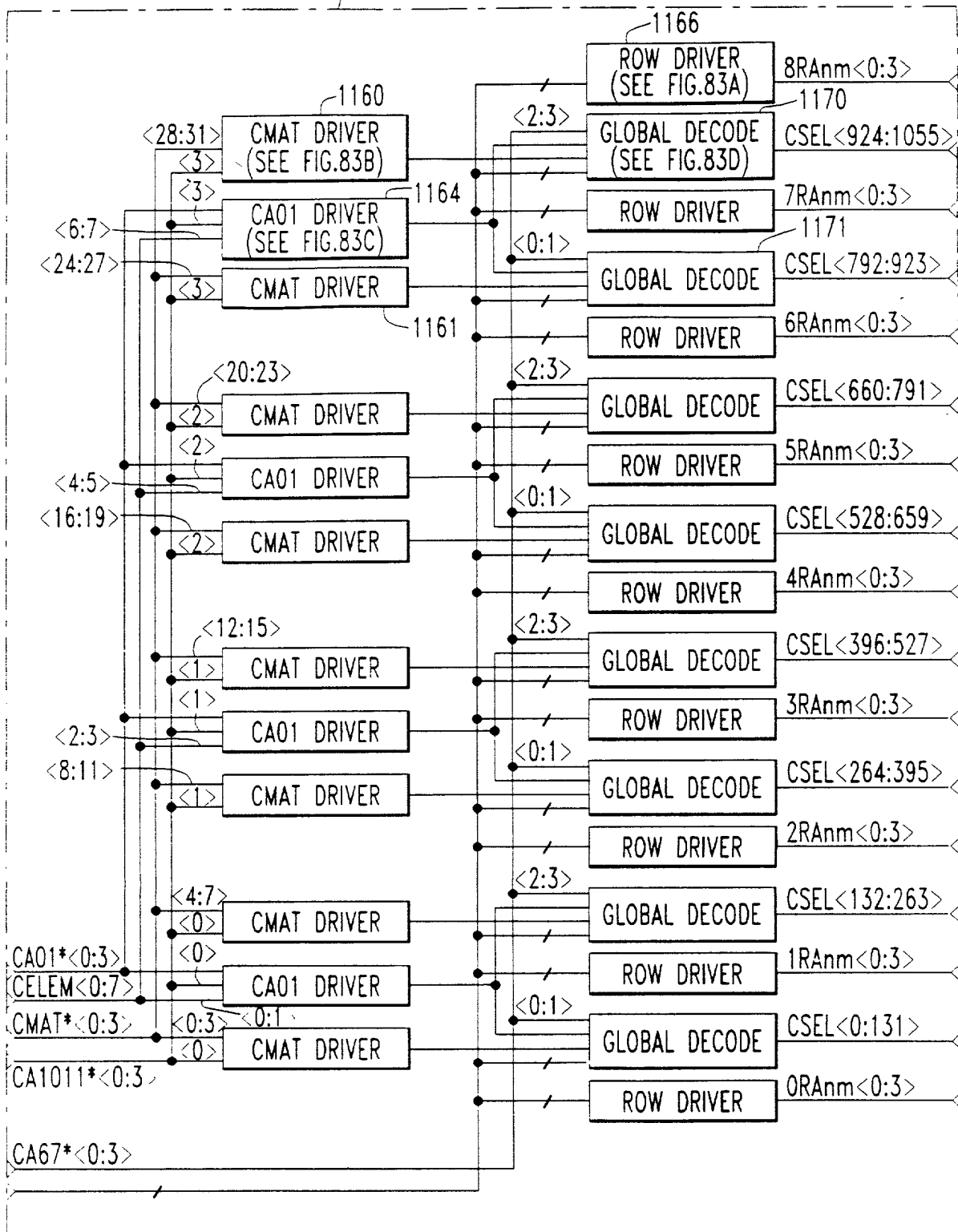


FIG. 82

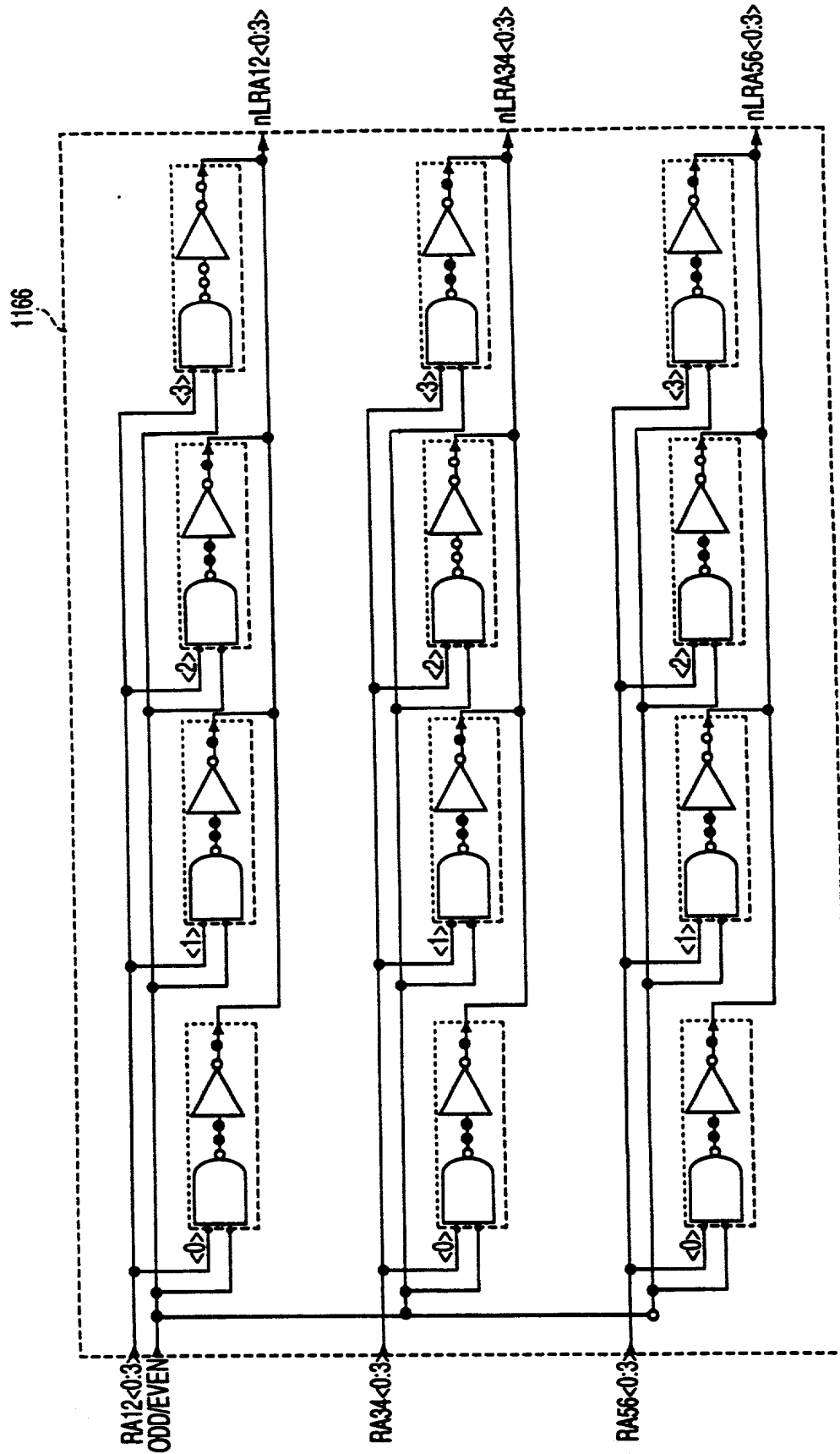


FIG. 83A

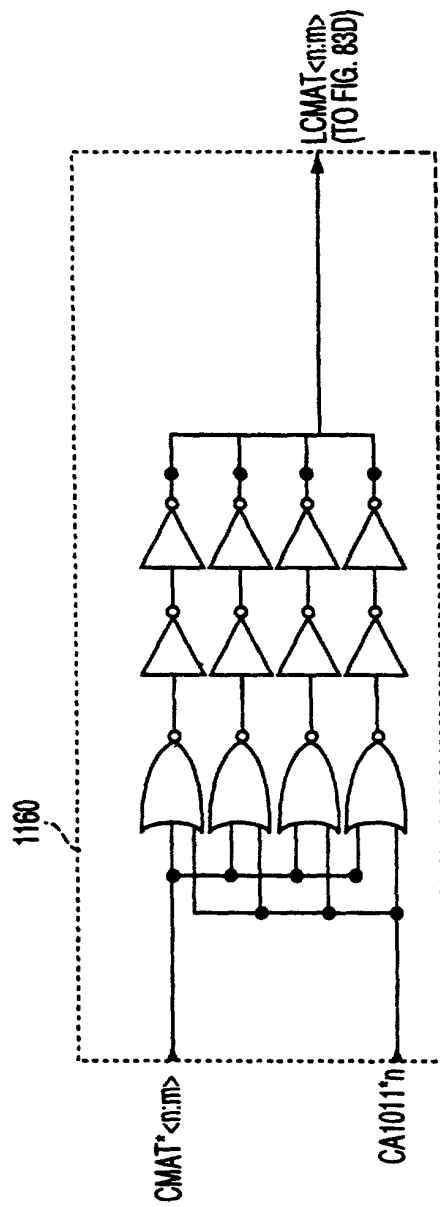


FIG. 83B

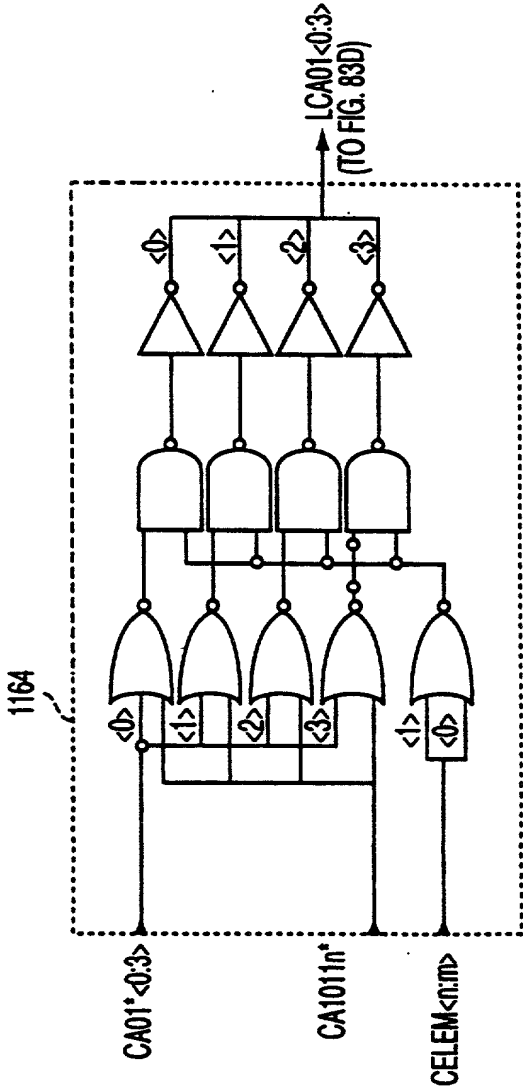
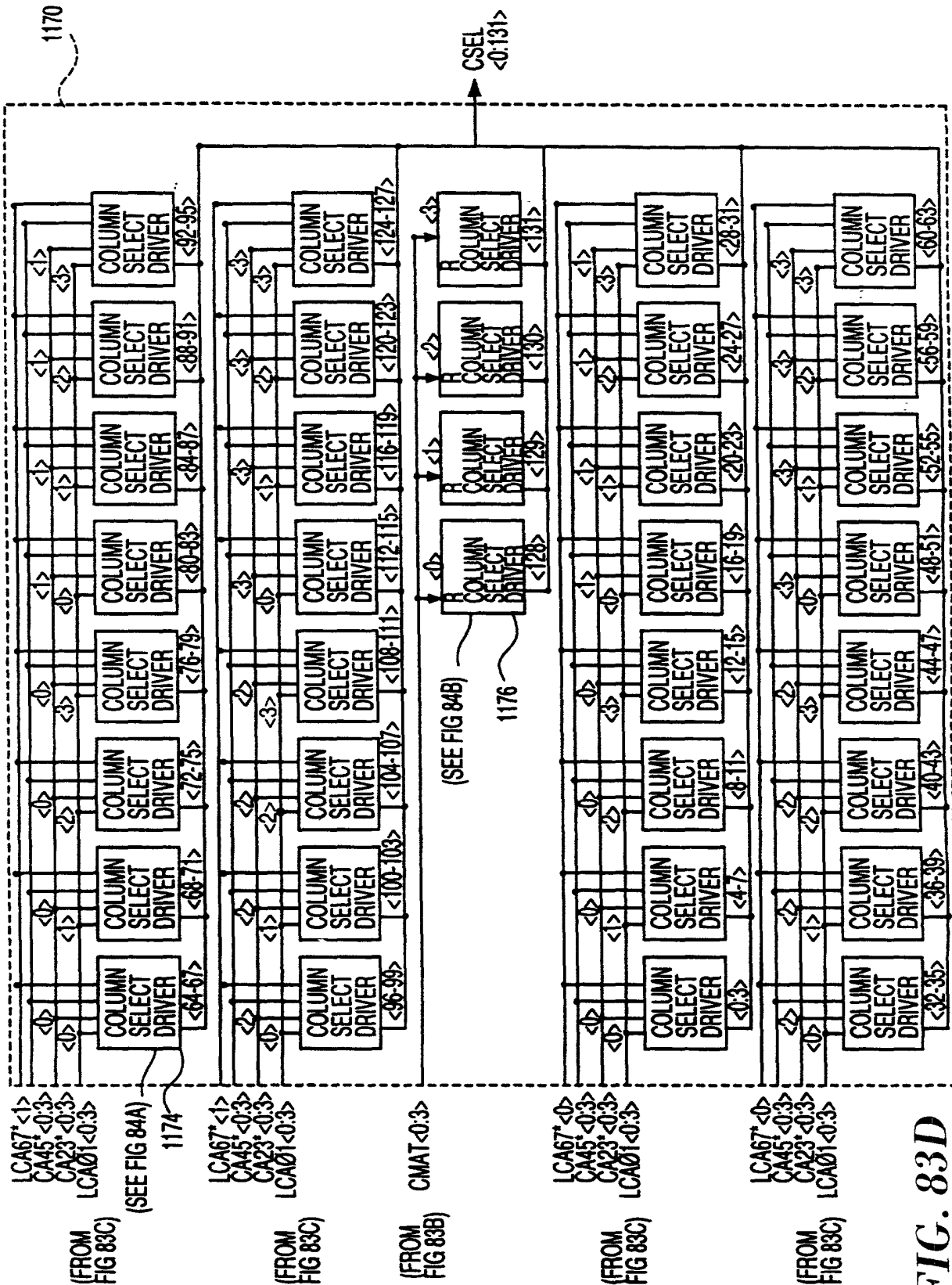


FIG. 83C





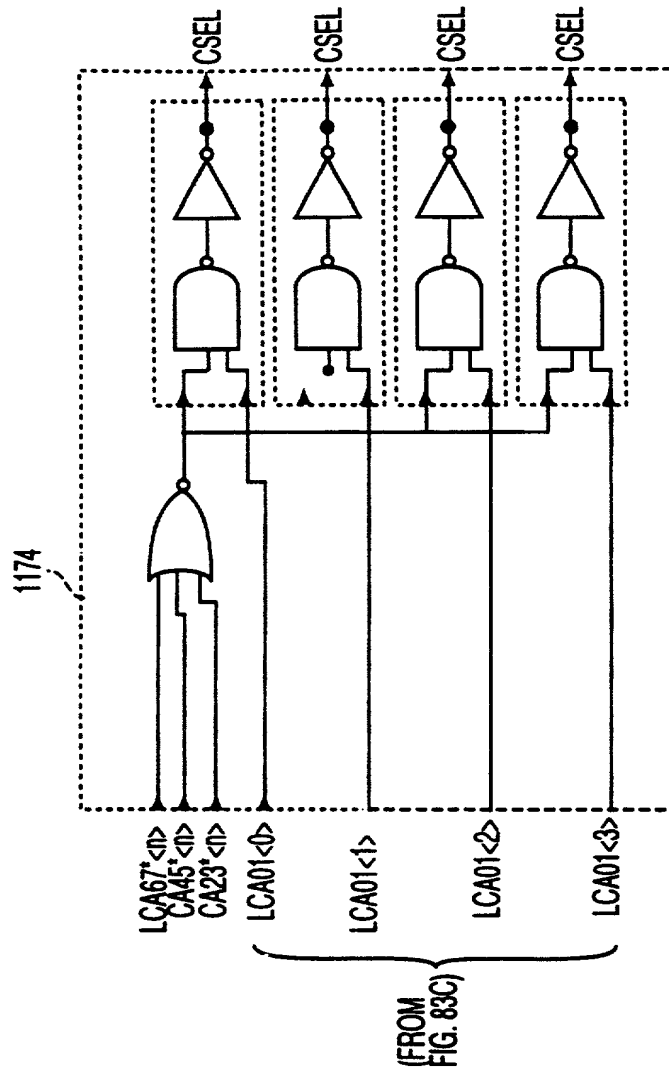
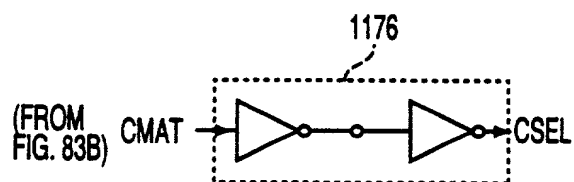


FIG. 84A

**FIG. 84B**

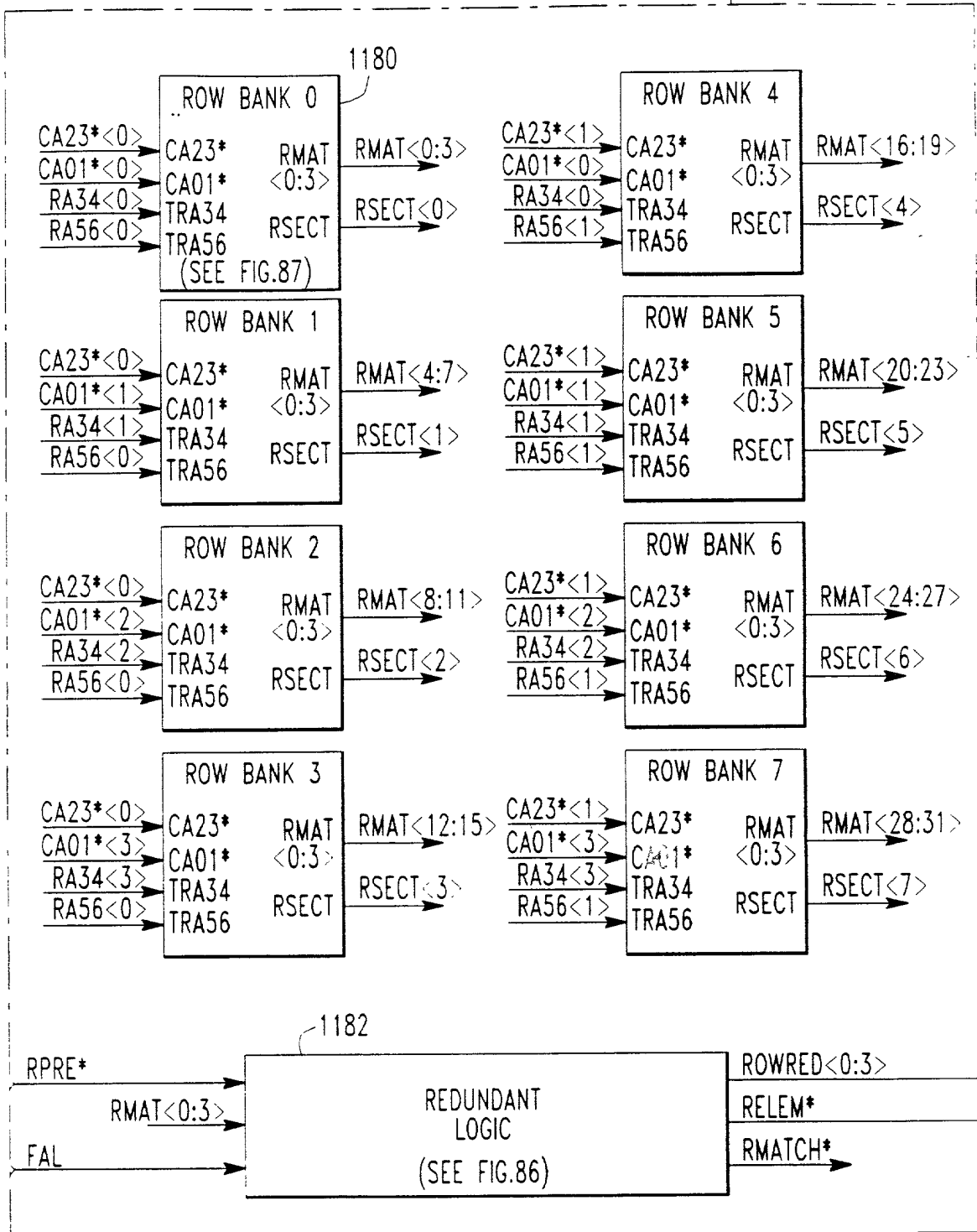


FIG. 85

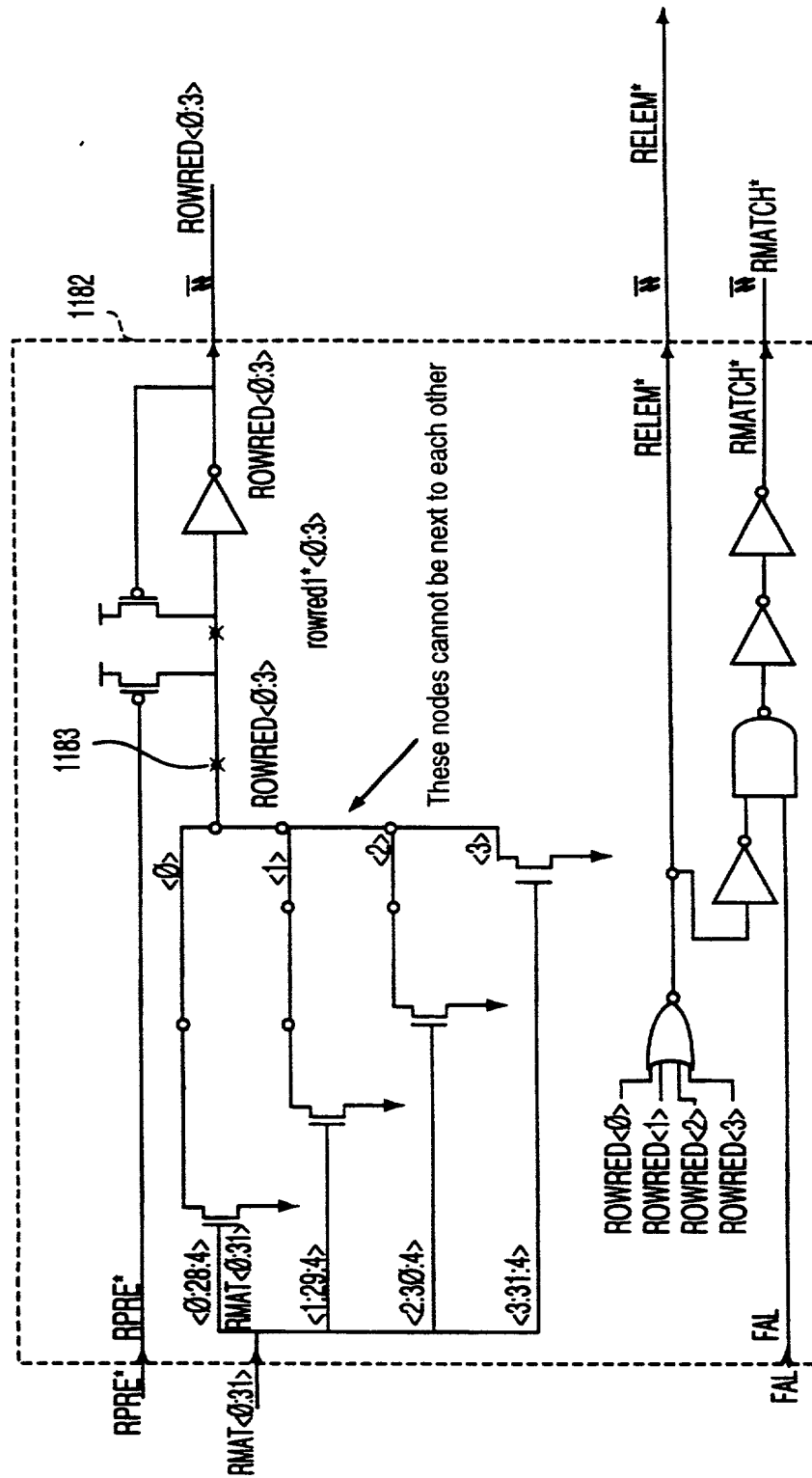


FIG. 86

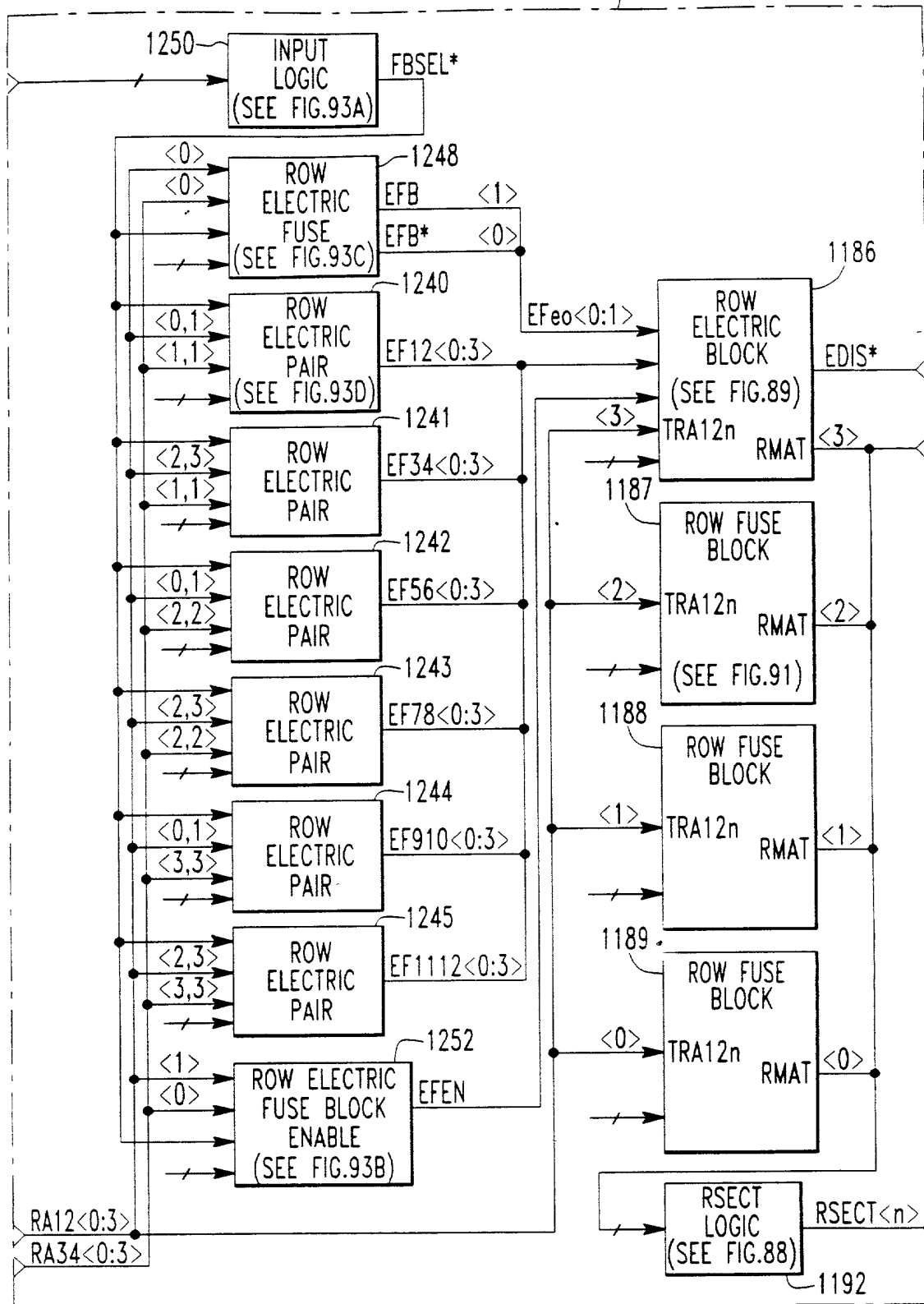


FIG. 87

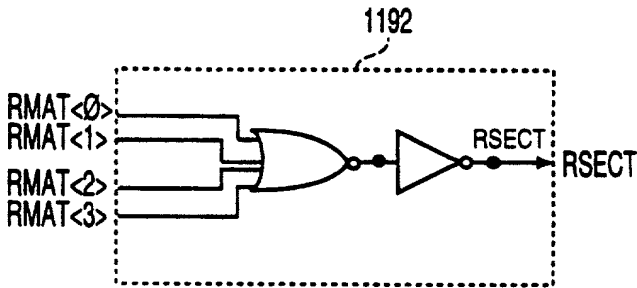


FIG. 88

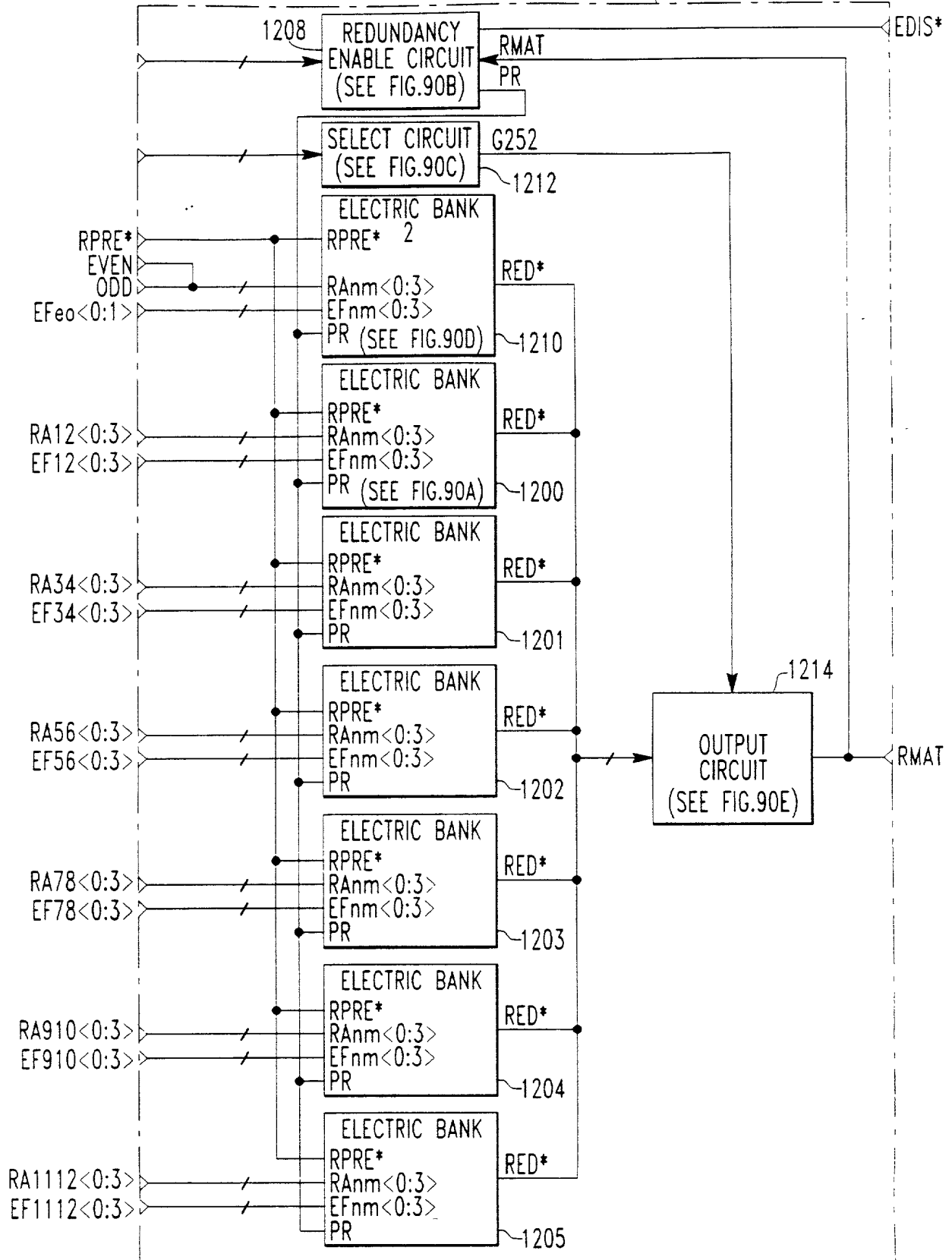
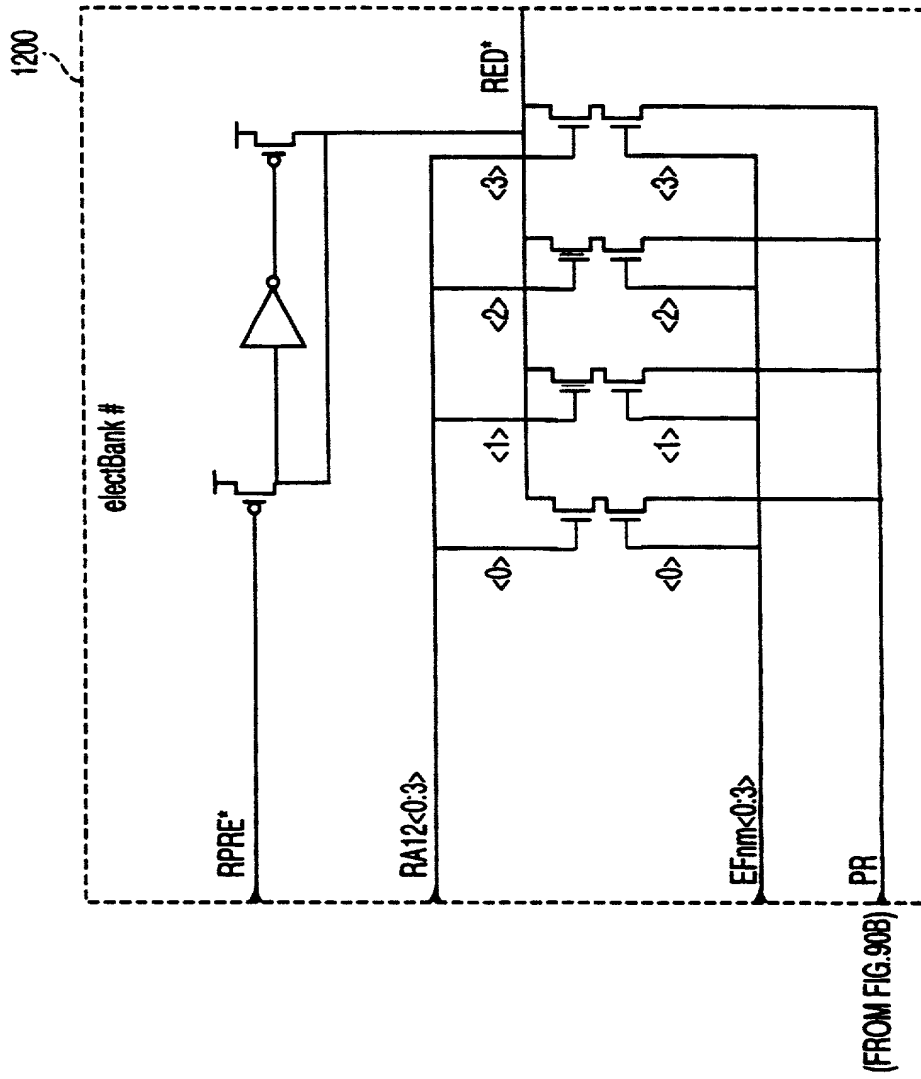


FIG. 89

FIG. 90A



(FROM FIG. 90B)

FIG. 90A



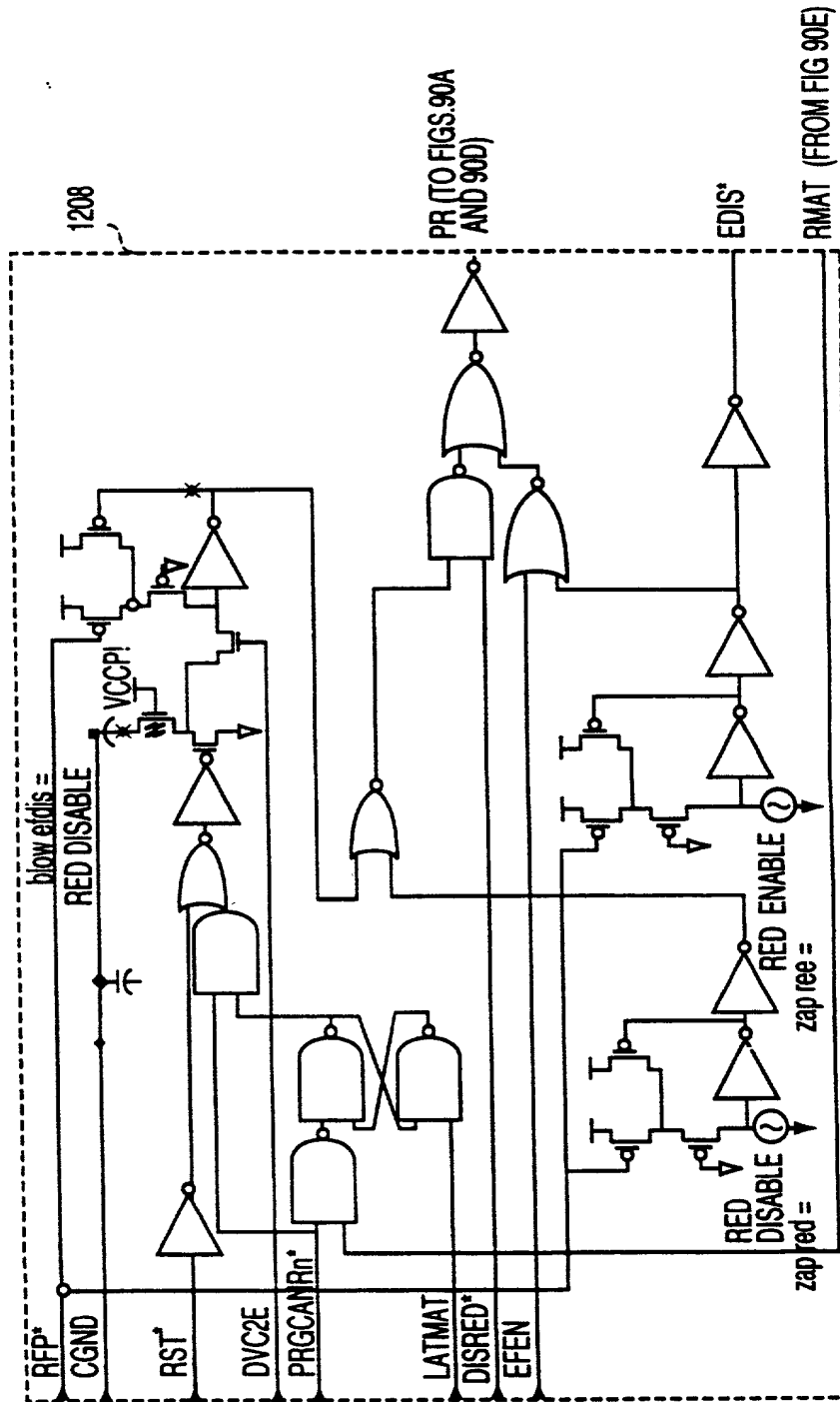


FIG. 90B

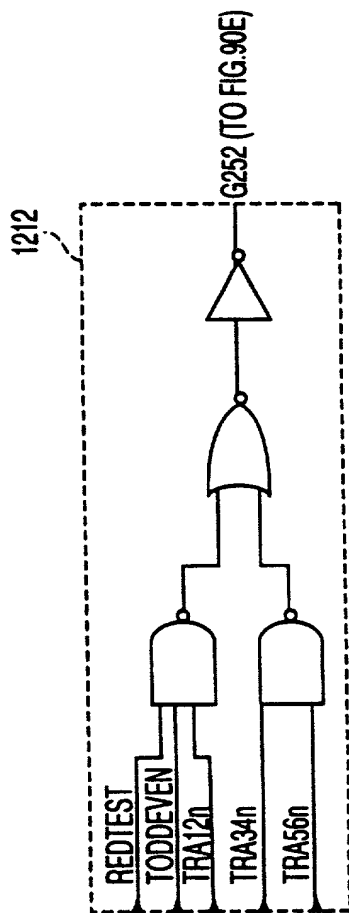


FIG. 90C

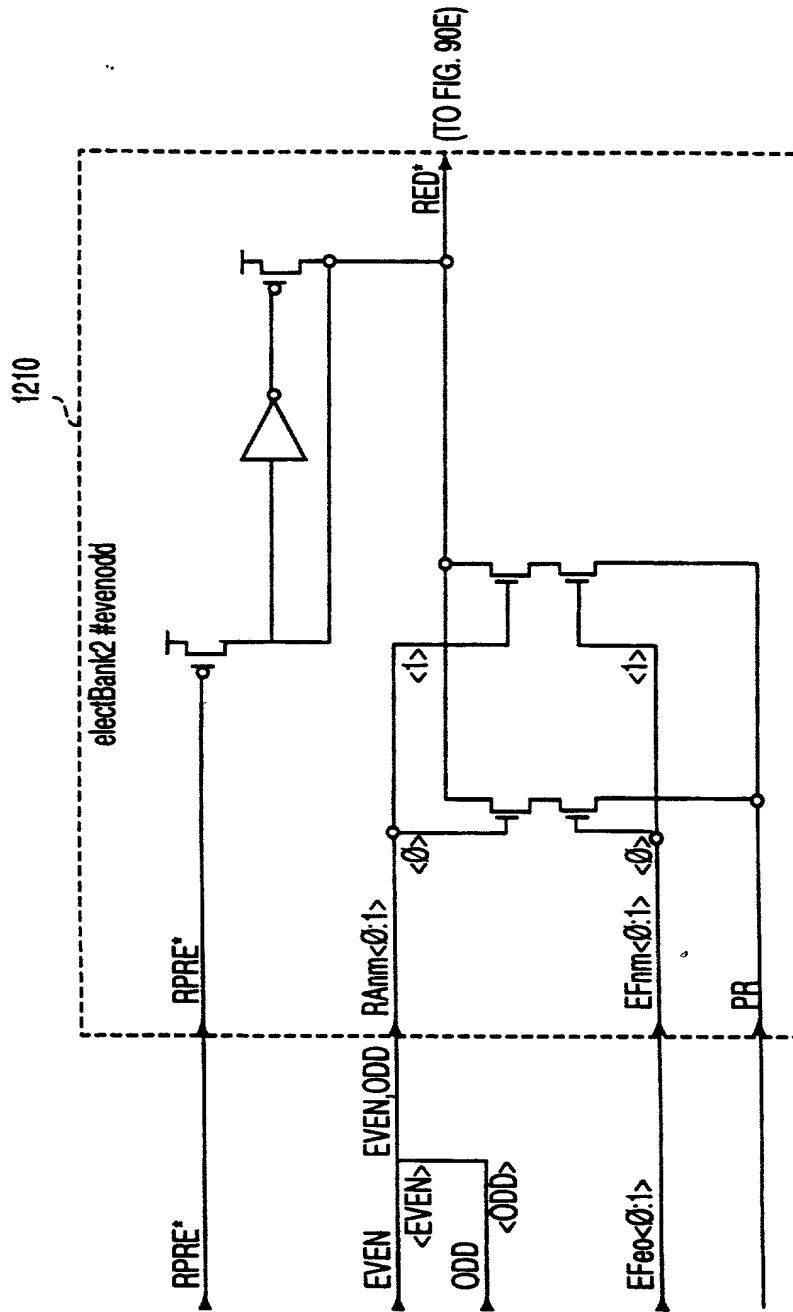


FIG. 90D

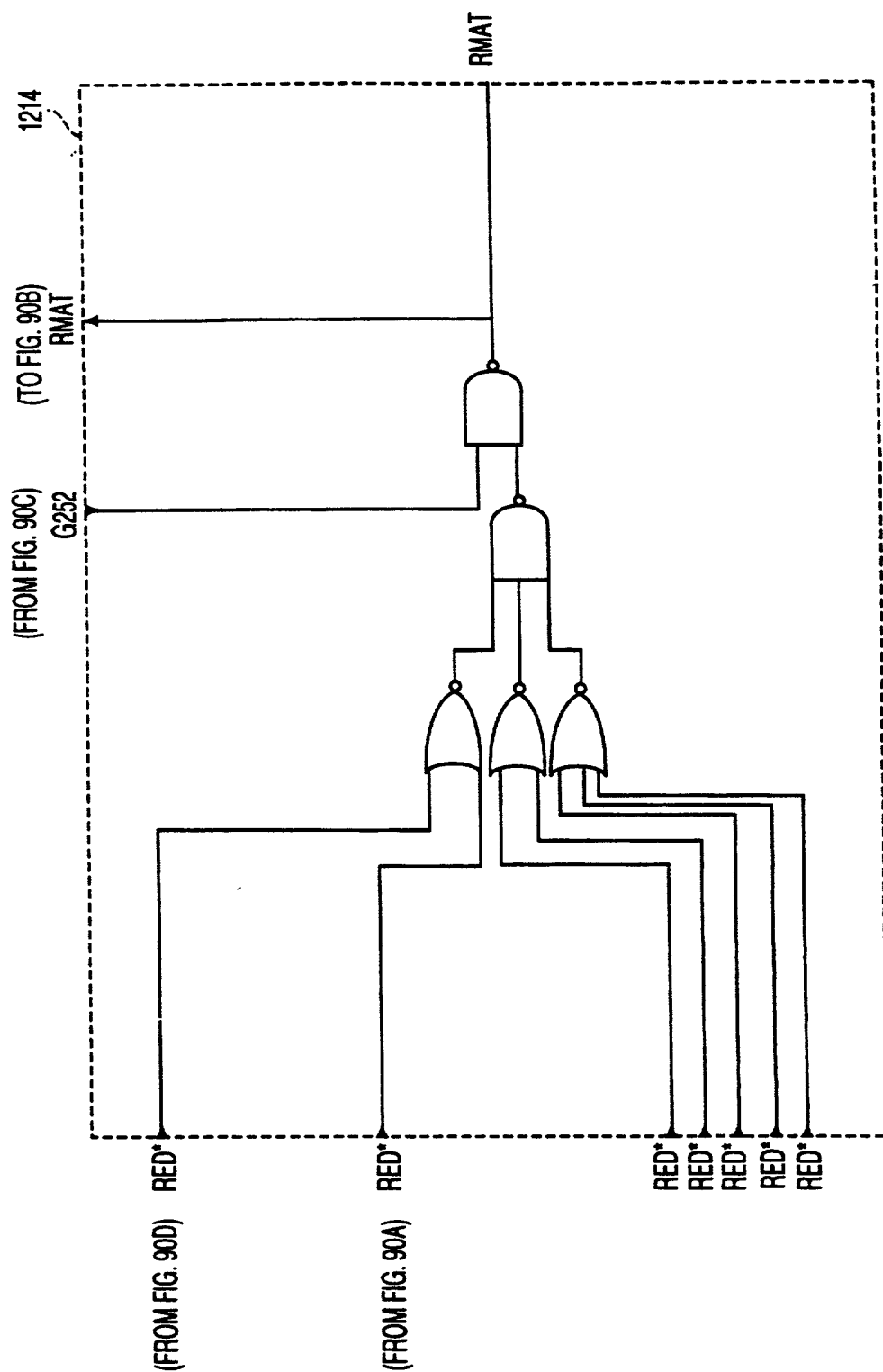


FIG. 90E

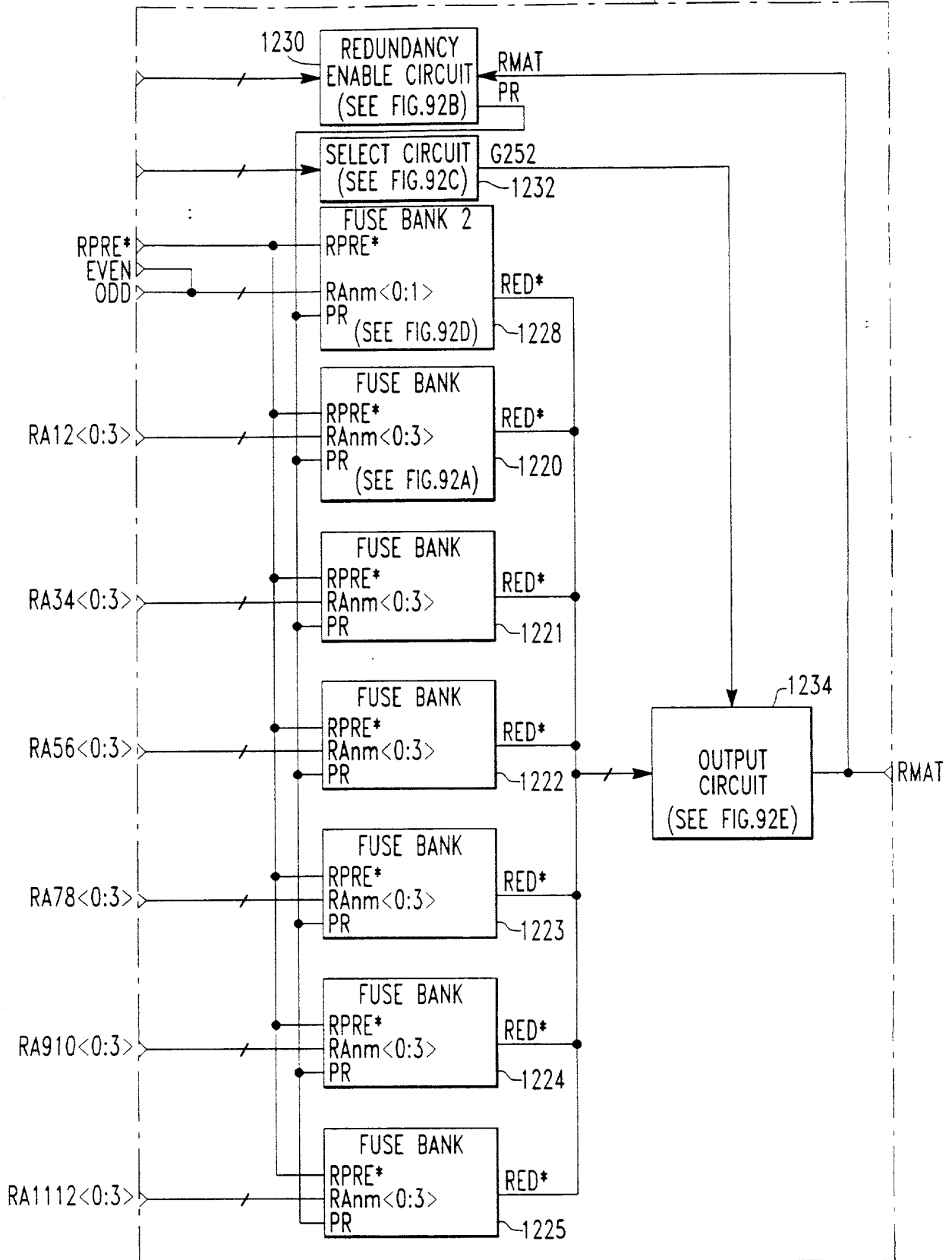


FIG. 91

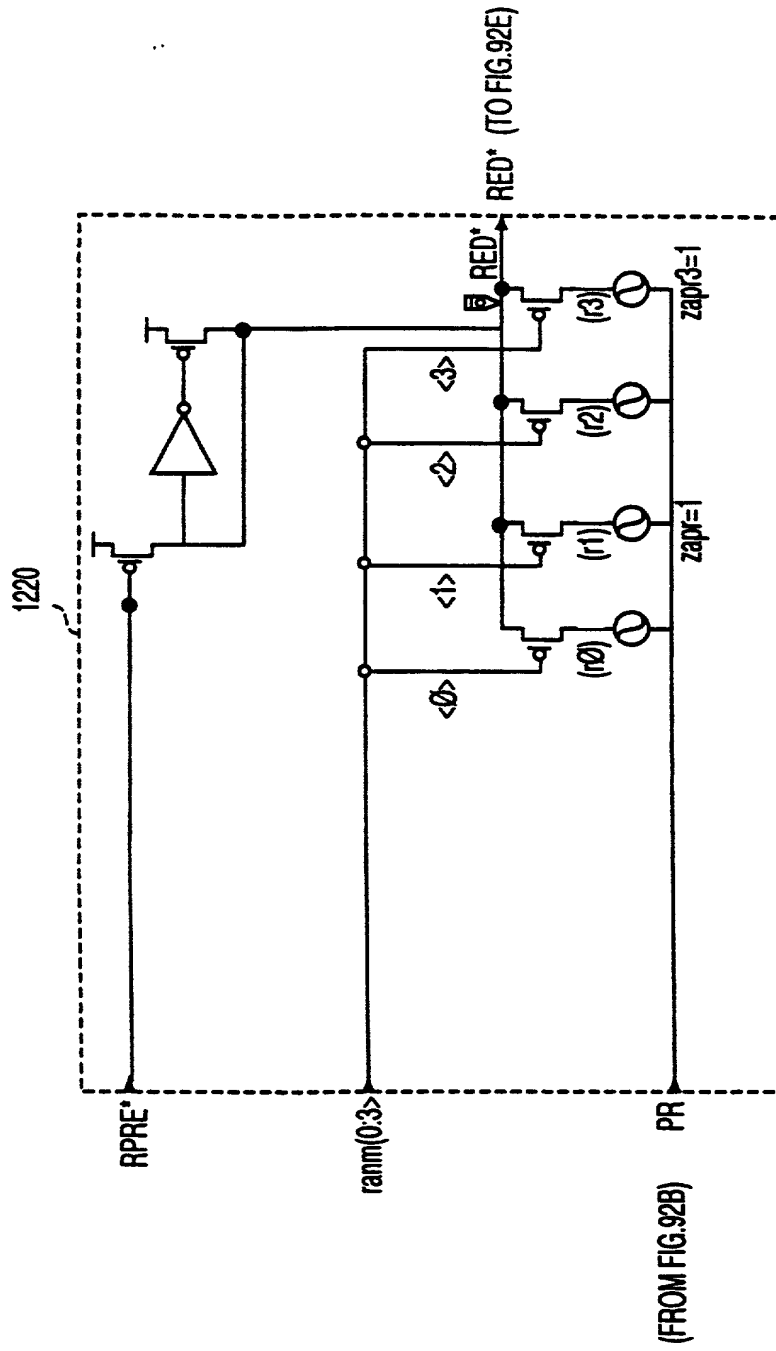


FIG. 92A



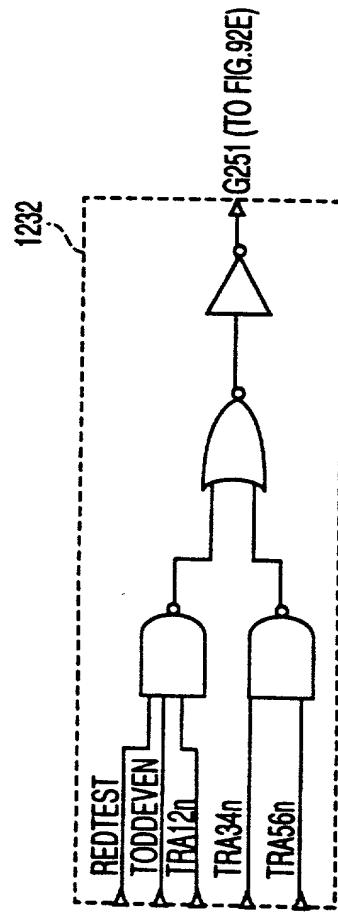


FIG. 92C



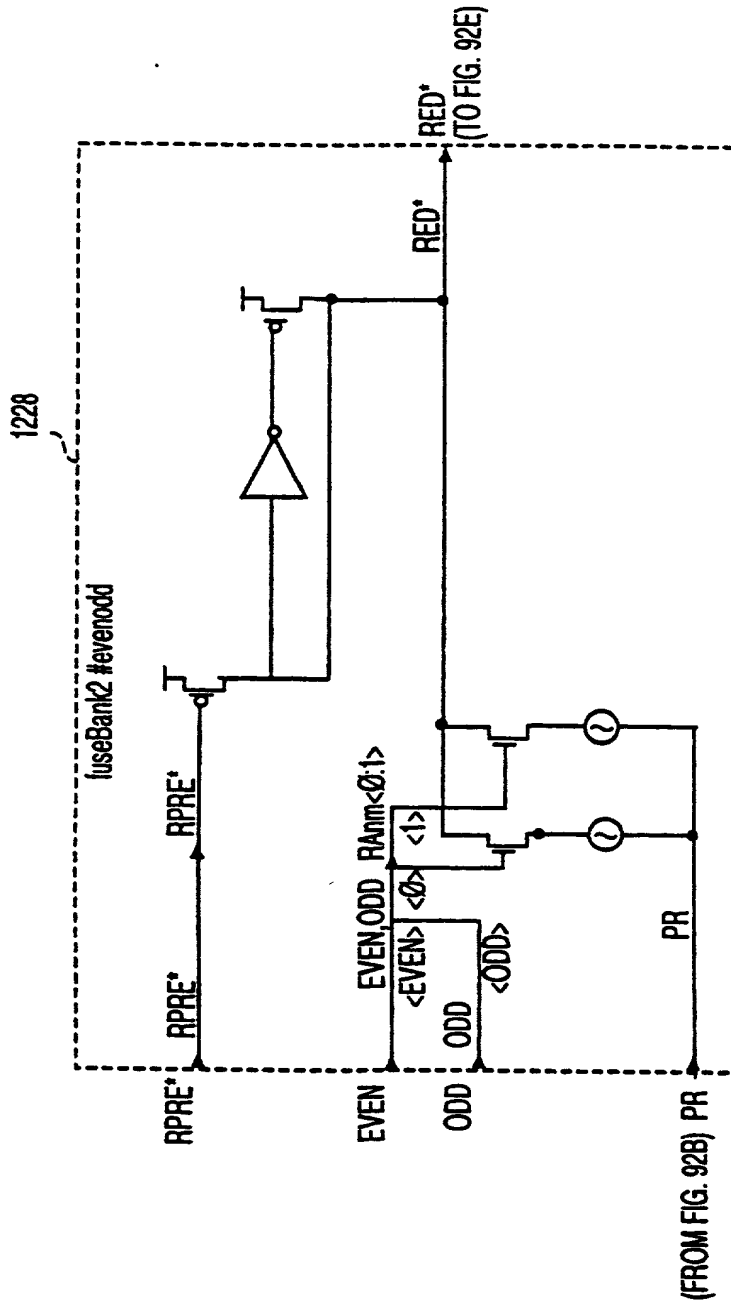


FIG. 92D

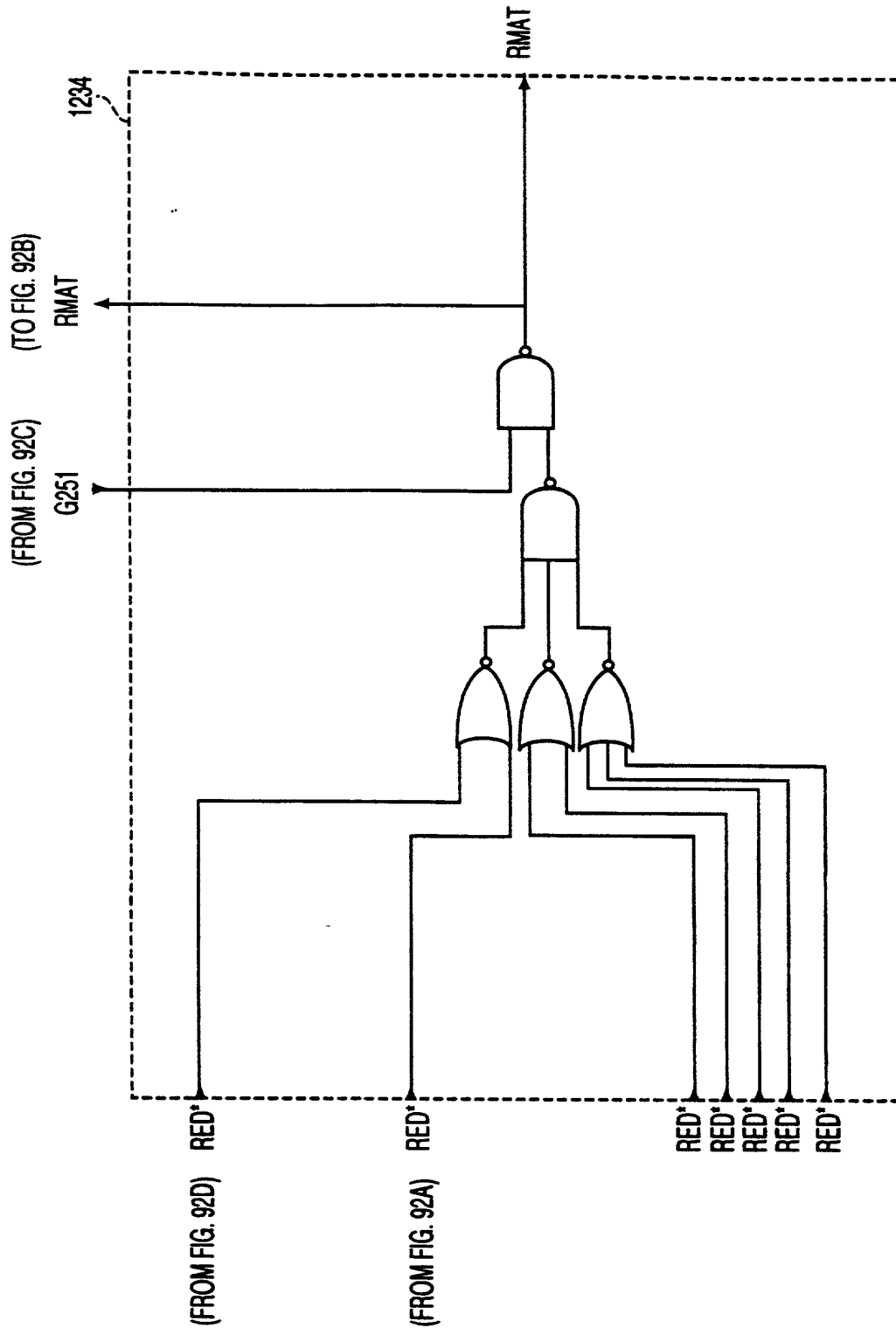
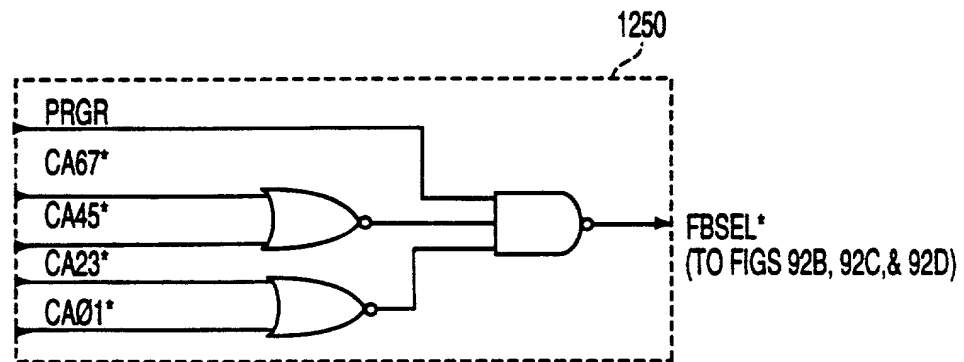
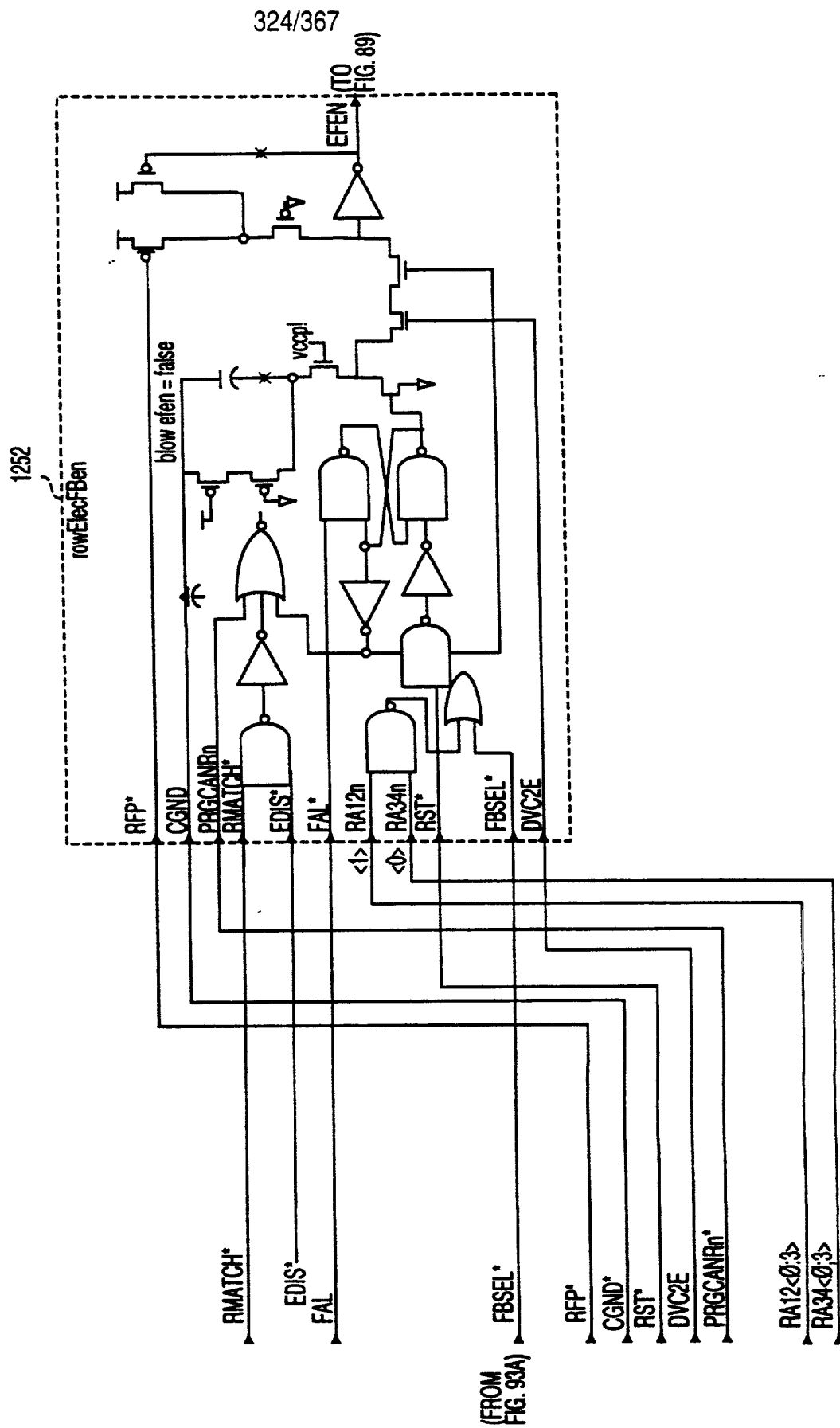


FIG. 92E

**FIG. 93A**



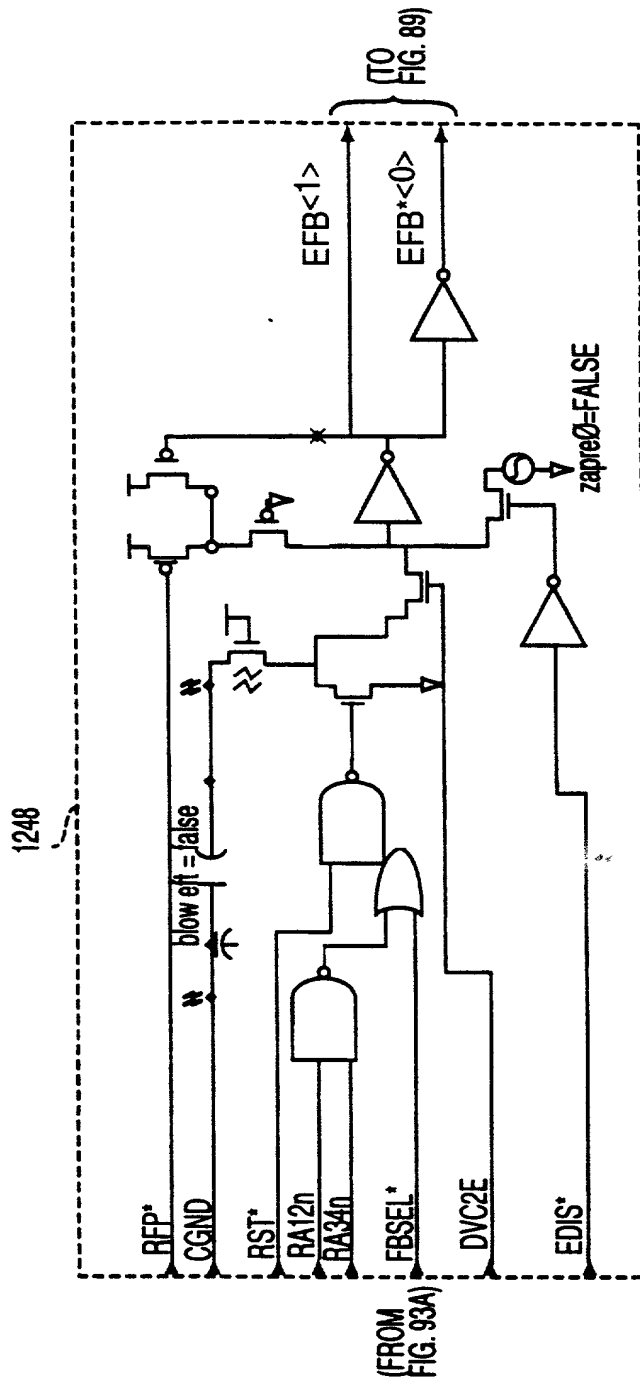


FIG. 93C

(TO  
FIG. 89)

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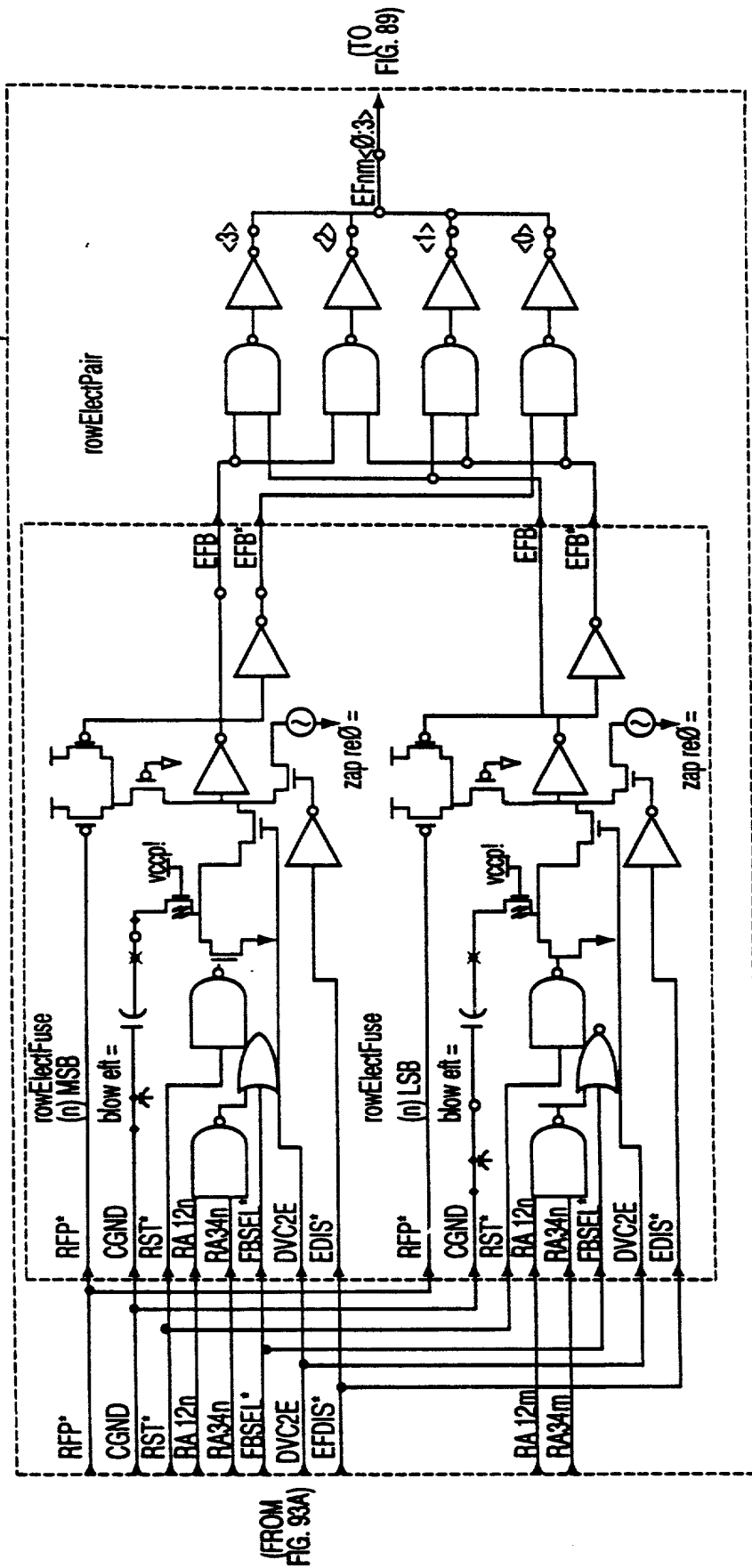
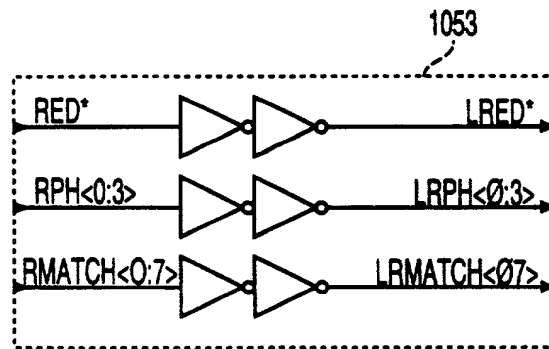


FIG. 93D

**FIG. 94**

1059

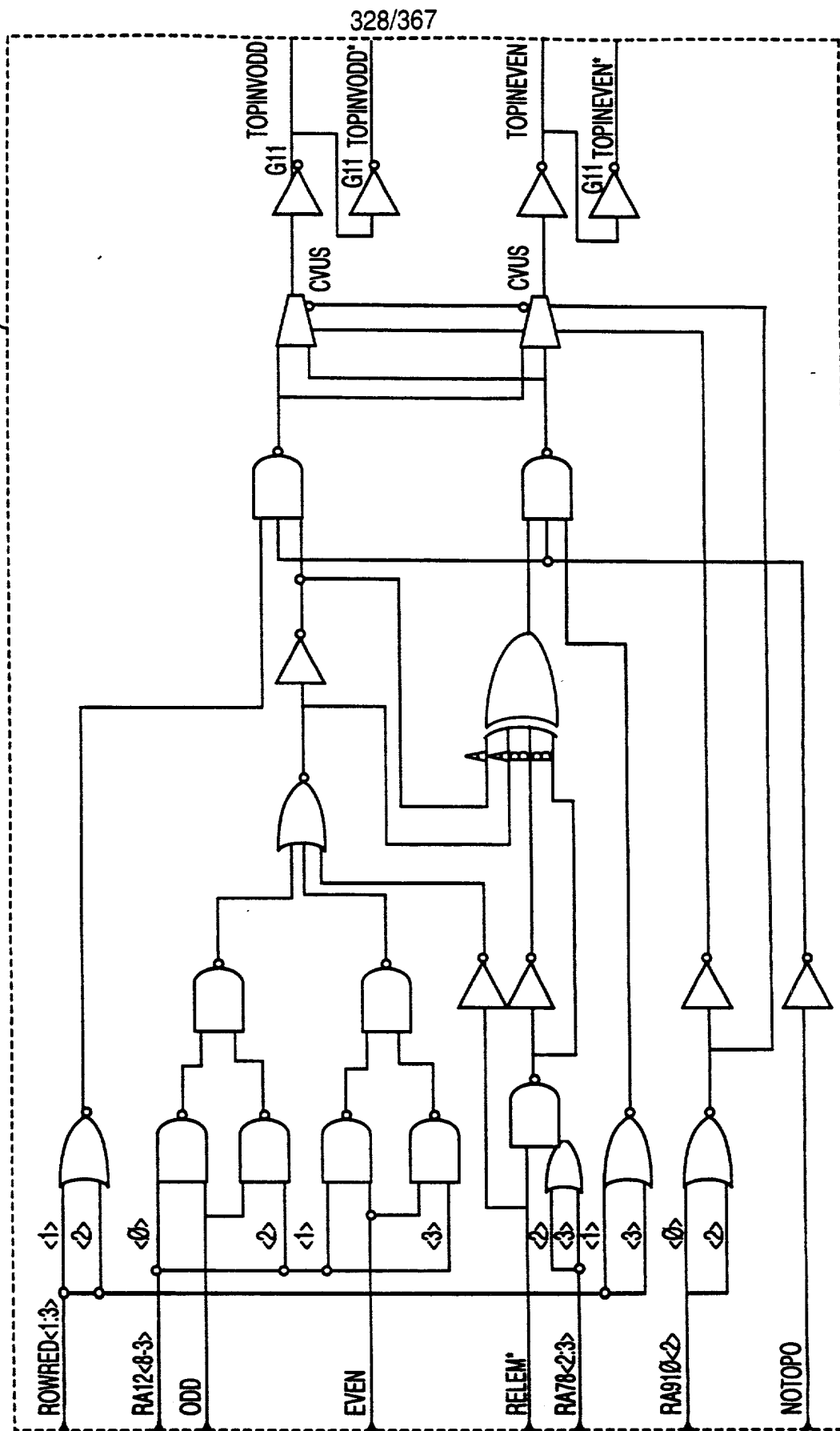
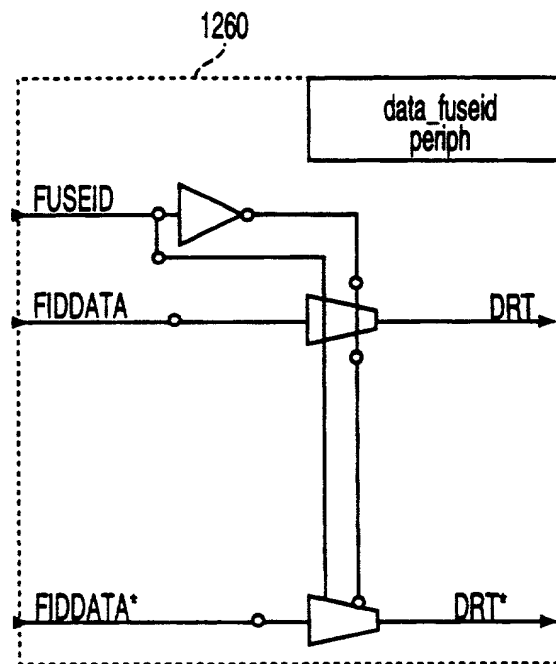


FIG. 95



**FIG. 96**



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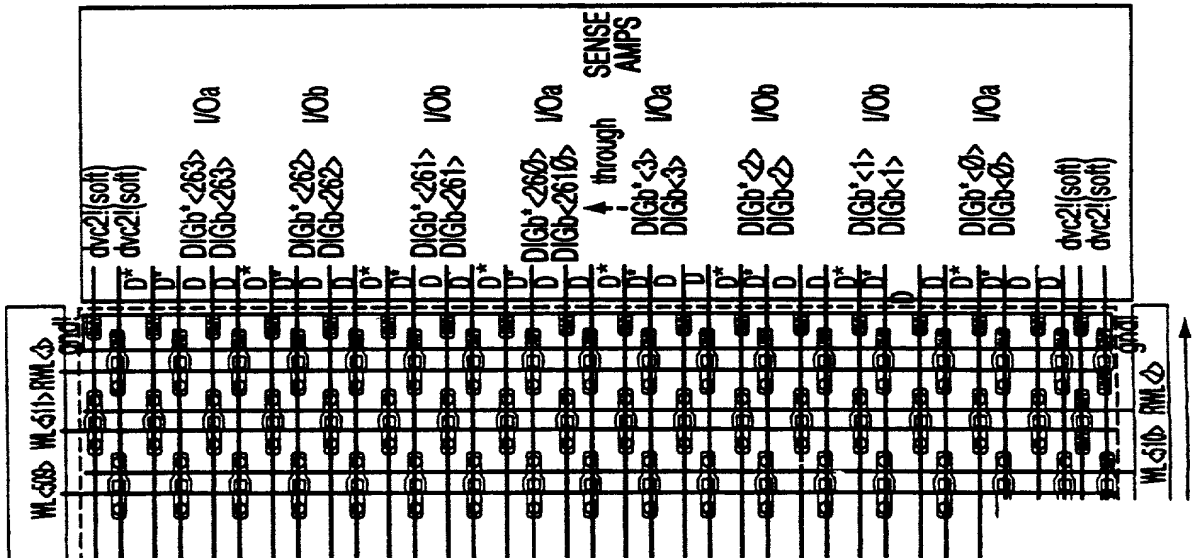
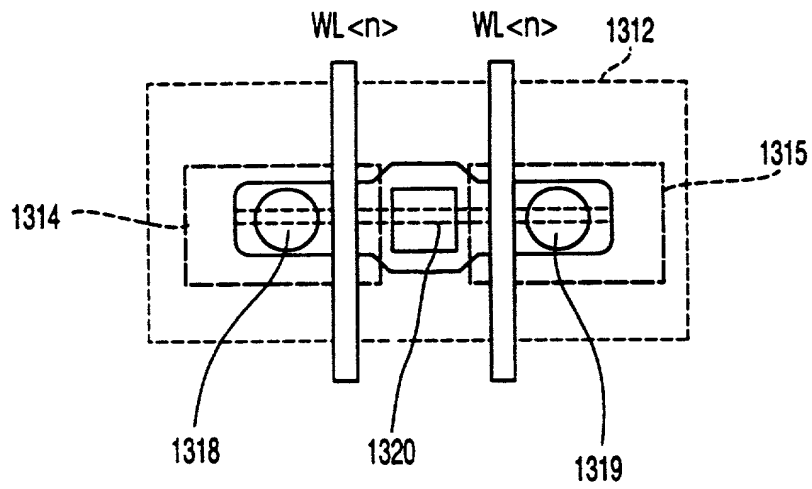


FIG. 97-1 FIG. 97-2

FIG. 97-2

**FIG. 98**

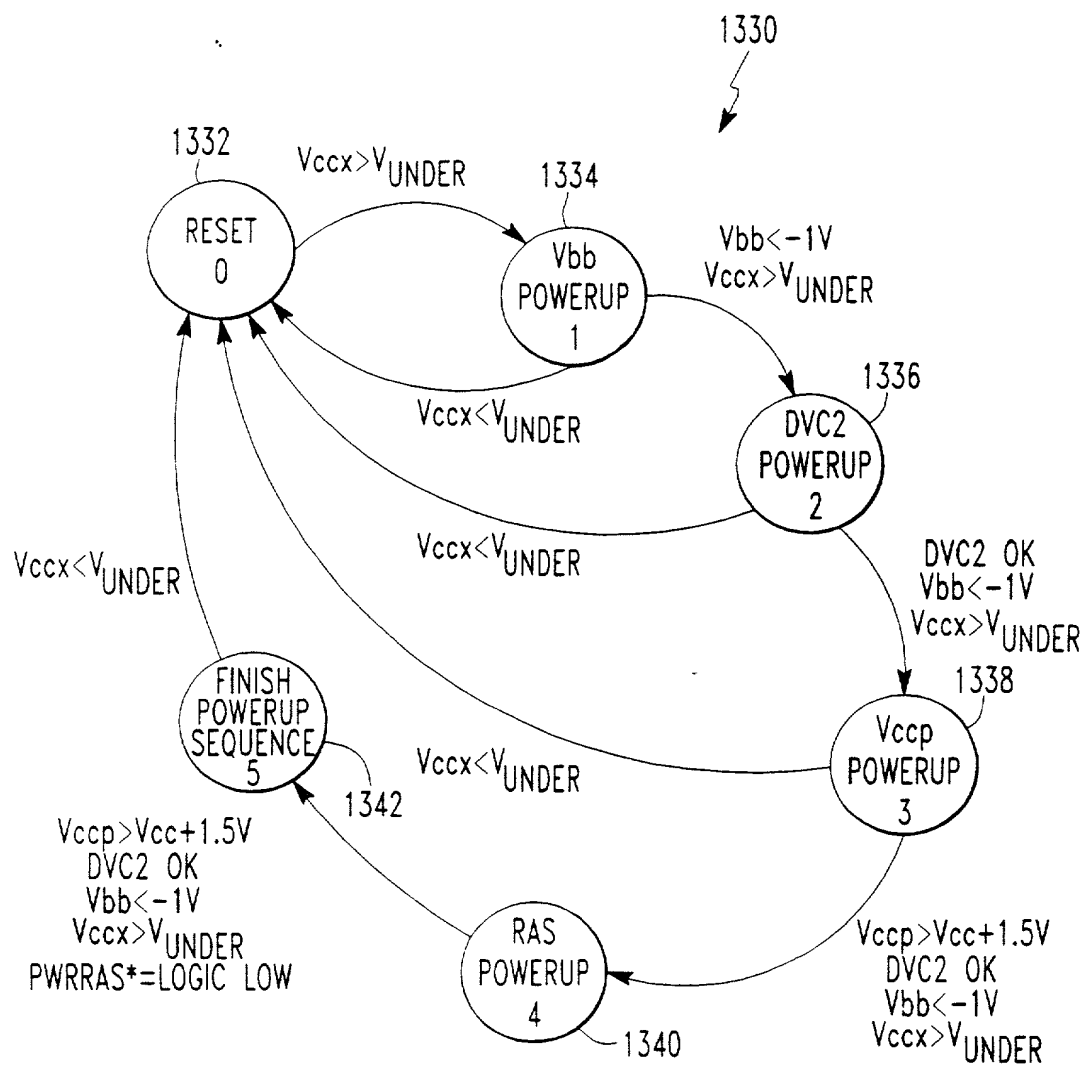


FIG. 99

1348

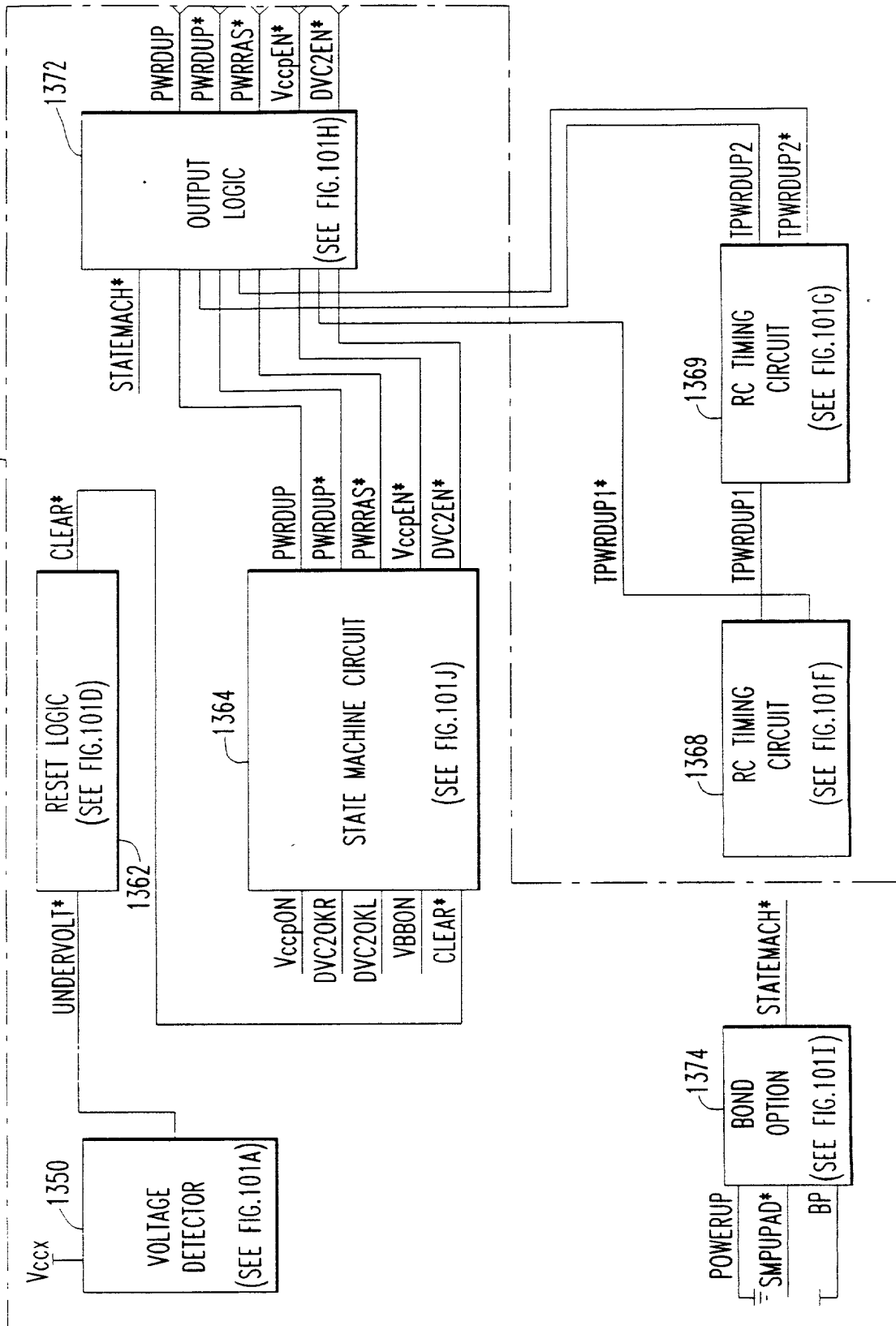
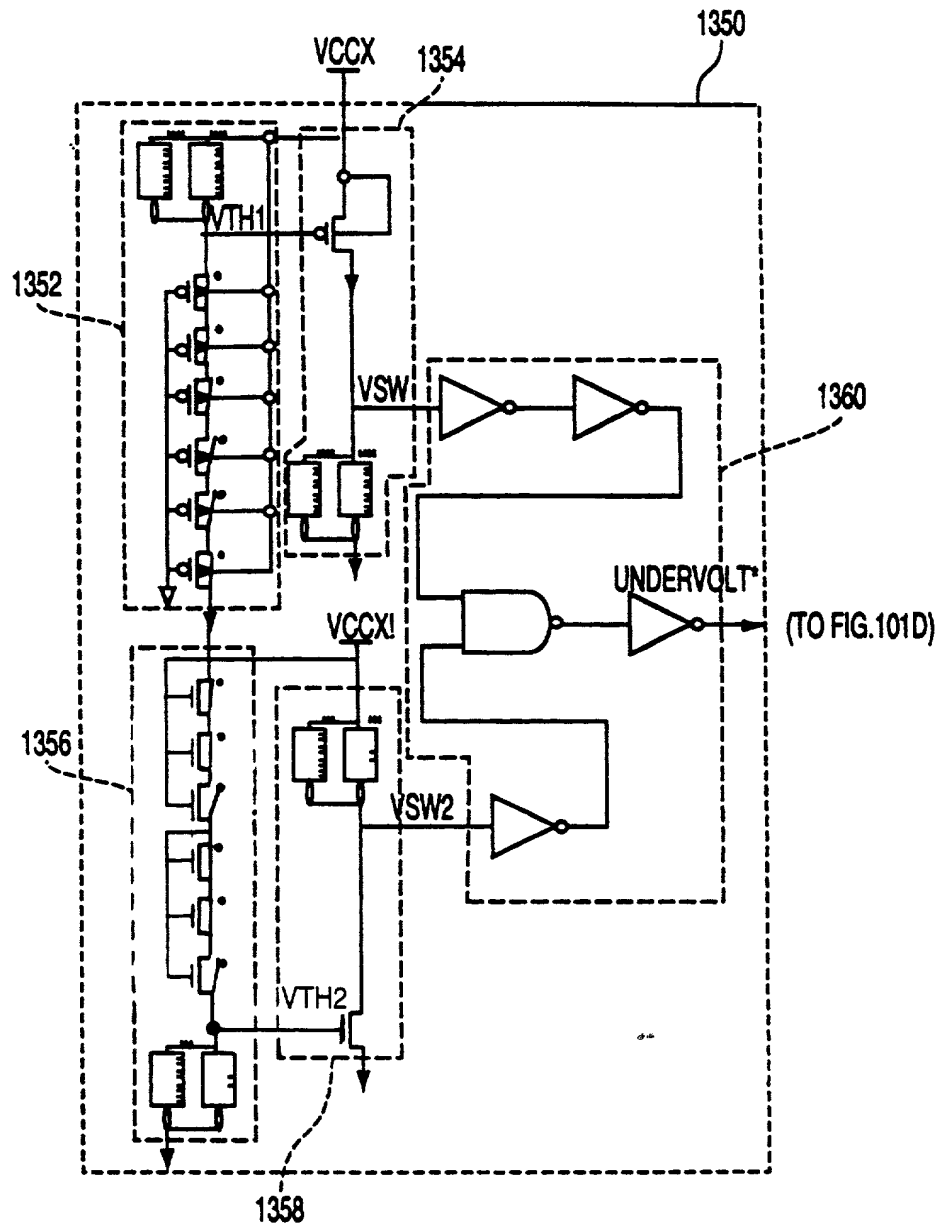
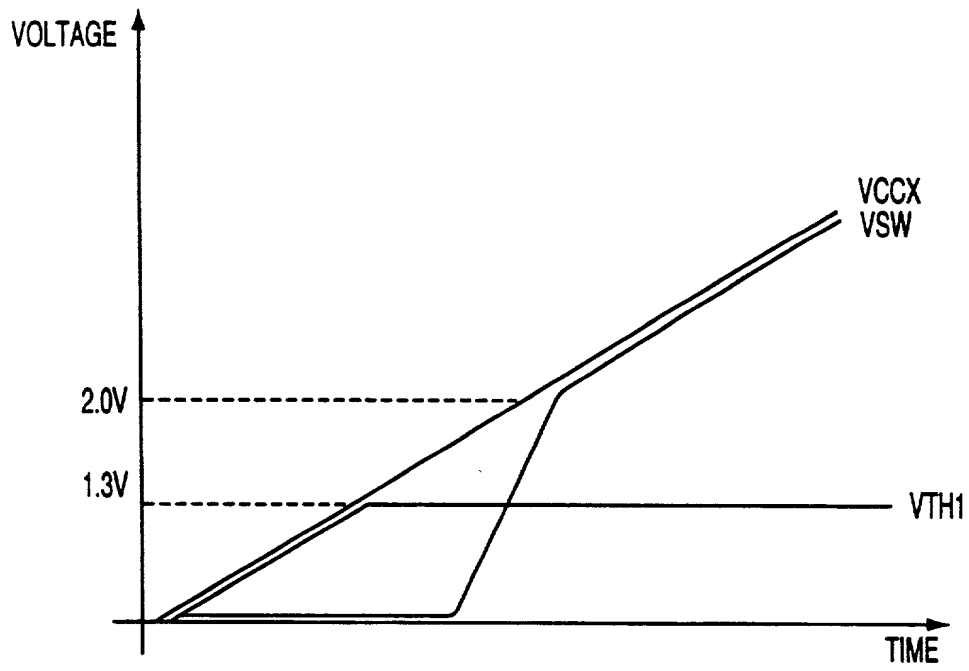


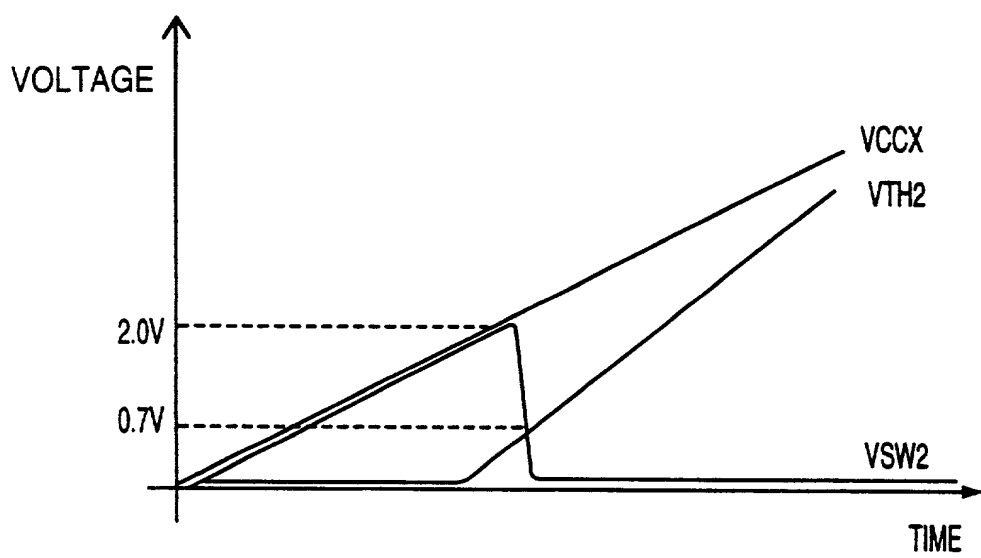
FIG. 100



**FIG. 101A**

**FIG. 101B**



**FIG. 101C**

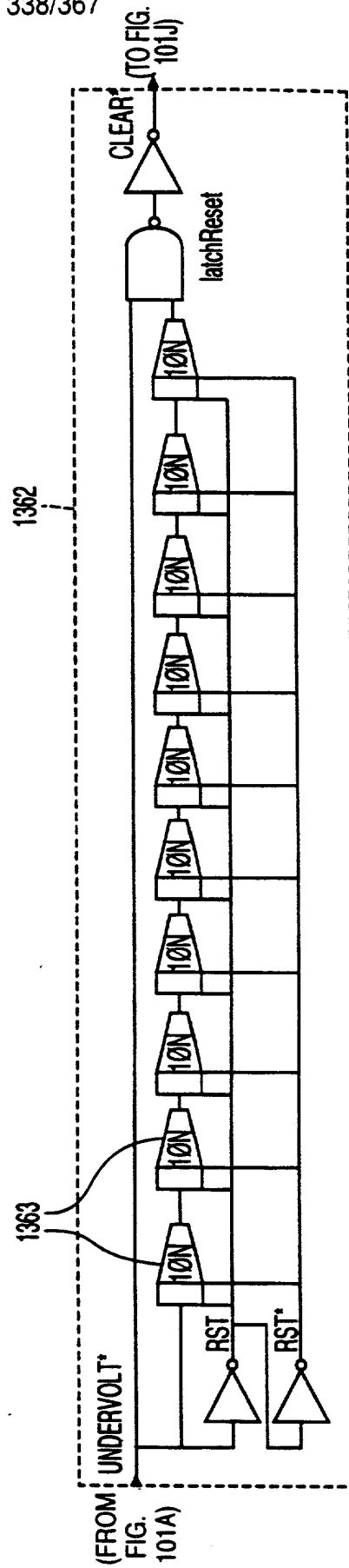


FIG. 101D

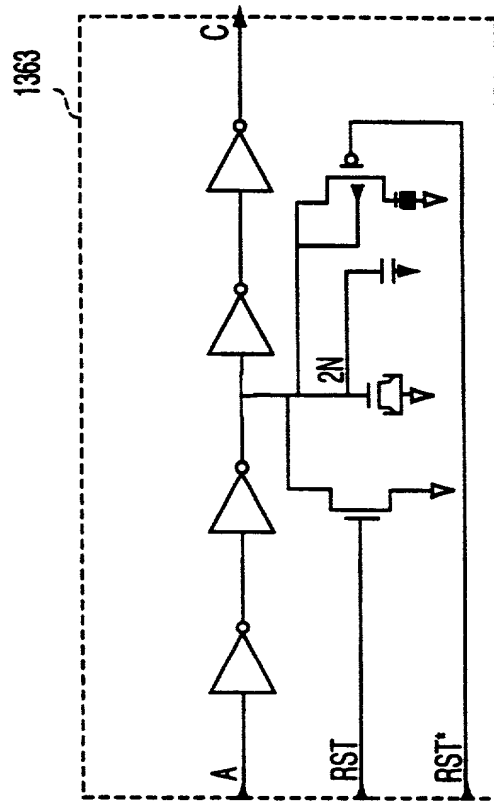


FIG. 101E

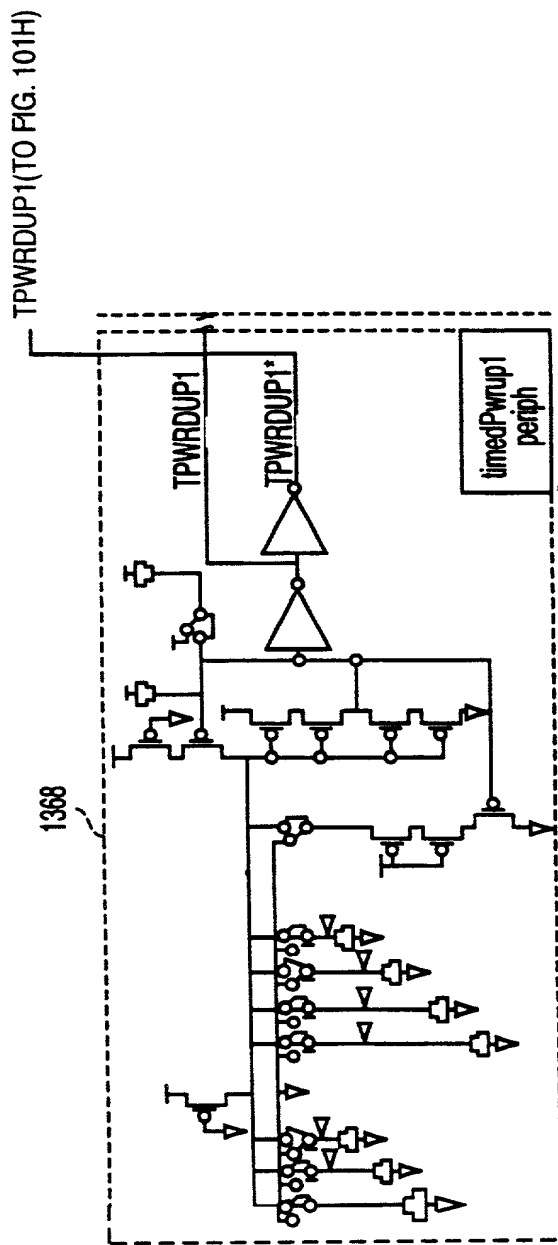


FIG. 101F

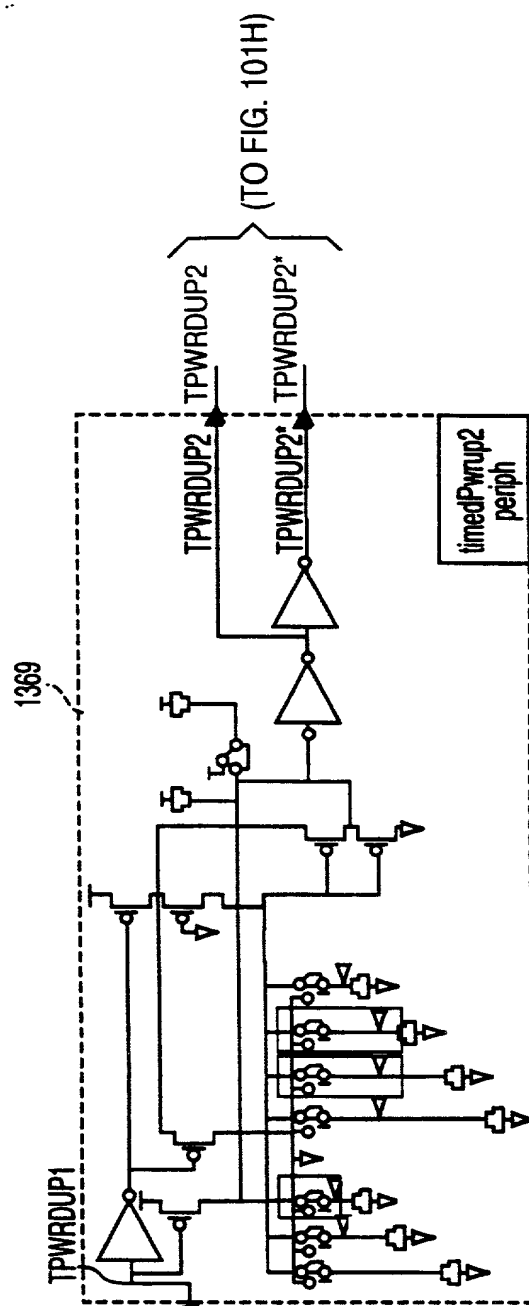


FIG. 101G

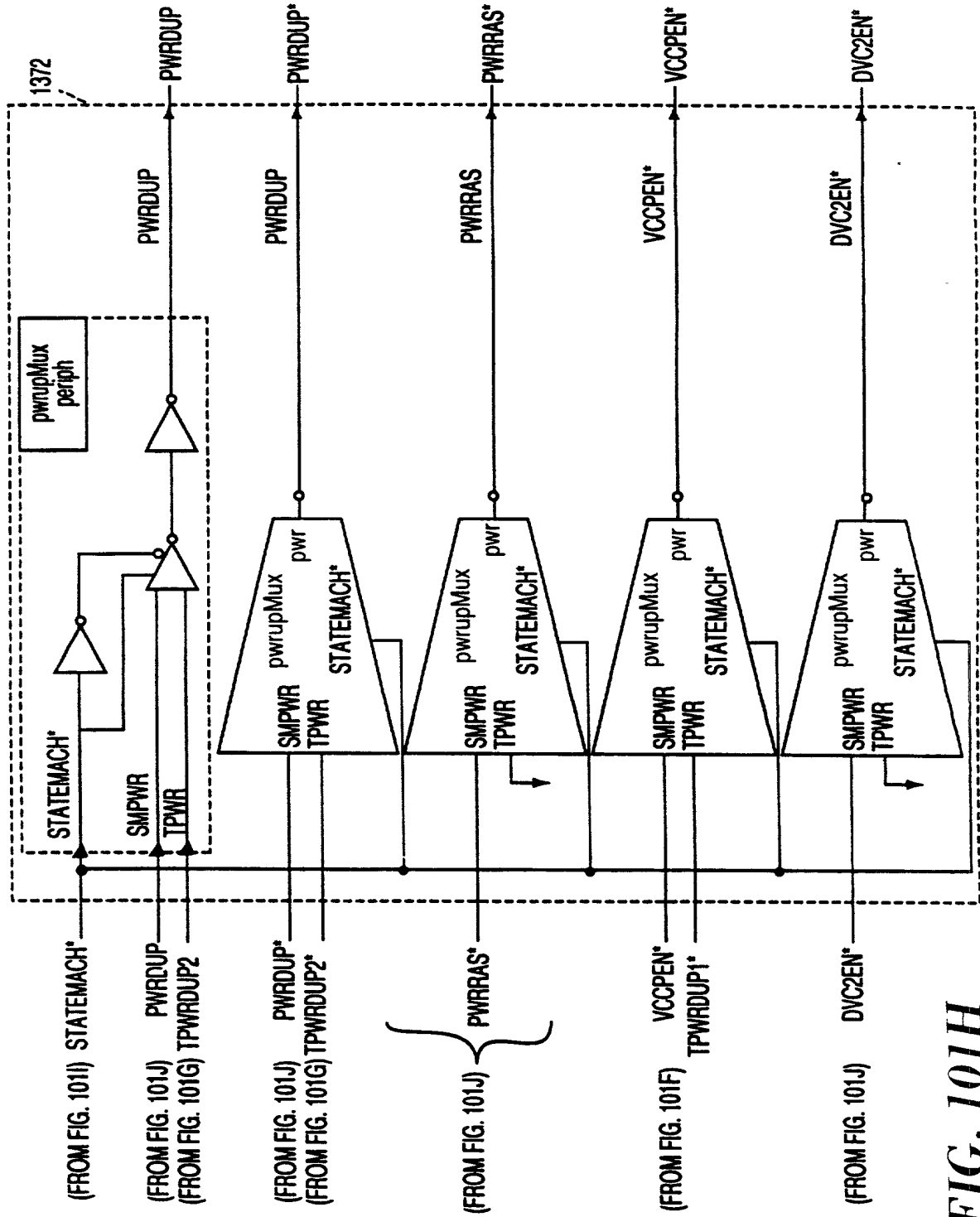


FIG. 101H

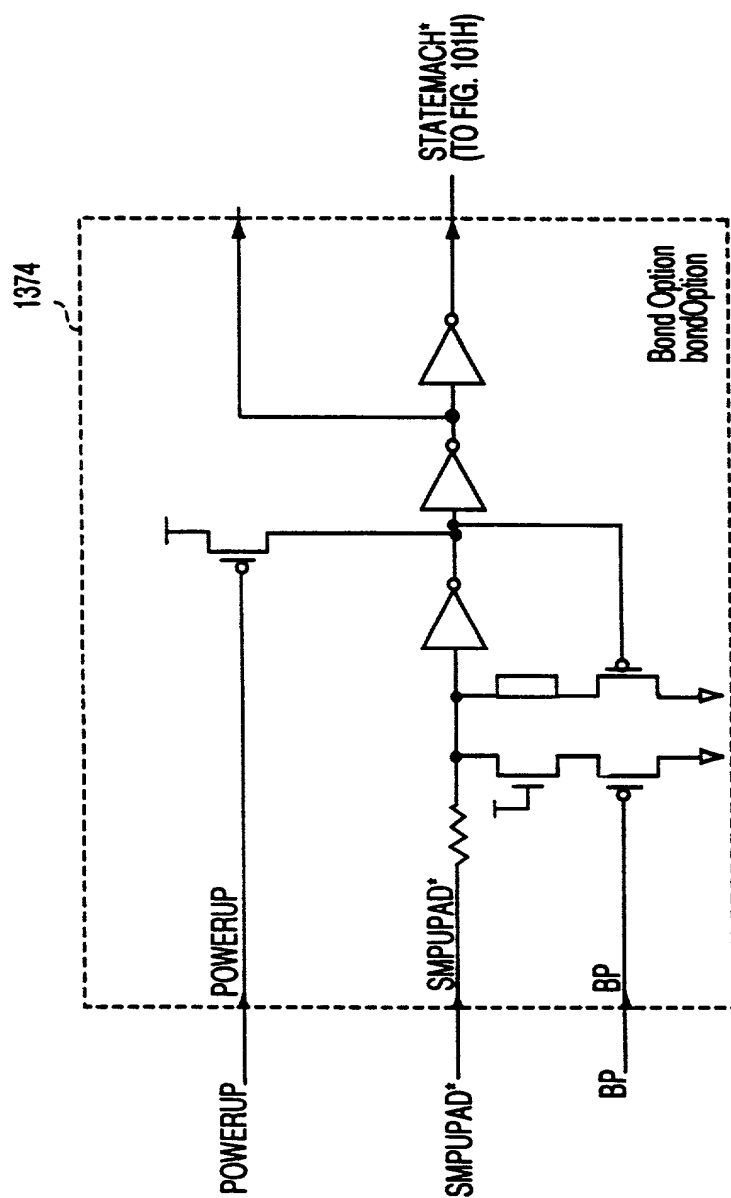


FIG. 101I

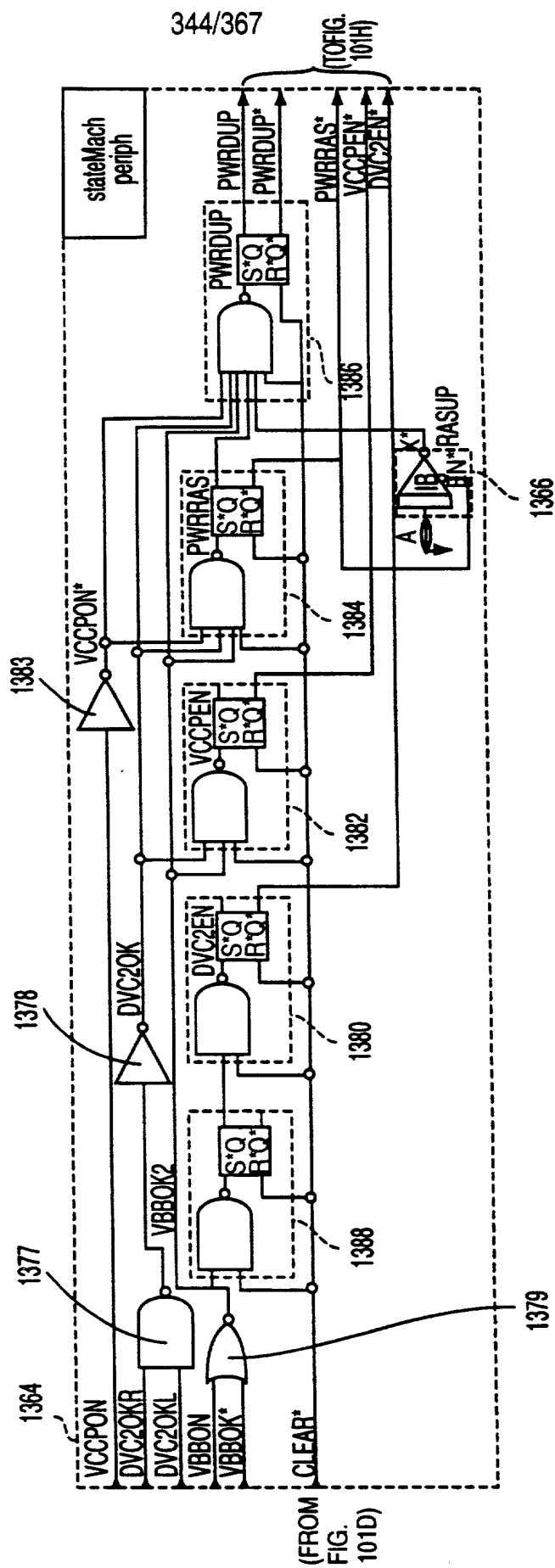
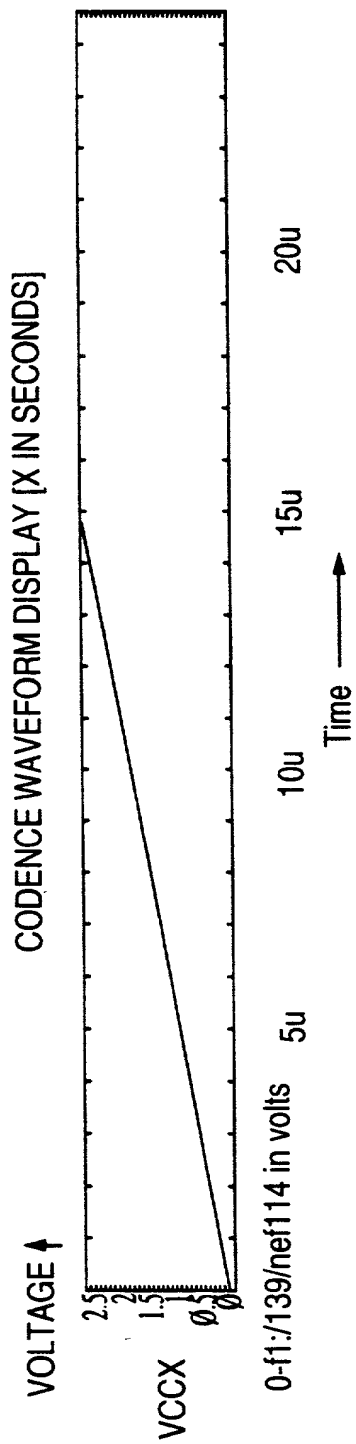


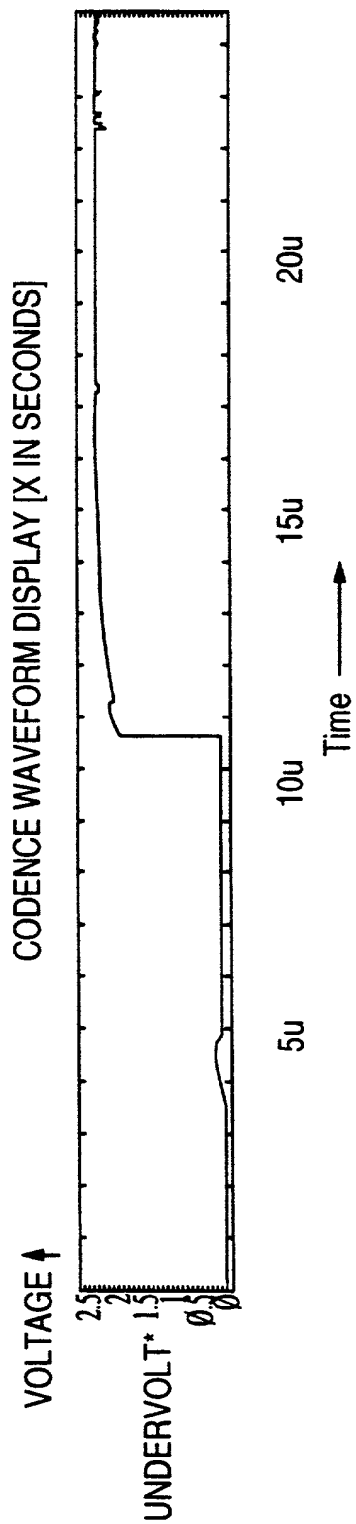
FIG. 101J





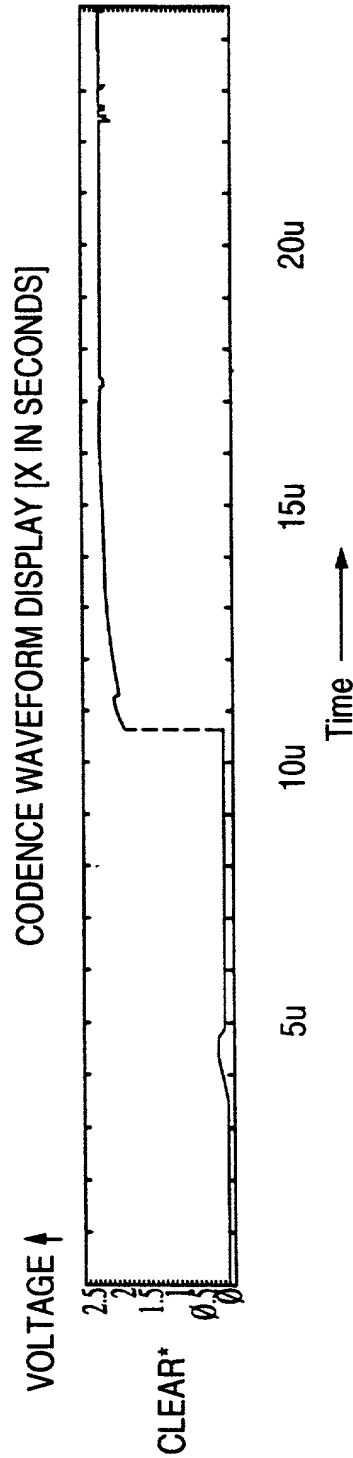
NOTE THAT PWRRAS\* WAITED FOR DVC20KR

FIG. 102A



NOTE THAT PWRRAS\* WAITED FOR DVC20KR

FIG. 102B



NOTE THAT PWRRAS\* WAITED FOR DVC20KR

FIG. 102C

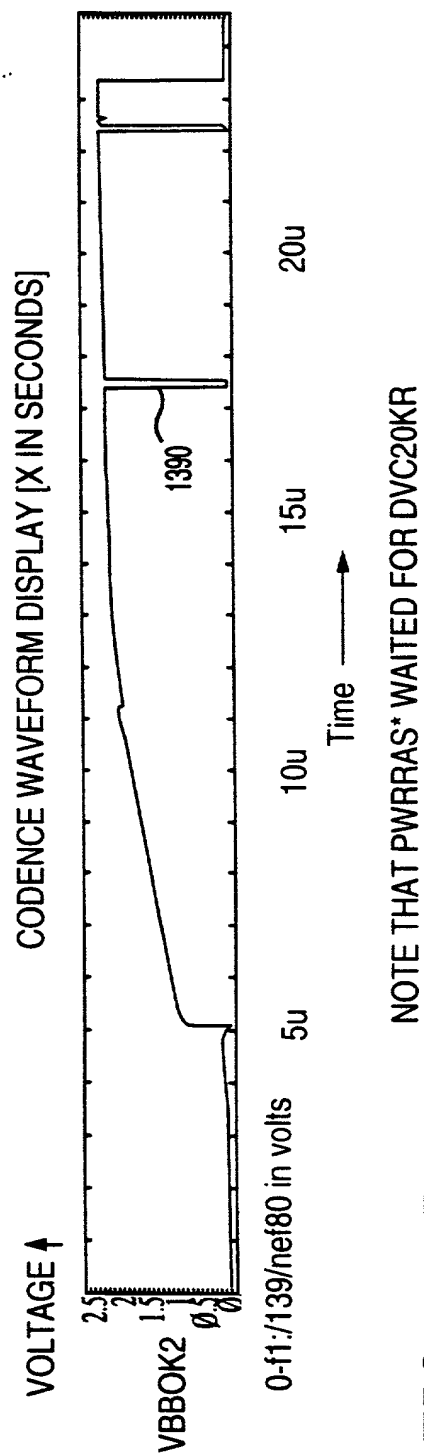
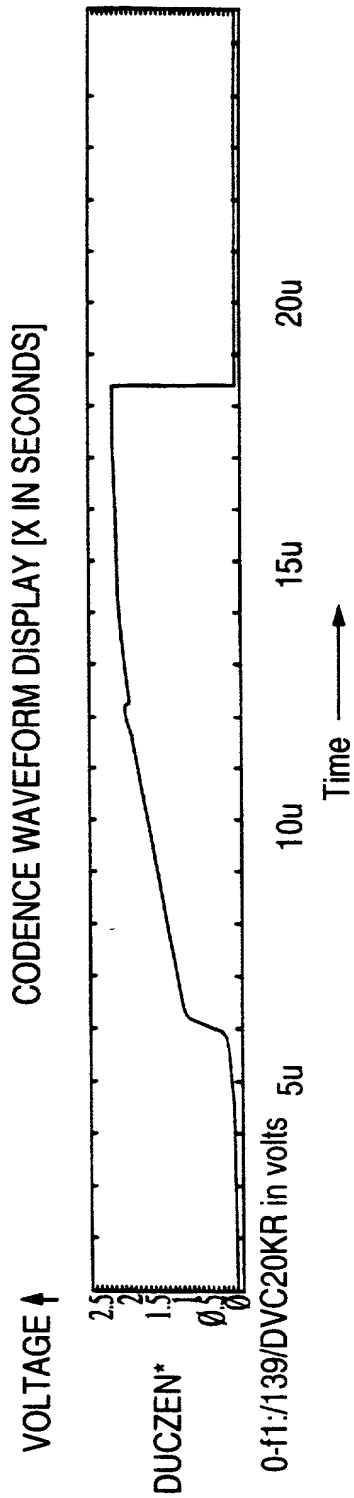
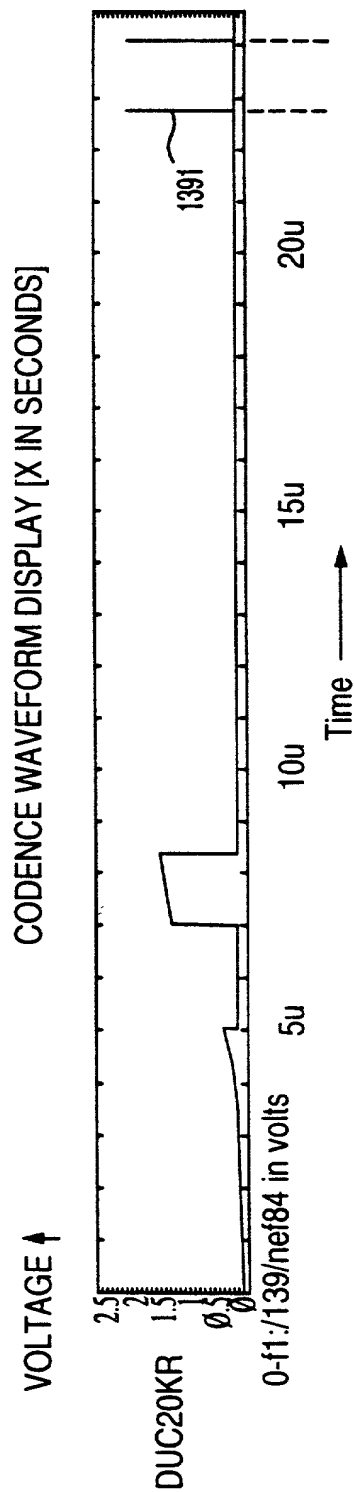


FIG. 102D



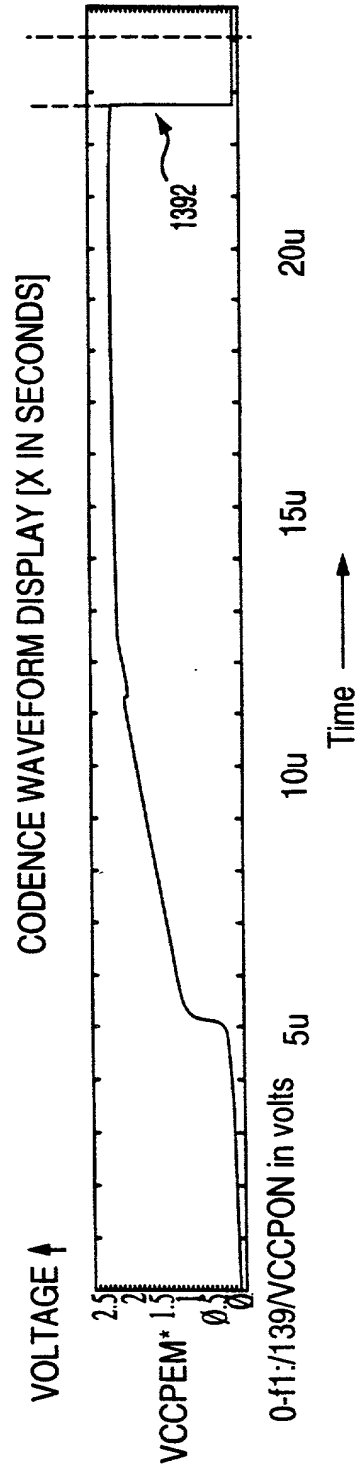
NOTE THAT PWRRAS\* WAITED FOR DVC20KR

FIG. 102E



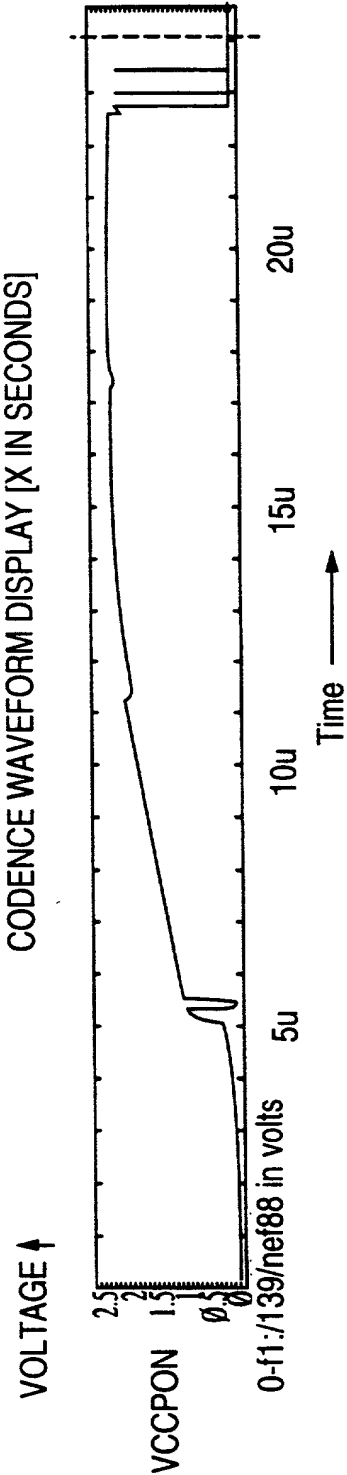
NOTE THAT PWRRAS\* WAITED FOR DVC20KR

FIG. 102F



NOTE THAT PWRRAS\* WAITED FOR DVC20KR

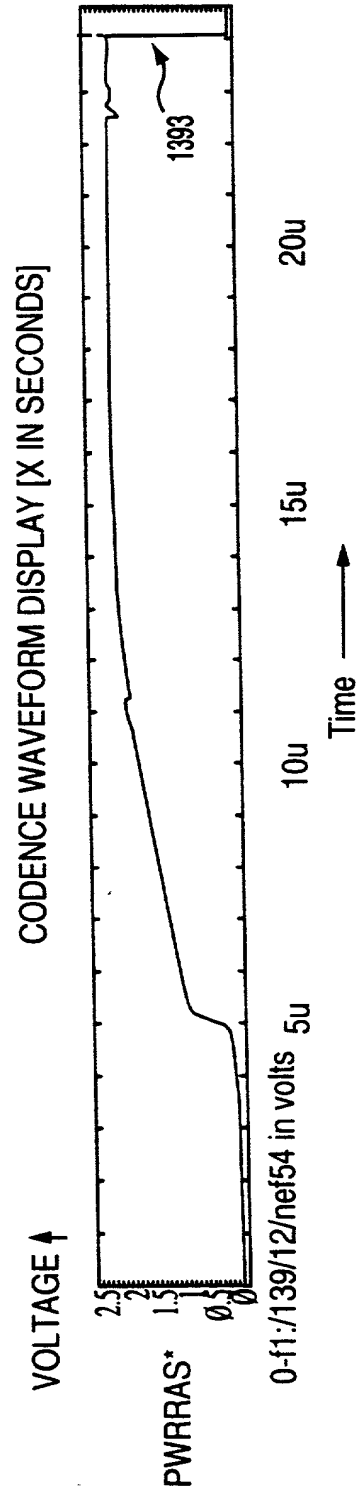
FIG. 102C



NOTE THAT PWRRAS\* WAITED FOR DVC20KR

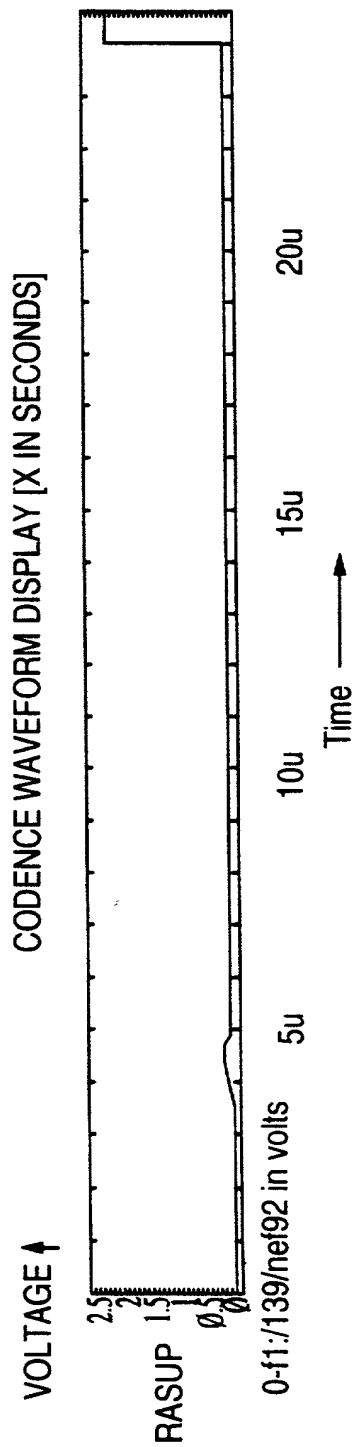
FIG. 102H





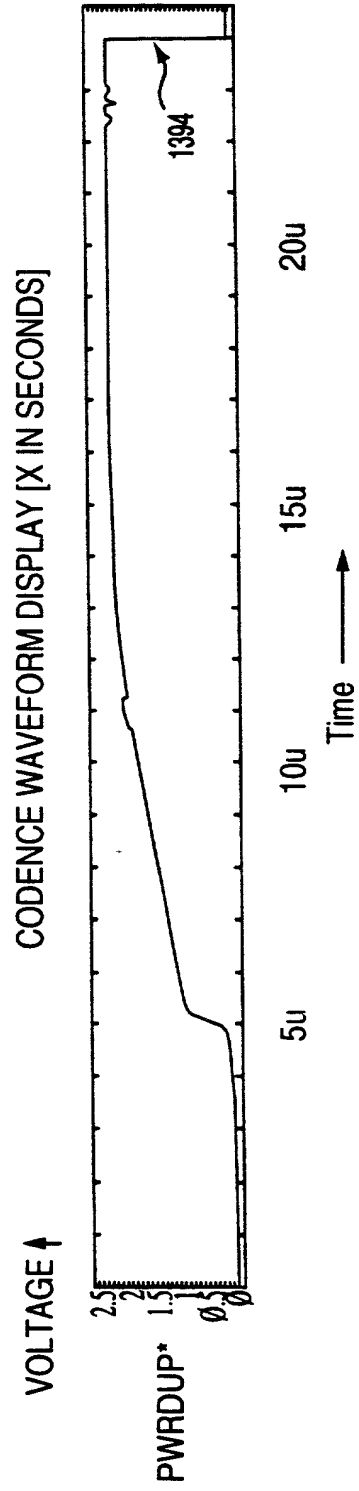
NOTE THAT PWRRAS\* WAITED FOR DVC20KR

**FIG. 1021**



NOTE THAT PWRRAS\* WAITED FOR DVC20KR

**FIG. 102J**



NOTE THAT PWRRAS\* WAITED FOR DVC20KR

FIG. 102K

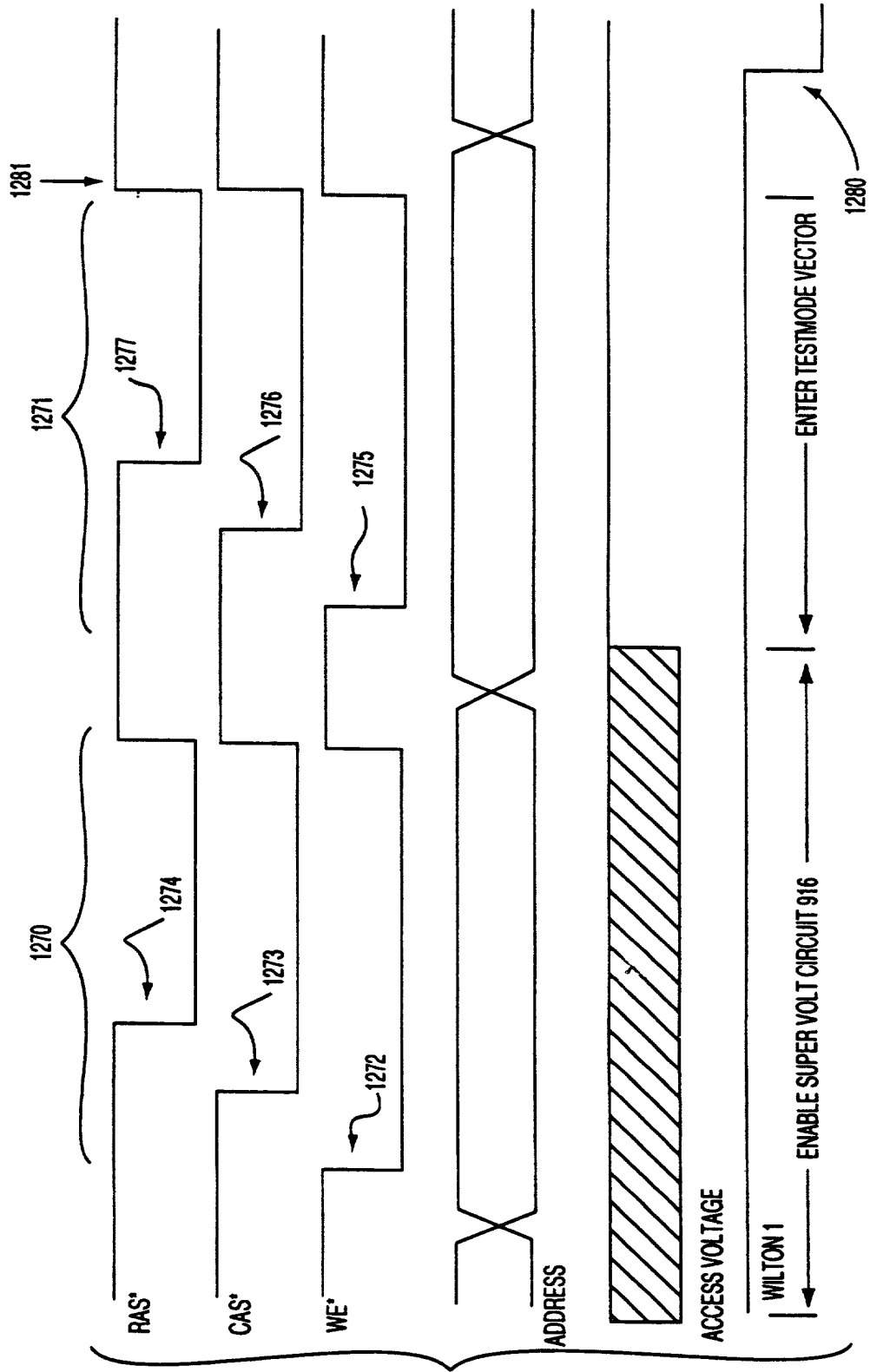
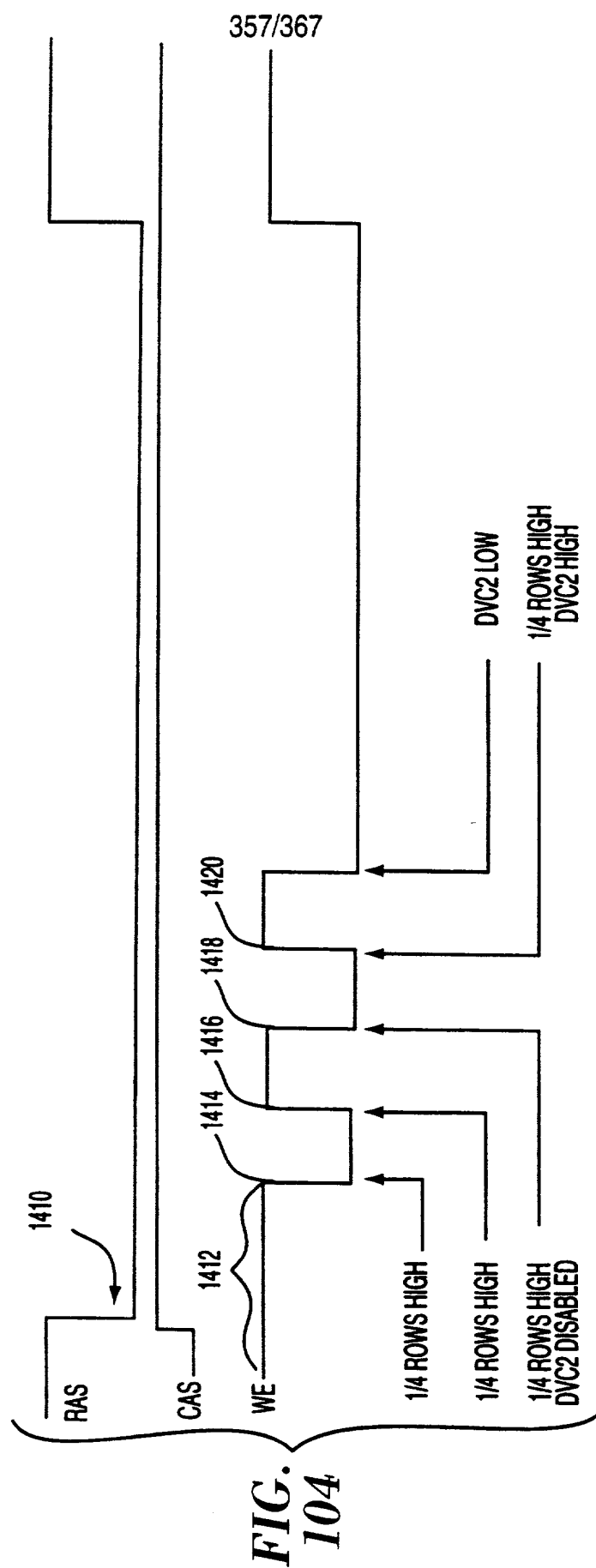
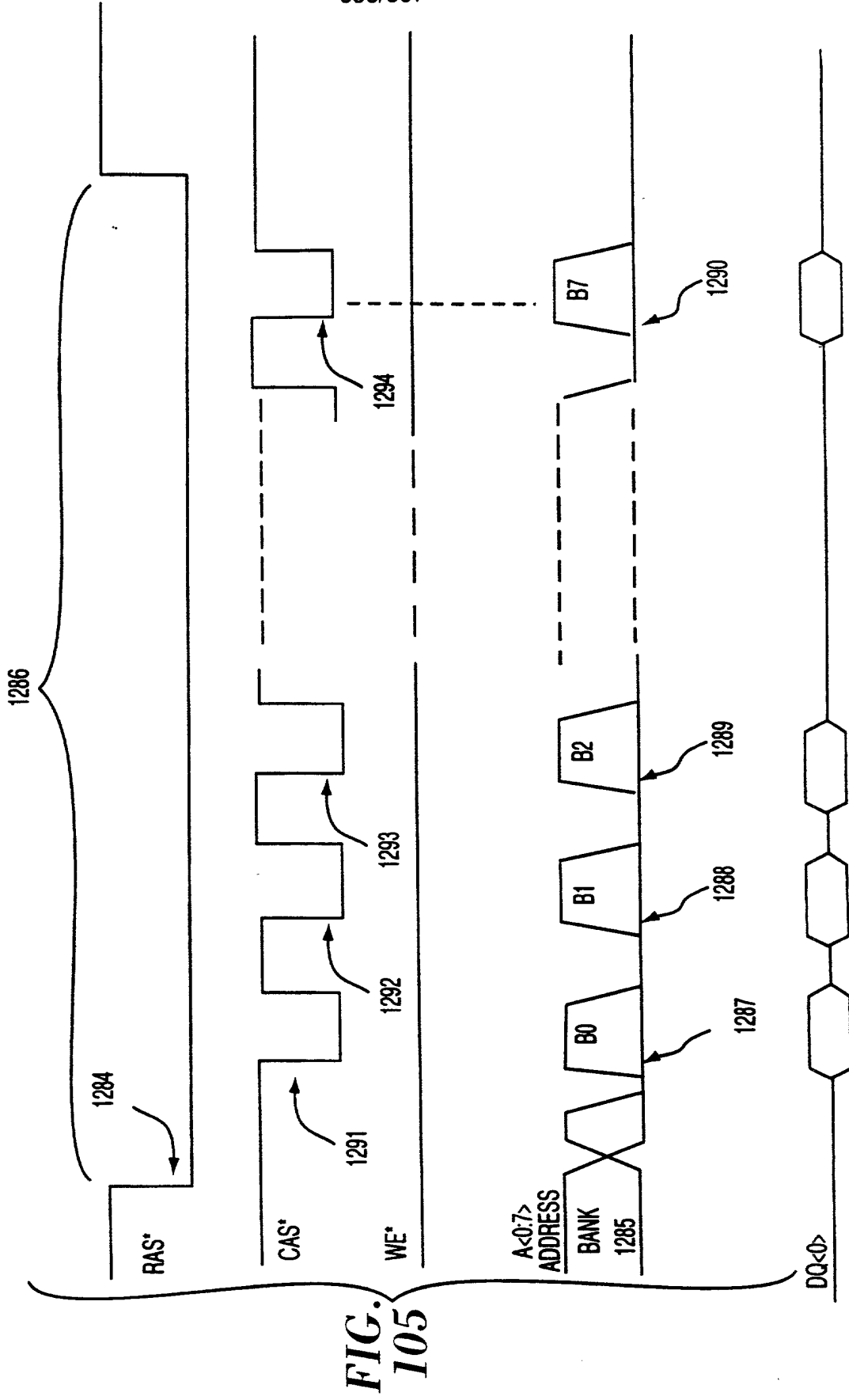


FIG.  
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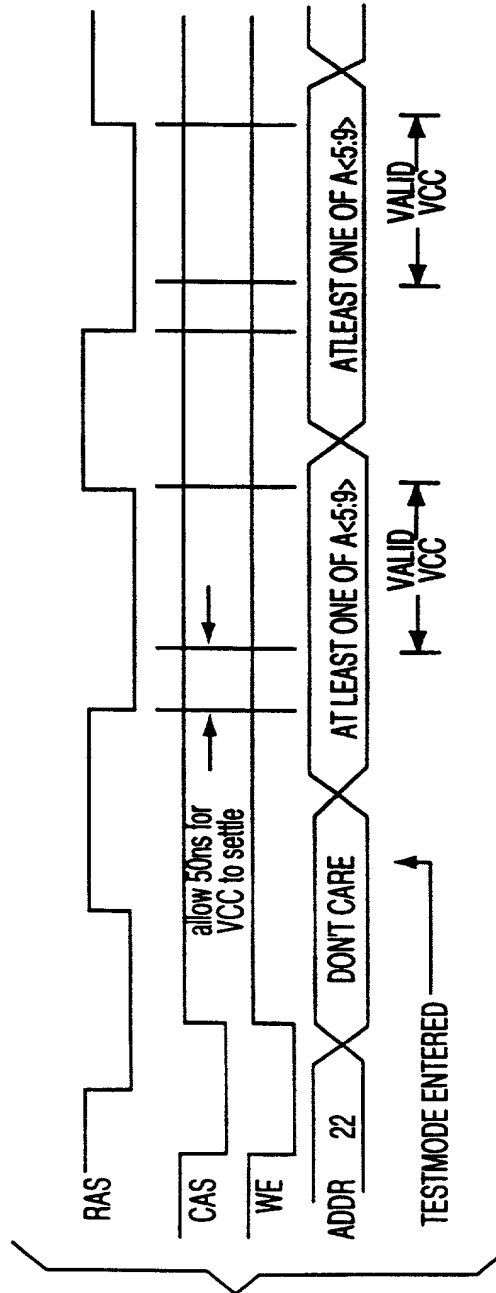


FIG. 106

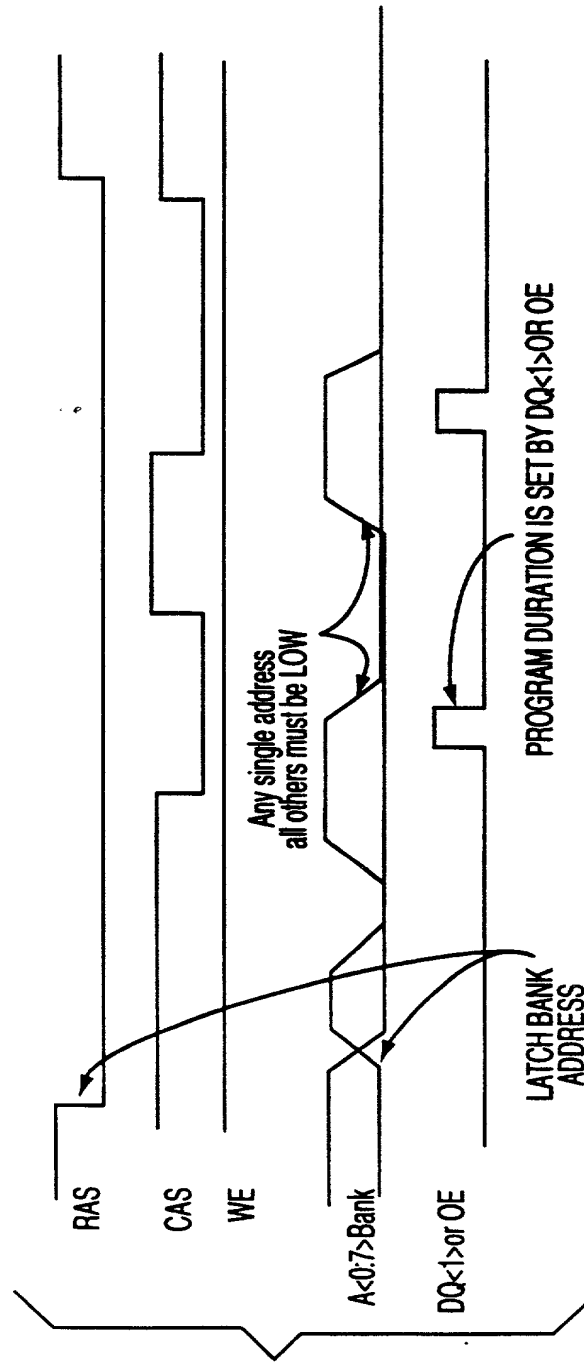


FIG. 107



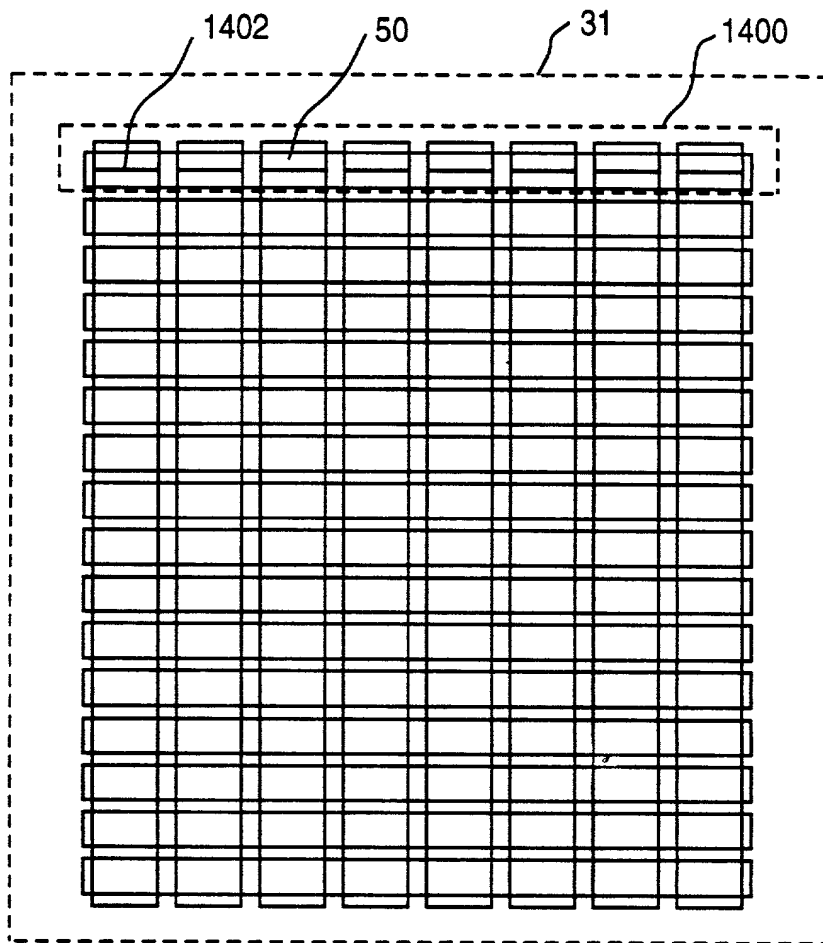
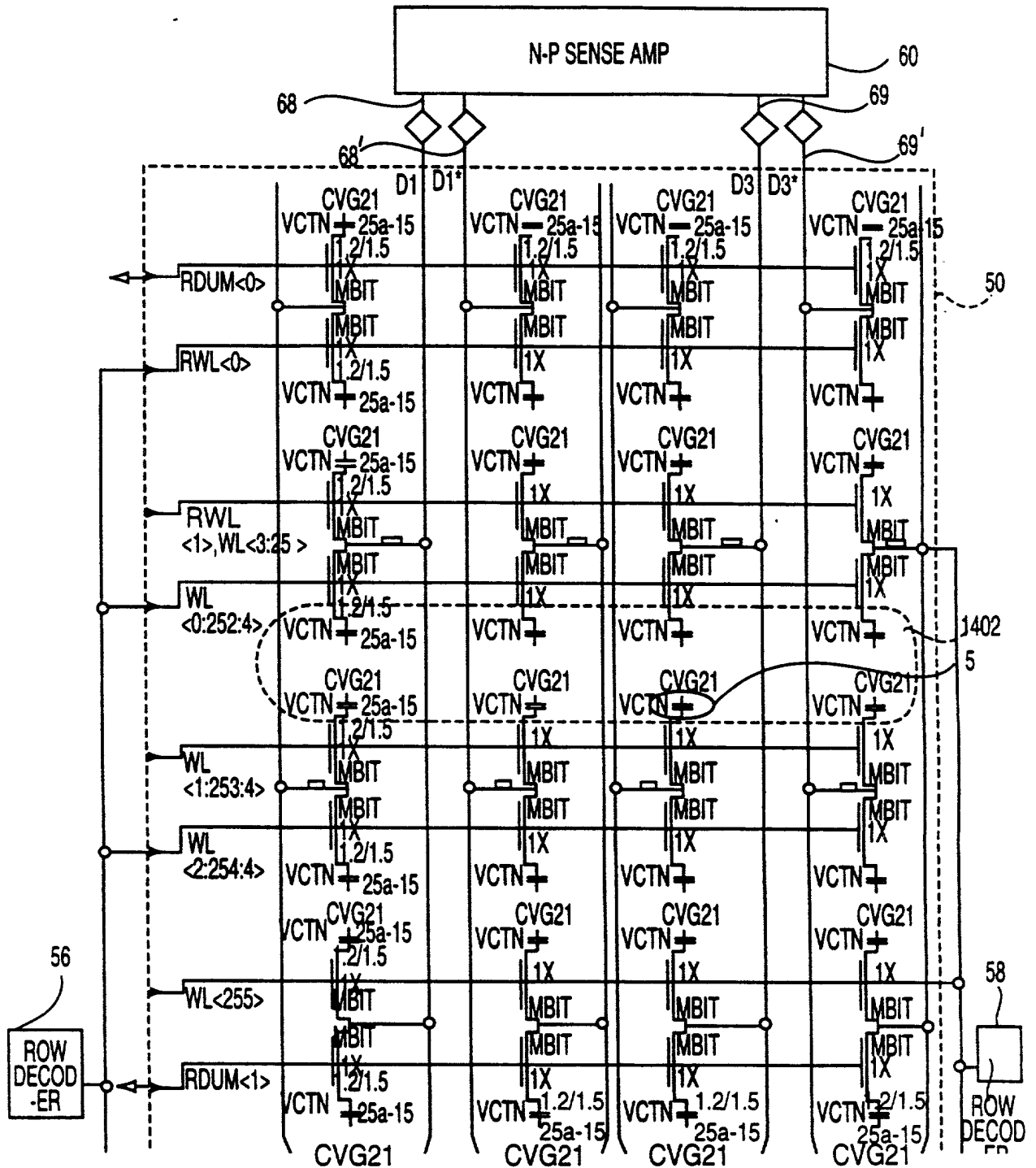
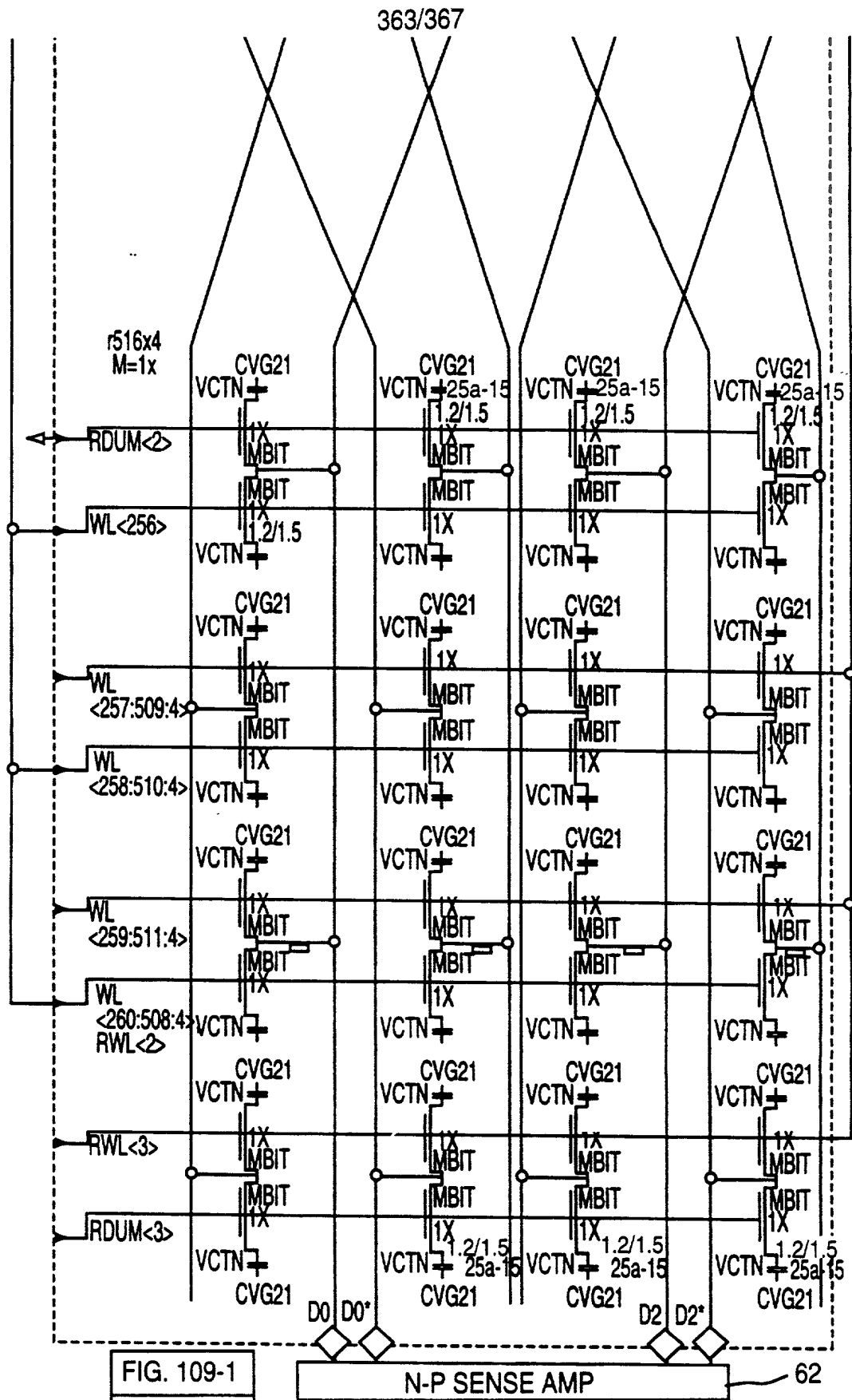
**FIG. 108**

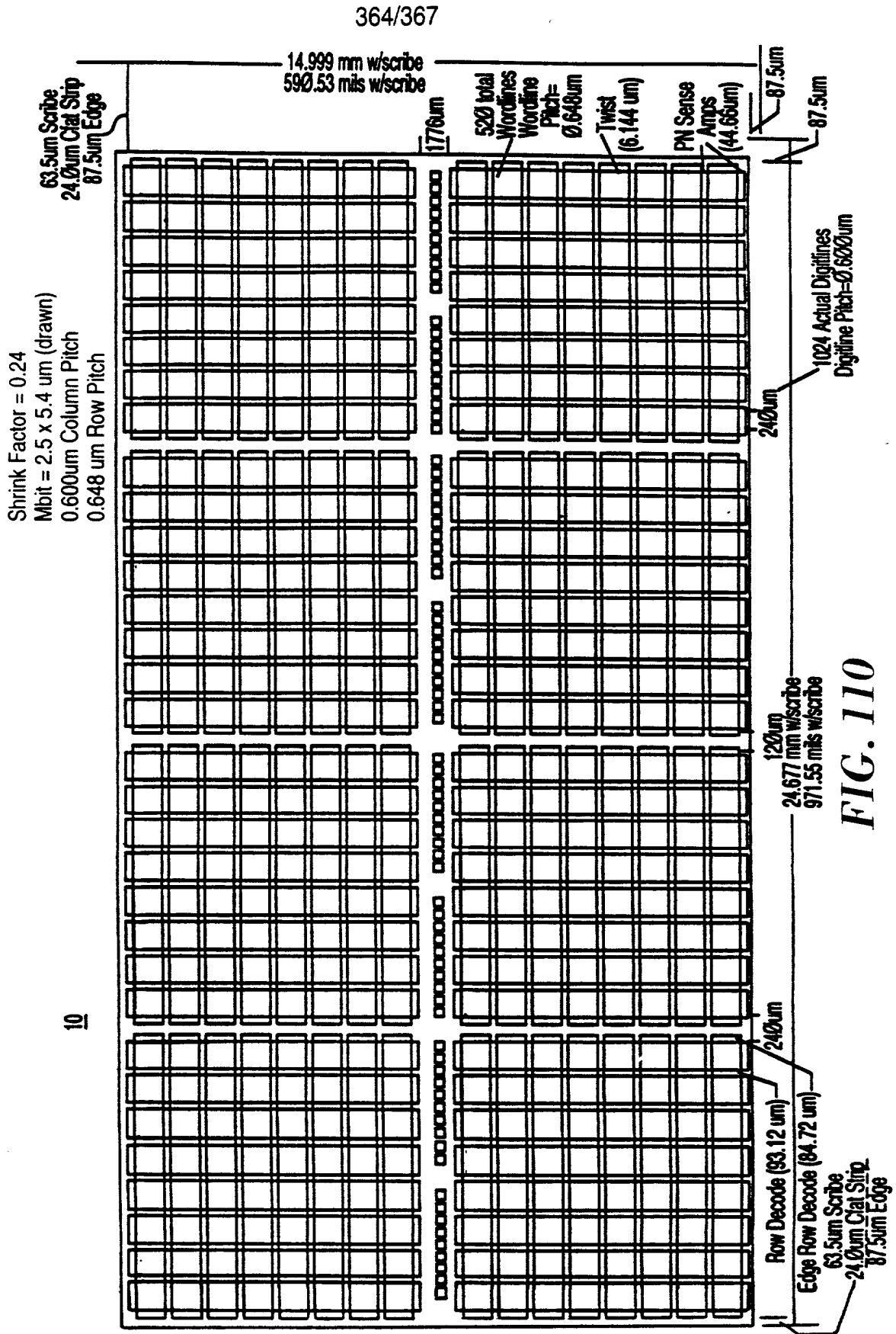
FIG. 109-1

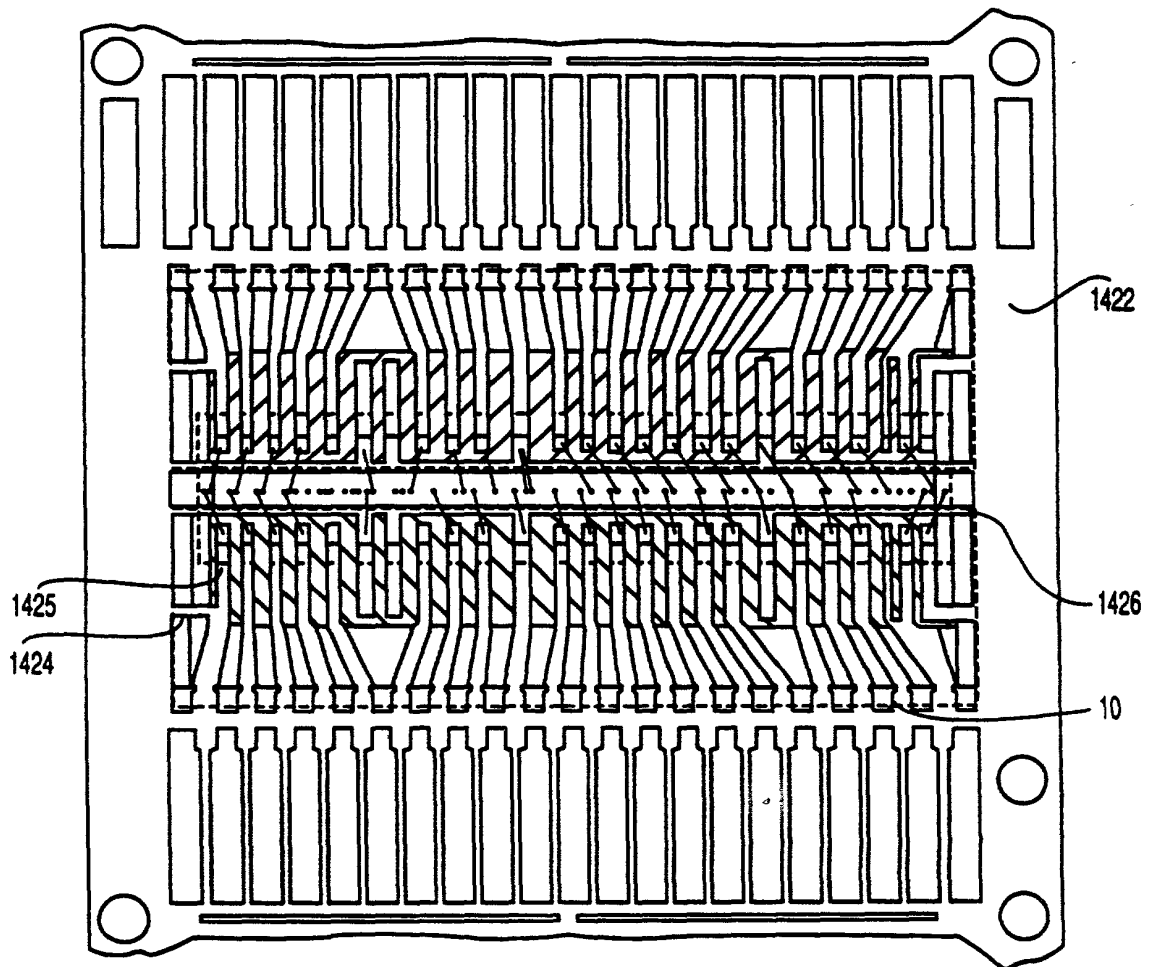
FIG. 109-1

FIG. 109-2

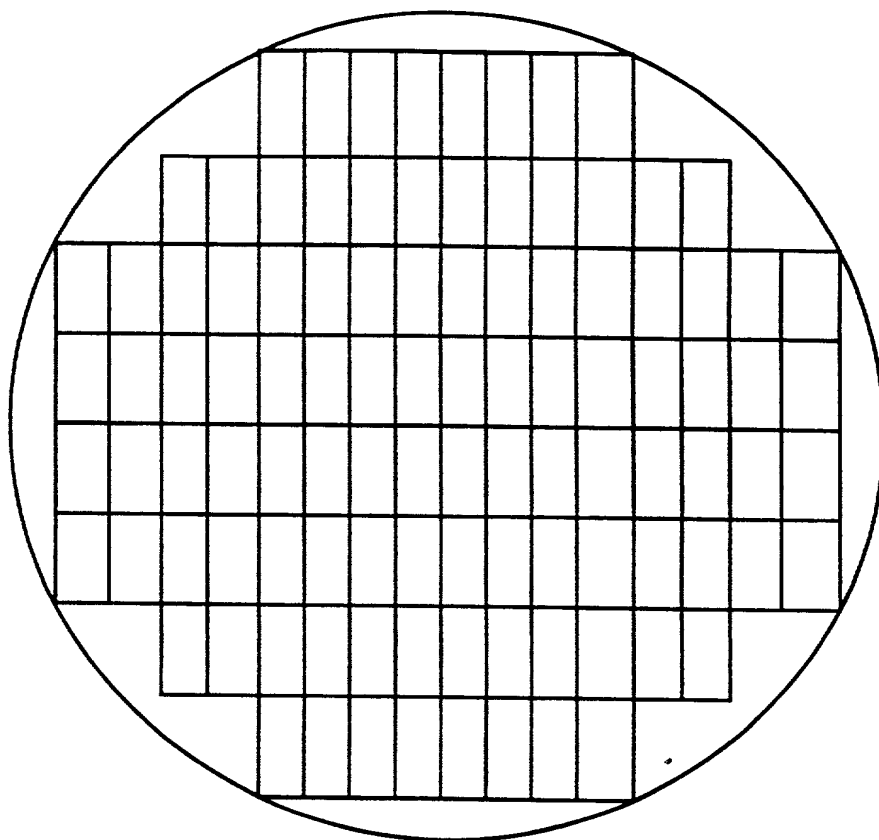




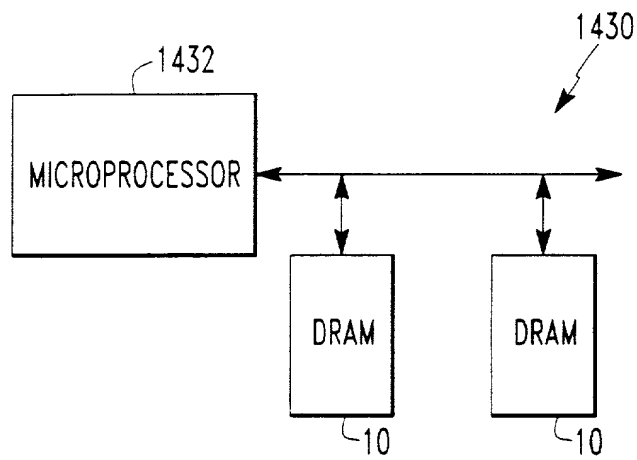




**FIG. 111**



**FIG. 112**

*FIG. 113*